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F198 F199 RDDLXB F7DS F7DA CHR\$(0) F18C F10F E 375'* F775 F778 DYS-DL JY? 7-7*9 F10C F10F F109 E29 #40 #450 F817 F810 CHR\$(1) SET F1FE F201 IMPUT 13-1*09 F818 F820 CHR\$(5) E.INK DA F203 F206 IMPUT F815 F824 F829 CHR\$(5) E.INK DA F208 F20E MESOUT F824 F829 CHR\$(6) E.INK DA F208 F20E MESOUT F824 F829 CHR\$(6) #460 H0VE F212 F215 H6X4 F829 F830 CHR\$(8) (== F2212 F215 H6X4 F829 F830 CHR\$(8) (== F2213 F226 H6XA LDW F843 F843 CHR\$(8) (== F227 F229 MEXA H18HT F845 F850 CHR\$(8) (== F227 F229 MEXA H18HT F845 F850 CHR\$(8) (== F227 F229 MEXA H18HT F856 F850 CHR\$(8); CR F245 F248 BELL F856 F850 CHR\$(2); CR F245 F246 BELL F856 F850 CHR\$(2); UP SCROL F247 F248 F24C J79'* IN F887 F839 CHR\$(2); UP SCROL F252 F255 IN H6X4 F892 F884 CLATY, CHR\$(C); CR F252 F256 IN H6X4 F892 F884 CLATY, CHR\$(C); CR F250 F256 IN H6X4 F892 F884 CLATY, CHR\$(C); IV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(2); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F26F B J79'* F860 F860 F860 CHR\$(1); INV F260 F360 F360 F860 F860 F860 F860 F860 F860 F860 F8						
F18C F18F E 370/* F18C F18F E 370/* F18C F18F E 370/* F18E F201 1NPUT 13-N*00 F818 F820 DHR*(S) BLINK DX F203 F206 NPUT 13-N*00 F818 F820 DHR*(S) BLINK DX F208 F206 MESDUT F824 F829 DHR*(S) BLINK DX F208 F208 MESDUT F824 F829 DHR*(A) MOMEL F212 F215 HEXA F828 F830 CHR*(A) MOMEL F214 F210 HEX2 F835 F830 CHR*(A) MOMEL F223 F226 MEXA LDW F843 F949 CHR*(B); ==) F227 F228 MEXA HIGHT F94F F854 CHR*(B); ==) F227 F228 MEXA HIGHT F94F F854 CHR*(B); ==) F227 F228 MEXA HIGHT F94F F856 CHR*(B); ==) F227 F228 MEXA HIGHT F94F F856 CHR*(B); CR CHR*(A); CR CHR*(A)		5+55				
FIDS FIOS FIOS FIOS FIST F810 CHR\$(1) SET FIFE F201 INPUT ID=10*07 F818 F820 CHR\$(5) BLINK DAT F203 F206 INPUT F208 F205 MSSOUT F815 F820 CHR\$(5) BLINK DAT F208 F205 MSSOUT F815 F820 CHR\$(5) BLINK DAT F208 F205 MSSOUT F816 F820 CHR\$(6) [C= F210 HEX2 F835 F830 CHR\$(8); (== F221 F215 HEXA F828 F830 CHR\$(8); (== F223 F226 HEXA LDW F843 F943 CHR\$(8); (== F224 F227 F228 HEXA MIDHT F845 F835 CHR\$(8); (== F227 F228 HEXA MIDHT F845 F855 F850 CHR\$(8); (== F245 F246 T246 T75* NO BELL F855 F856 CHR\$(2); CR F245 F246 BELL F855 F856 CHR\$(2); CR F245 F246 T746 T75* IN F887 F831 X 75 1 F75 CLR F252 F255 IN HEXA F822 F864 CLR*(C); CR F252 F255 IN HEXA F822 F864 CLR*(C); CR F252 F255 IN HEXA F822 F864 CLR*(C); CR F255 F256 IN HEXA F822 F864 CLR*(C); CR F256 F256 IN HEXA F822 F864 CLR*(C); CR F257 F350 F372 CR, "" F249"/97 F862 F865 F867 CHR\$(2); DATA(C); CR F368 F372 CR, "" F249"/97 F957 F958 H876A F373 F375 *376 F870 F957 F958 H876A F380 F372 CR, "" F249"/97 F957 F958 H876A F380 F372 R304 F375 F976 F989 DISPLAY F380 F372 R304 F375 F989 F989 DISPLAY F380 F372 F376 F870 F984 F385 * BREAK POINT F380 F376 F376 F870 F984 F385 F870 F986 F386 F387 F387 F387 F388 F388 F388 F388 F388						
### F201 INPUT ID=N*07 F818 F820 CHR\$(5) BLINK DN P203 F206 INPUT F815 F824 CHR\$(6) BLINK DT F820 F208 F206 M850UT F824 F829 CHR\$(6) BLINK DT F820 F208 F206 M850UT F824 F829 CHR\$(4) HDMS! F821 F212 F215 HEXA F822 F835 CHR\$(4); (== F212 F215 HEXA LDA F828 F830 CHR\$(477); 155 CHR\$(6); (== F212 F225 HEXA LDA F843 F843 CHR\$(8); (== F227 F226 HEXA LDA F843 F843 CHR\$(8); (== F227 F228 HEXA HIDHT F845 F854 CHR\$(3); (== F227 F228 HEXA HIDHT F845 F855 CHR\$(3); (== F227 F228 HEXA HIDHT F845 F856 CHR\$(3); (== F237 F238 CUTIX F858 F850 CHR\$(3); (== F237 F238 CUTIX F858 F850 CHR\$(3); (== F241 F244 **?* AND BELL F855 F856 CHR\$(3); (== F245 F246 CHR\$(2); (HP SCROL) CHR\$(2)						
F203 F206 INPLT F81F F924 CHR*(6) BLINK CFT F208 F206 MESOUT F824 F829 CHR*(4) MOVE F212 F215 H5X4 F828 F830 CHR*(4) MOVE F212 F215 H5X4 F828 F830 CHR*(4) MOVE F213 F216 H5X2 F835 F83A CHR*(6); (== F223 F226 H5X1 LDW F843 F849 CHR*(7); 25 CHR*(8); (== F227 F228 H5X1 H18HT F94F F854 CHR*(8); (== F227 F228 H5X1 H18HT F94F F855 CHR*(8); (== F227 F228 H5X1 H18HT F94F F855 CHR*(8); (== F227 F228 H5X1 H18HT F94F F855 CHR*(8); (== F241 F244 **2" AND BELL F855 F856 CHR*(8); (== F242 F245 F246 T77)* IN F887 F899 IX M7 I F*97 CLR F245 F246 T77)* IN F887 F899 IX M7 I F*97 CLR F252 F255 IN H5X2 F892 F894 CHR*(2); UP SCROL F252 F255 IN H5X2 F892 F894 CHR*(2); UP SCROL F252 F255 IN H5X2 F892 F894 CHR*(2); UN SCROL F255 F256 IN H5X2 F892 F894 CHR*(2); UN SCROL F256 F257 I BYTE NOWD F802 F803 CHR*(1); DDAN SCROL F257 F357 F350 I J J J J J J J J J J J J J J J J J J						
F208 F206 MSSOUT F824 F829 CHR\$(4) HOME F212 F215 H544 F828 F830 CHR\$(8); (== F212 F215 H544 F828 F830 CHR\$(8); (== F213 F226 H540 LDW F843 F846 CHR\$(8); (== F227 F228 H540 LDW F843 F846 CHR\$(8); (== F227 F228 H540 LDW F843 F846 CHR\$(8); (== F227 F228 H540 H10HT F94F F854 CHR\$(8); (== F227 F228 H540 H10HT F94F F854 CHR\$(8); (== F227 F228 GUTIX F858 F860 CHR\$(A); F241 F244 "?" AND BELL F856 F868 CHR\$(A); F242 F245 F246 B544 F864 F865 F866 CHR\$(A); F245 F246 B544 F864 F865 F866 CHR\$(A); F246 F247 F240 H10HT F866 F867 CHR\$(C); UP SCROL F249 F240 T77)* IN F887 F899 IX \$7 1 F*30 CLR F249 F240 T77)* IN F887 F899 IX \$7 1 F*30 CLR F252 F255 IN H544 F892 F864 CLRTV, CHR\$(C); F255 F256 IN H542 F882 F864 CLRTV, CHR\$(C); F256 F256 IN H542 F882 F860 CHR\$(3); DOWN SCROL F257 F258 IN H544 F892 F864 CLRTV, CHR\$(C); F258 F259 IN H544 F892 F864 CLRTV, CHR\$(C); F256 F256 IN H542 F865 F867 CHR\$(2); INV F256 F257 IN H544 F892 F864 CLRTV, CHR\$(C); F257 F350 JJ 707/* F866 F867 CHR\$(2); INV F257 F350 F372 CR, "=" 9249099 F957 F958 *8REAK F367 F360 JJ 707/* F960 F971 *851 INV F368 F360 F371 CR, "=" 9249099 F957 F958 *8REAK F368 F369 F960 F971 *851 INV F369 F360 F371 T0 F977 F999 *8REAK F368 F369 F960 F971 *851 INV F369 F360 F372 R 707/* F969 F960 INV F360 F370 F370 F970 F980 F980 INV F360 F371 CR, "=" 924909 F990 F990 INV F360 F371 CR, "=" 924909 F990 F990 INV F360 F371 T0 F977 F999 *8REAK F360 F371 T0 F977 F999 F990 INV F360 F371 T0 F990 F990 INV F360 F371 T0 F990 F990 INV F360 F371 CR, "=" 92490 F990 F990 INV F360 F371 T0 F990 F990 INV F360 F371 CR, "=" 92490 F990 F9						
F212 F215 H2X4 F828 F830 CHR*(\$77);85 F218 F210 HEX2 F835 F835 CHR*(\$);(===================================						
F210 F210 HEX2 F835 F83A CHR\$(8); (== F223 F226 HEXA LDW F843 F945 CHR\$(9); ==) F227 F228 HEXA HISHT F84F F856 CHR\$(9); => F227 F228 HEXA HISHT F85F F850 CHR\$(0); => F237 F23A CUTIX F85F F850 CHR\$(0); CR F241 F244 "7" RND BELL F855 F85B CHR\$(0); CR F245 F248 BELL F86D F872 CHR\$(0); CR F249 F240 JY/* IN F887 F893 IX #7 1 #/90 CLR F252 F255 IN HEXA F892 F894 CHR\$(3); DOWN SCROL F252 F255 IN HEXA F892 F894 CHR\$(3); DOWN SCROL F255 F256 IN HEXA F892 F894 CHR\$(3); DOWN SCROL F25C F25F I BYTE ADD F802 F803 CHR\$(3); DOWN SCROL F25C F25F I BYTE ADD F802 F803 CHR\$(5); INV F25C F25F I BYTE ADD F802 F803 CHR\$(5); INV F25C F25F B JYD/* F802 F805 CHR\$(5); INV F25C F25F B JYD/* F806 F807 CHR\$(5); INV F25C F25F B JYD/* F906 F807 CHR\$(5); INV F25C F25F B208 M JYD/* F957 F958 W8EAK F25C F25F B208 M JYD/* F957 F958 F808 DISPLAY F25C F25C F25T JYD/* F994 F995 F808 DISPLAY F25C F25C F25T JYD/* F995 F995 DISPLAY F25C F25C F25T JYD/* F995 F995 F807 F996 F808 DISPLAY F25C F25T DELAY F36C F36C F807 MAYE F36C F36C F36D CHAY F36C F36C F36C F807 MAYE F36C F36C F36C F36C F807 MAYE F36C F36C F36C F36C F36C F807 MAYE F36C F36C F36C F36C F36C F36C F36C F36C						
F223						
F227 F228 HEXA HIGHT F85F F850 CHR\$(B);^ F237 F238 CUTIX F85B F850 CHR\$(D); CR F241 F244 "?" AND BELL F85D F872 CHR\$(D); CR F245 F248 BELL F85D F872 CHR\$(D); CR F249 F24C TV/N* IN F887 F839 IX \$7 1 ******** CLR F252 F255 IN HEX4 F892 F884 CLRTV, CHR\$(C) F252 F255 IN HEX4 F892 F884 CLRTV, CHR\$(C) F252 F255 IN HEX2 F882 F894 CLRTV, CHR\$(C) F252 F255 IN HEX2 F882 F894 CLRTV, CHR\$(C) F255 F256 IN HEX2 F882 F894 CLRTV, CHR\$(C) F256 F257 IN TO F805 F807 CHR\$(E); NOMAL F257 F258 F258 IN HEX2 F882 F894 CLRTV, CHR\$(C) F258 F256 IN HEX2 F882 F894 CLRTV, CHR\$(C) F257 F257 B J7V;* F357 F350 F372 CR, "" Sampling F805 F805 F807 CHR\$(E); NOMAL F358 F373 F375 "?" Sampling F948 F95C ************************************						
F237 F238 CUTIX F85B F860 CHR*(A); F241 F244 **?** AND BELL F855 F85B CHR*(D); CR F245 F248 BELL F855 F872 CHR*(2); UP SCRDL F246 F247 F248 BELL F855 F872 CHR*(2); UP SCRDL F247 F248 F248 T248 F887 F899 IX か 1 **** DCR F252 F255 IN HEX4 F892 F884 CLR*(C) F252 F255 IN HEX4 F892 F884 CLR*(C) F255 F255 IN HEX2 F842 F884 CHR*(D); NDMAL F256 F256 F356 V HEX2 F882 F893 CHR*(F); NDMAL F257 F258 F255 IN HEX2 F882 F893 CHR*(F); NDMAL F258 F256 F356 V HEX2 F884 F895 CHR*(E); INV F258 F257 F358 F897 CHR*(E); INV F258 F357 F358 F358 F358 F358 F358 F358 F358 F358						•
F241						*
F245						
F249						
F352						•
F25E F25B IN HEX2 F582 F894 CHR\$(3); DOWN SCROL F2EC F2EF 1 BYTE ADDD F8C2 F8D3 CHR\$(5); NDMAL F2FC F2FF B JYDA* F8C6 F8D7 CHR\$(6); INV F2FC F2FF B JYDA* F8C6 F8D7 CHR\$(6); INV F3567 F35C J JYDA* F8C6 F8D7 MOVBLK F3568 F372 CR, "" 9209787 F958 F95C WSTEP F3739 F375 "?" 9209787 F958 F957 F958 WSRAK F388 D JYDA* F960 F971 WSWI F388 F380 F300 F80M F957 F960 F971 WSWI F388 F380 F301 TO F97F F992 WINDUT COMMAND F35C5 F3C6 M JYDA* F994 F995 WSRAK F35C5 F3C6 M JYDA* F994 F995 WSRAK F35C6 F3C7 R JYDA* F994 F995 F80M F35C8 F3C8 M JYDA* F994 F995 F80M F35C9 F3C7 R JYDA* F994 F995 F80M F36C9 F3C7 R JYDA* F994 F995 F80M F3C7 F466 INX YMF F994 F995 F80M F3C8 F3C8 F3C8 M JYDA* F998 F999 DISPLAY F3C9 F3C9 F3C9 F80M TD F995 F80M F3C9 F3C9 F3C9 F80M TD F995 F80M F3C9 F3C9 F3C9 F3C9 F3C9 F3C9 F3C9 F3C9						
F2EC F2EF 1 BYTE ADD FBC2 F8D3 CHR\$(F); NDMAL F2FC F2FF B 377)* F367 F36C J 370)* F367 F36C J 370)* F368 F8D7 CHR\$(E); INV F367 F36C J 370)* F368 F372 CR, "-" 32090307 F379 F375 "?" 32090307 F383 F388 D 370)* F384 F395 ** F385 F30C F3CM F377 F3F8 ** F386 F3C1 TO F377 F392 ** F387 F36C F3C1 TO F377 F392 ** F388 F3C2 F3CM F3C1 F3C1 F3C1 F3C1 F3C2 F3C3 ** F36C F3C1 TO F3C1 F3C2 F3C3 ** F36C F3C3 M 370)* F36C F3C4 F3C5 F3C5 F3C4 F3C5 F3C5 F3C5 F3C5 F3C5 F3C5 F3C5 F3C5						
# F8C6 F8D7 CHR*(E); INV F2FC F2FF B コマント* F367 F35C J コマント* F368 F8C0 F8DD MOVBLK F360 F372 CR, "-" 9ュッツリョウ F948 F95C *STEP F379 F375 "?" 9ュッツリョウ F957 F958 *8REAK F383 F388 D コマント* F360 F371 T0 F957 F958 *8REAK F360 F371 T0 F957 F958 *8REAK F360 F3C1 T0 F97F F992 *INPUT COMMAND F35C5 F3C6 M コマント* F994 F995 *REAK POINT F3ED F3F2 R コマント* F998 F999 DISPLAY F45F F45A INX F0F F998 F999 DISPLAY F45B F45D DEX F0F F998 F999 DISPLAY F46B F46D T970ト* F998 F908 INPUT ADDR AND DATA F44B F46D T970ト* F993 F994 SP CD B A IX PC F4CF F4CF F80M TD F995 F903 START ADDR - F4CF F45E F コマント* FA02 FA13 T0 F518 F510 BELL FA10 F82E TRANSFER F52C F531 2.44F1 F928 FA39 *ERROR F55C F531 2.44F1 F928 FA39 *ERROR F55C F551 2.44F1 F928 FA39 *ERROR F55C F555 DELAY F90F F900 : F57F F57E DELAY F90F F90 : F586 F586 DELAY F90F F90 : F586 F586 DELAY F90F F90 : F587 F578 DELAY FFBC *** BASIC MASTER V1.1						
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F36D F372 CR, "-" シュッツショウ F34B F95C **S*EP F379 F375 "?" シュッツショウ F957 F968 **BREAK F383 F388 D コマント* F960 F971 **SWI F384 F385 F386 F386 F387 F378 ステート メッケーシ F386 F361 T0				-nnn		MAINING IS
F379						
F383			,			
F3AA F3AC FROM F357 F378 スチートメッセージ。 F3BC F3C1 TO F37F F392 * INPUT COMMAND F3C5 F3C8 M コマンド。 F394 F3A5 * BREAK POINT F3ED F3F2 R コマンド。 F3A4 F385 F3A5 F3A5 F3A6 F3A7 F3A6 F3A7 F3A8 F3A						
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F3C5 F3C8 M 370% F394 F3A5 * BREAK POINT F3ED F3F2 R 370% F3A4 F3B5 - F45F F46A INX 76F F3A9 F3B9 DISPLAY F468 F46D DEX 76F F3BA F3C8 INPUT ADDR AND DATA F471 F475 G370% F3BA F3CB INPUT ADDR AND DATA F4AB F4B0 T 370% F3D3 F3E4 S2 CC B A IX PC F4CF F4CF F3CM TD F3F2 FA03 START ADDR - F4E7 F4EE F 370% FA02 FA13 TO F516 ? 71907 FA09 FA1A MASTER BLOCK ADDR F518 F51D BELL FA1D FA2E TRANSFER F52C F531 2.4KHZ FA28 FA39 * ERROR F540 F545 1.2KHZ FA32 FA43 PROGRAM NAME F555 F556 DELAY F3CF F3CF F3E0 : *** BASIC MASTER V1.1 F586 F586 DELAY F586 F586 DELAY						
F3ED F3F2 R JVJ)** F4SF F464 INX 7HF F3A9 F9B9 DISPLAY F458 F46D DEX 7HF F9BA F9C5 FROM F471 F476 GJVJ** F48B F4BO T JVJ** F48B F4BO T JVJ** F40F F4CF FROM TD F9F2 FAO3 START ADDR - F4CF F4CF FACE F JVJ** F515 ? 71/27 FAO9 FA1A MASTER BLOCK ADDR F518 F51D BELL FA1D FA2E TRANSFER F52C F531 2.4KHZ FA28 FA39 * ERROR F540 F545 1.2KHZ FA28 FA39 * ERROR F555 F565 DELAY F9CF F9CO : F57E F57E DELAY FFBC **** BASIC MASTER V1.1						
F45F F454 INX THF F3A8 F3B9 DISPLAY F458 F45D DEX 765 F3B4 F3C5 FROM F471 F475 G37ント・ F3BA F3CB INPUT ADDR AND DATA F4AB F4B0 T J7ント・ F3B3 F3E4 S2 CC B A IX PC F4CF F4CF FROM TD F3F2 FA03 START ADDR - F4E7 F4EE F J7ント・ FA02 FA13 TO F515 ? 71507 FA09 FA1A MASTER BLOCK ADDR F518 F51D BELL FA1D FA2E TRANSFER F52C F531 2.4KHZ FA2B FA39 * ERROR F540 F545 1.2KHZ FA32 FA43 PROGRAM NAME F555 F565 DELAY F3CF F3E0 : **** BASIC MASTER V1.1 F585 F586 DELAY F58C **** BASIC MASTER V1.1						
F468 F46D DEX 762 F304 F905 F304 F475 G3704 F475 G3704 F98A F908 INPUT ADDR AND DATA F48B F480 T J7044 F983 F984 S9 CC 8 A IX PC F405 F405 F405 F405 F405 F405 F405 F405						
F471 F476 GDYD ** F3BA F9CB INPUT ADDR AND DATA F4AB F4B0 T DYD ** F3D3 F9E4 S2 CC B A IX PC F4CF F4CF FROM TD F9F2 FA03 START ADDR - F4E7 F4EE F DYD ** FA02 FA13 TO F515 ? 7507 FA09 FA1A MASTER BLOCK ADDR F518 F51D BELL FA1D FA2E TRANSFER F52C F531 2.4KHZ FA2B FA39 * ERROR F540 F545 1.2KHZ FA32 FA43 PROGRAM NAME F555 F565 DELAY F3CF F9E0 : F57E F57E DELAY FFBC **** BASIC MASTER VI.1 F585 F586 DELAY FFBC **** BASIC MASTER VI.1						
F4AB F4BO T JYDA* F9D3 F9E4 SP CC B A IX PC F4CF F4CF FROM TO F9F2 FRO3 START ADDR — F4E7 F4EE F JYDA* FA02 FA13 TO F515 ? 711-07 FA09 FA1A MASTER BLOCK ADDR F518 F51D RELL FA1D FA2E TRANSFER F52C F531 2.4KHZ FA28 FA39 * ERROR F540 F545 1.2KHZ FA32 FA43 PROGRAM NAME F555 F565 DELAY F3CF F9E0 : **** BASIC MASTER V1.1 F585 F57E DELAY FFBC **** BASIC MASTER V1.1						
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FFF8
                    CLRTV, CHR$($C)
            F848
F832
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次回かり モニターワークエリア BASICサブルーチン等も載せます。