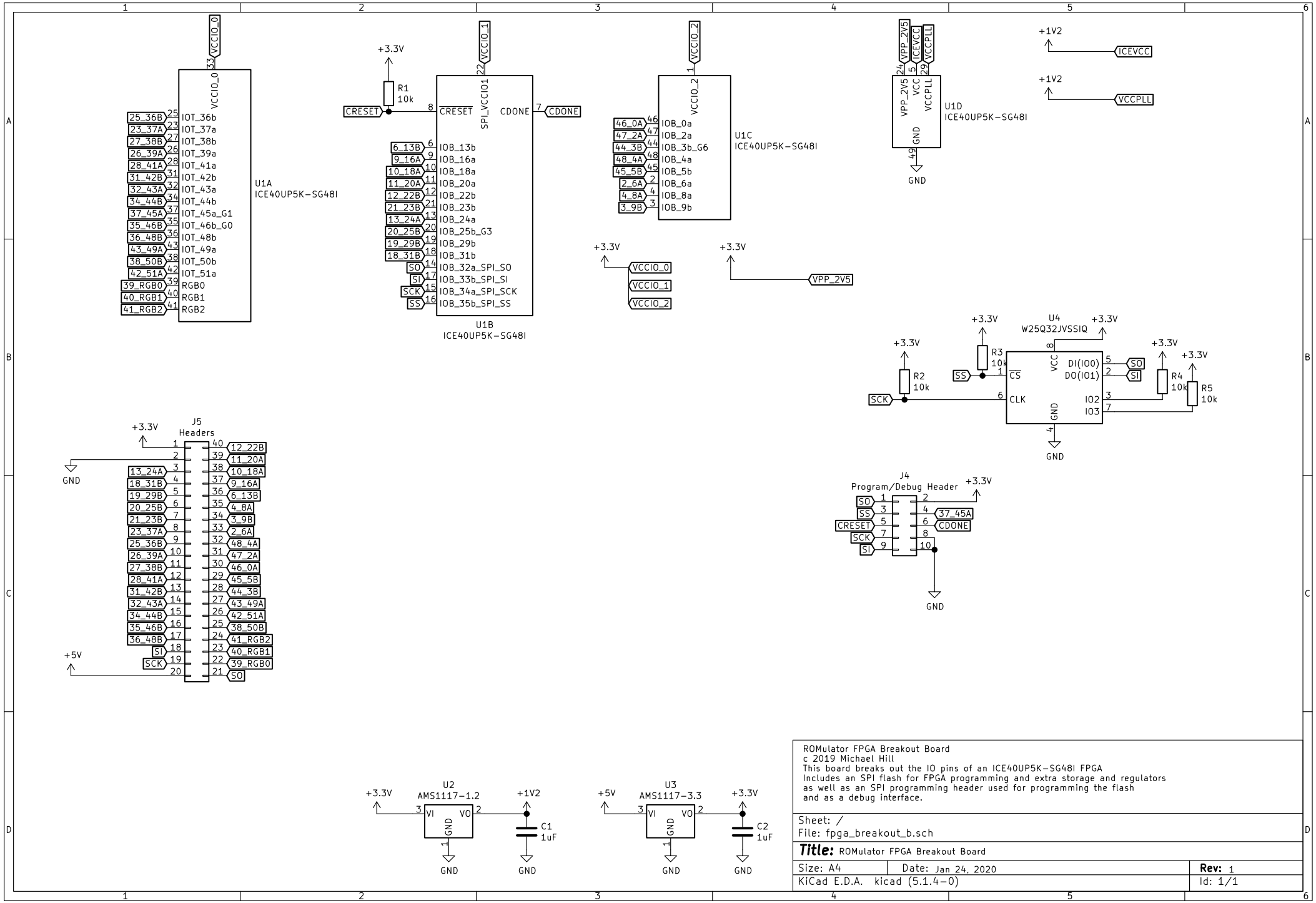

ROMulator FPGA Breakout Board
c 2019 Michael Hill
This board breaks out the IO pins of an ICE40UP5K-SG48I FPGA
Includes an SPI flash for FPGA programming and extra storage and regulators
as well as an SPI programming header used for programming the flash
and as a debug interface.

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File: fpga_breakout_b.sch

Title: ROMulator FPGA Breakout Board

Size: A4	Date: Jan 24, 2020	**Rev:** 1
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