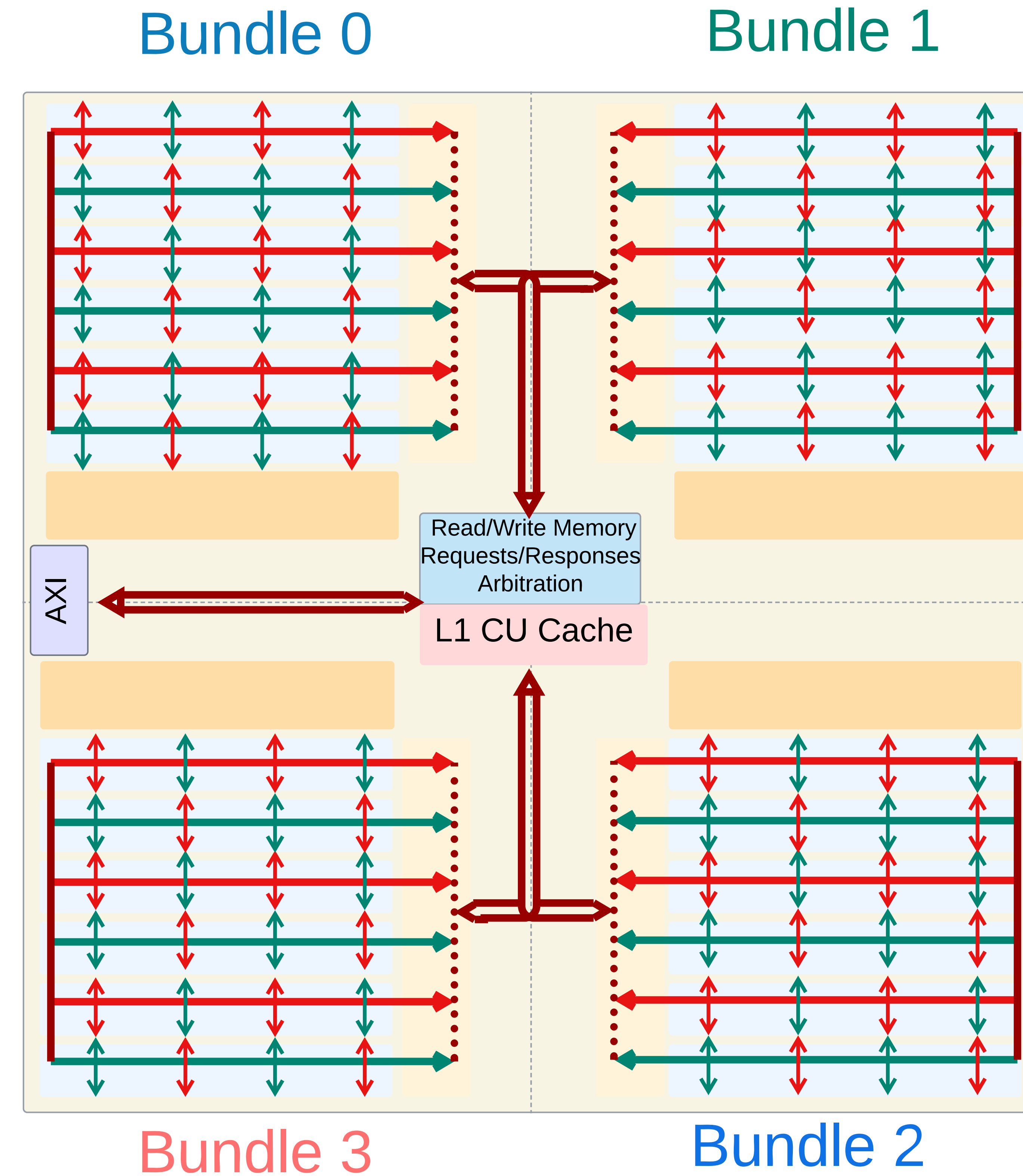
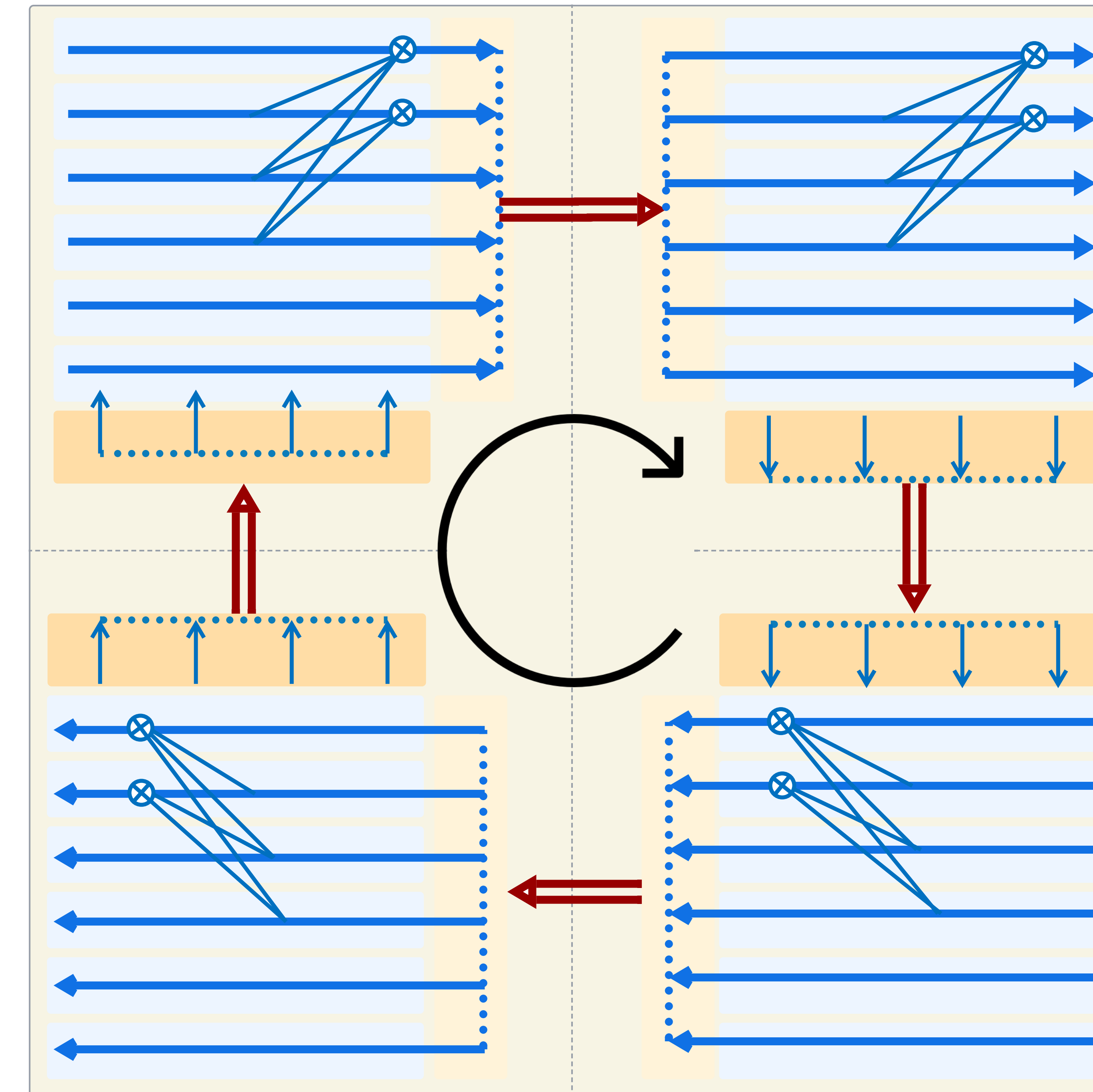


Memory/Control Interconnect



Engine Data Interconnect



GraphBlox Multi-CU Overlay Architecture

