Memory/Control Interconnect Engine Data Interconnect Bundle 1 Bundle 0 Read/Write Memory Requests/Responses Arbitration L1 CU Cache Bundle 2 Bundle 3 Engine/Memory Packets Responses Arbitration Engine Memory Control

GraphBlox Packet Types

Configuration	CSR/Sequence Read/Write
increment	1-bit
decrement	1-bit
sequence	1-bit
buffer (mode)	1-bit
array_pointer	64-bit
array_size	32-bit
index_start	32-bit
index_end	32-bit
stride	32-bit
granularity	6-bit

Engine Configuration

Engine

Metadata	Engine Packet
routing	from, to, sequence
cmd	data_type, configure
Data	32-bit Fields
field_0	32-bit
field_1	32-bit
field_2	32-bit
field_3	32-bit

Packet Routing/Flow

Data Payload

Memory

Metadata	Memory Packet
routing	from
cmd	memory command
Data	32-bit Fields
field_0	32-bit

Control

Metadata	Control Packet
routing	sequence
cmd	control command