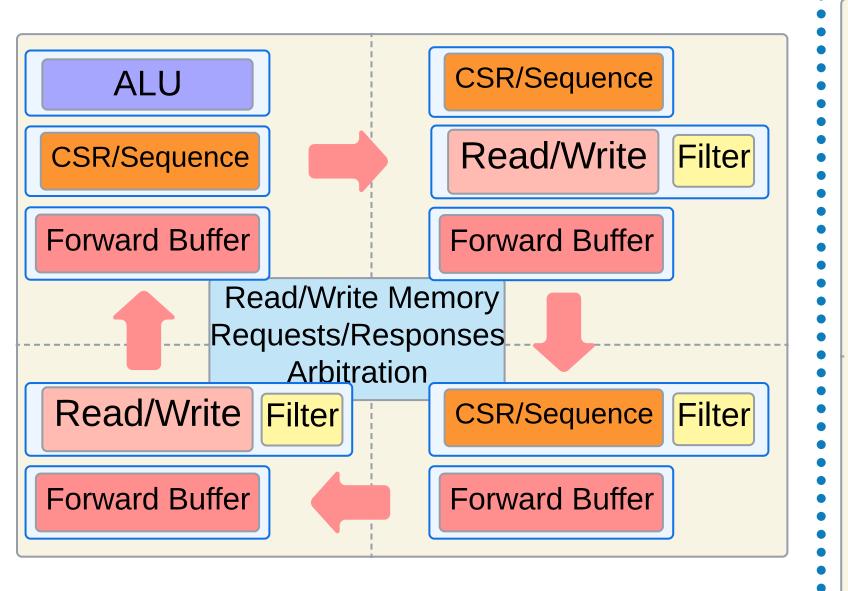
PR

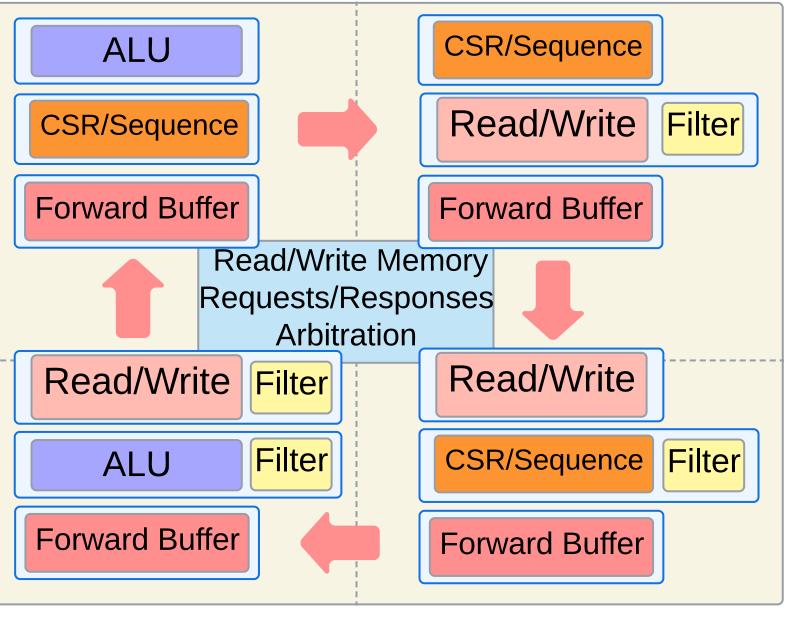
ALU Read/Write CSR/Sequence Forward Buffer Forward Buffer Read/Write Memory Requests/Responses_ Arbitration CSR/Sequence Read/Write Forward Buffer Forward Buffer

Add engines for more algorithms

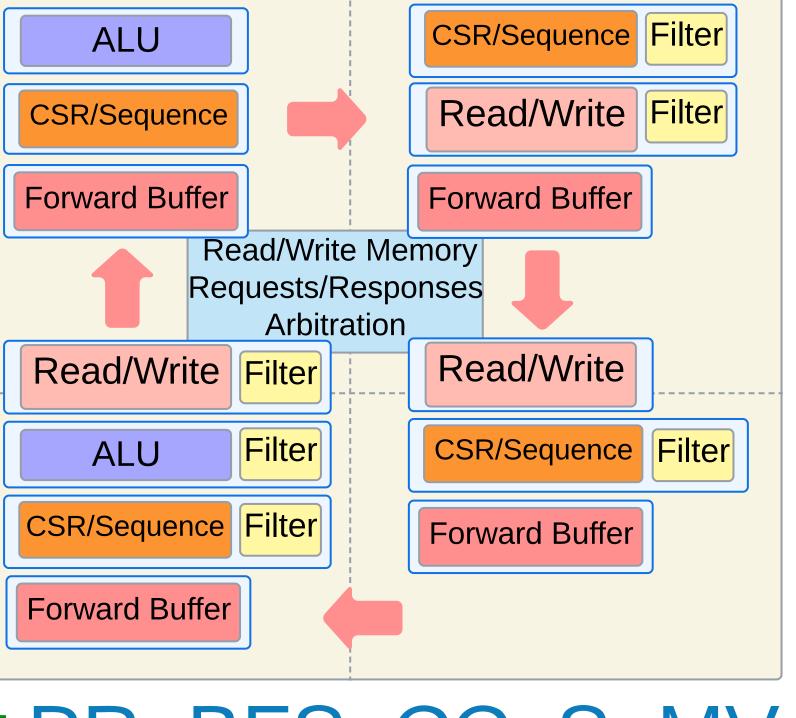
BFS



+PR, SpMV



+PR, BFS, SpMV



+PR, BFS, CC, SpMV