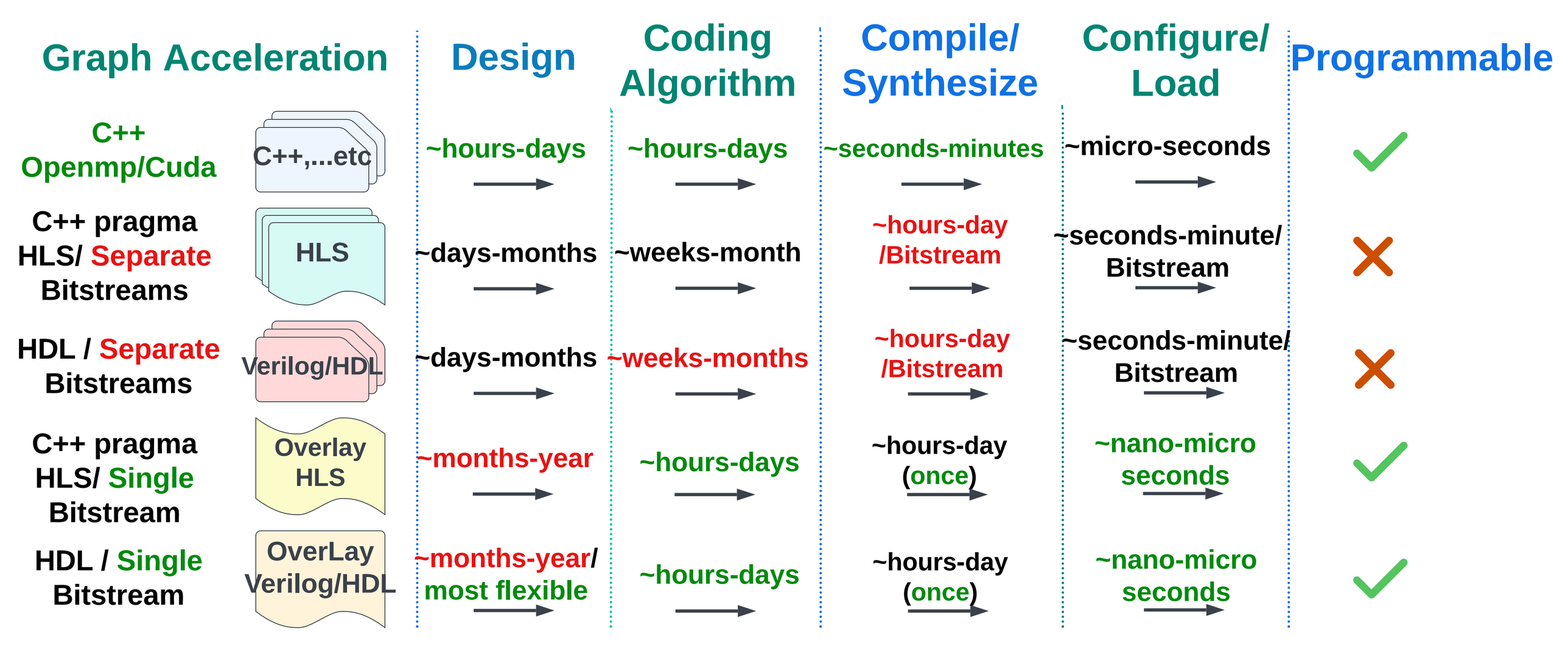
Graph Acceleration
Design
Coding Algorithm
Compile/ Synthesize
Configure/ Load
Programmable
C++ Openmp/Cuda
C++,...etc
~hours-days
~hours-days
~seconds-minutes
~micro-seconds
C++ pragma HLS/ Separate Bitstreams
HLS
~days-months
~weeks-month
~hours-day /Bitstream
~seconds-minute/ Bitstream
HDL / Separate Bitstreams
Verilog/HDL
~days-months
~weeks-months
~hours-day /Bitstream
~seconds-minute/ Bitstream
C++ pragma HLS/ Single Bitstream
Overlay HLS
~months-year
~hours-days
~hours-day (once)
~nano-micro seconds
HDL / Single Bitstream
OverLay Verilog/HDL
~months-year/ most flexible
~hours-days
~hours-day (once)
~nano-micro seconds