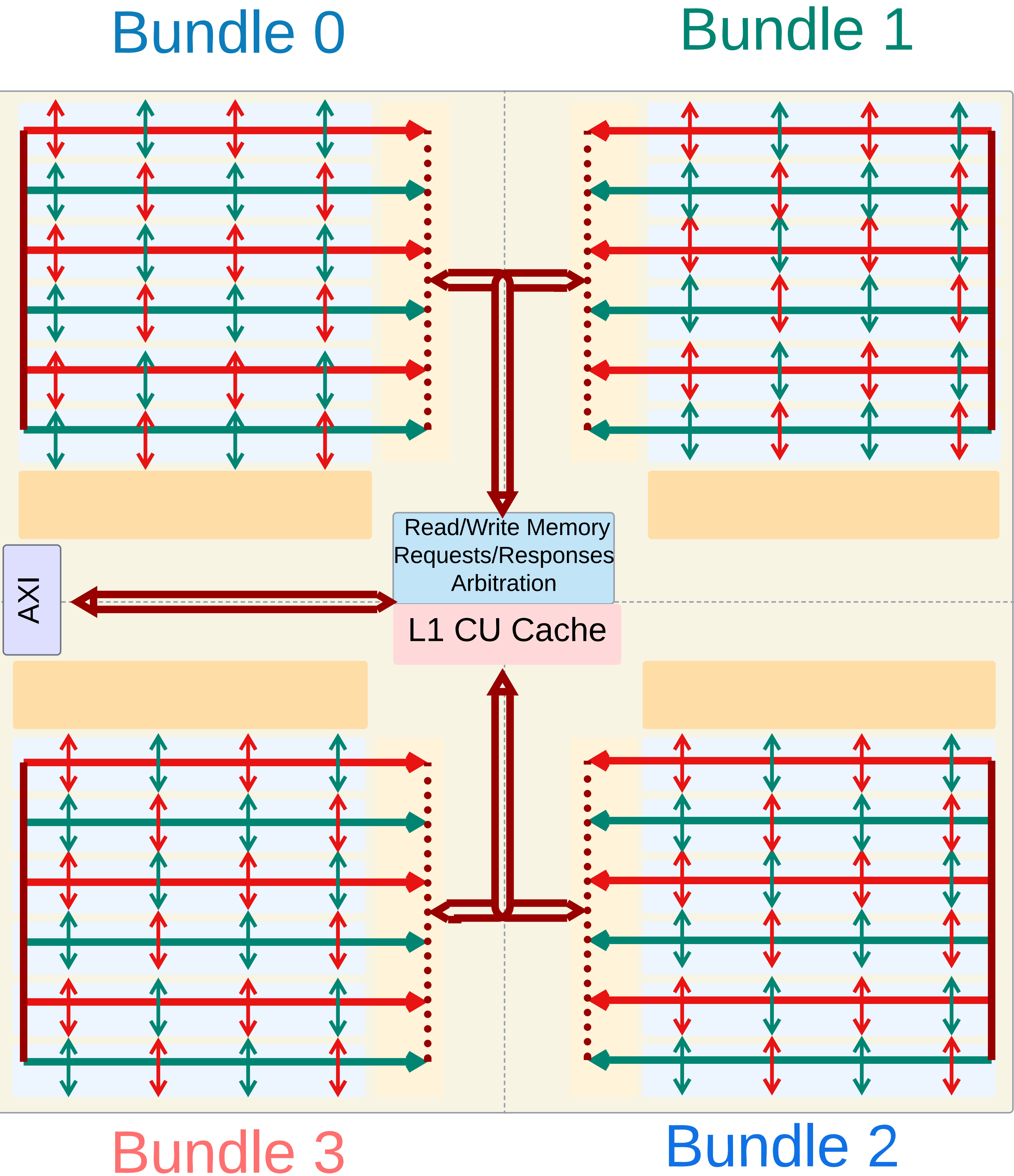


Memory/Control Interconnect

Vertex CU 0



Engine Data Interconnect

