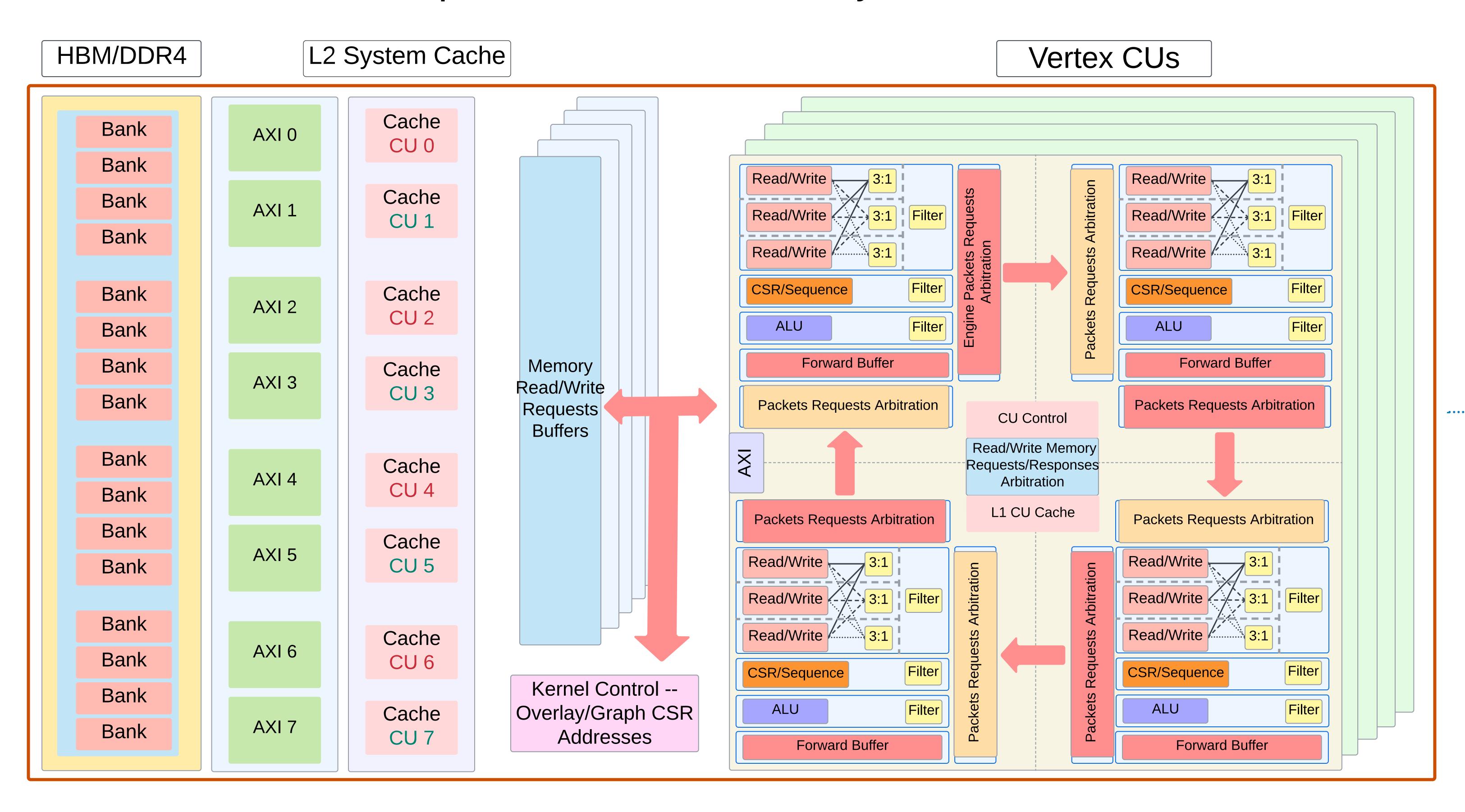
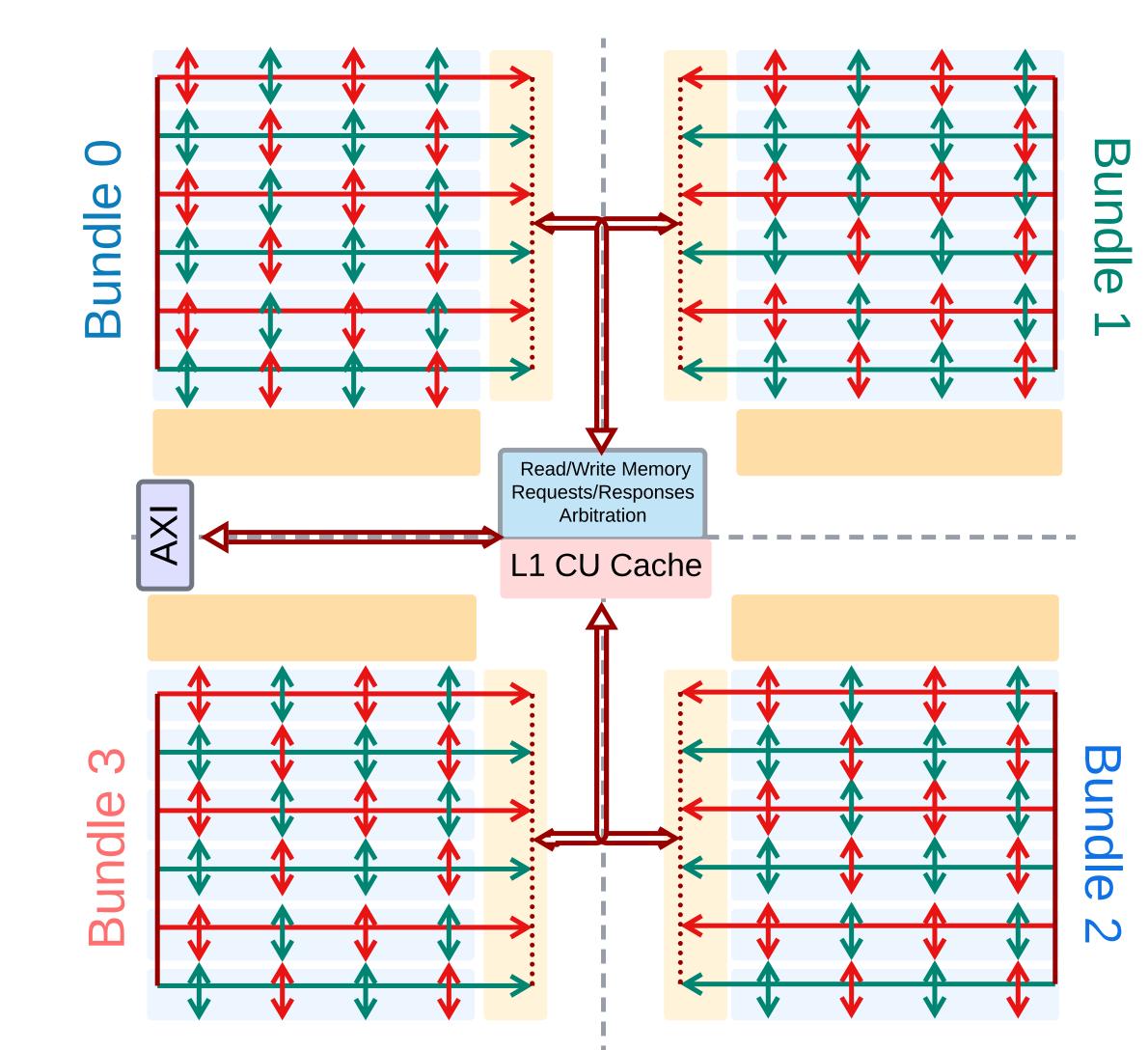
GraphBlox Multi-CU Overlay Architecture



Memory/Control Interconnect



Engine Data Interconnect

