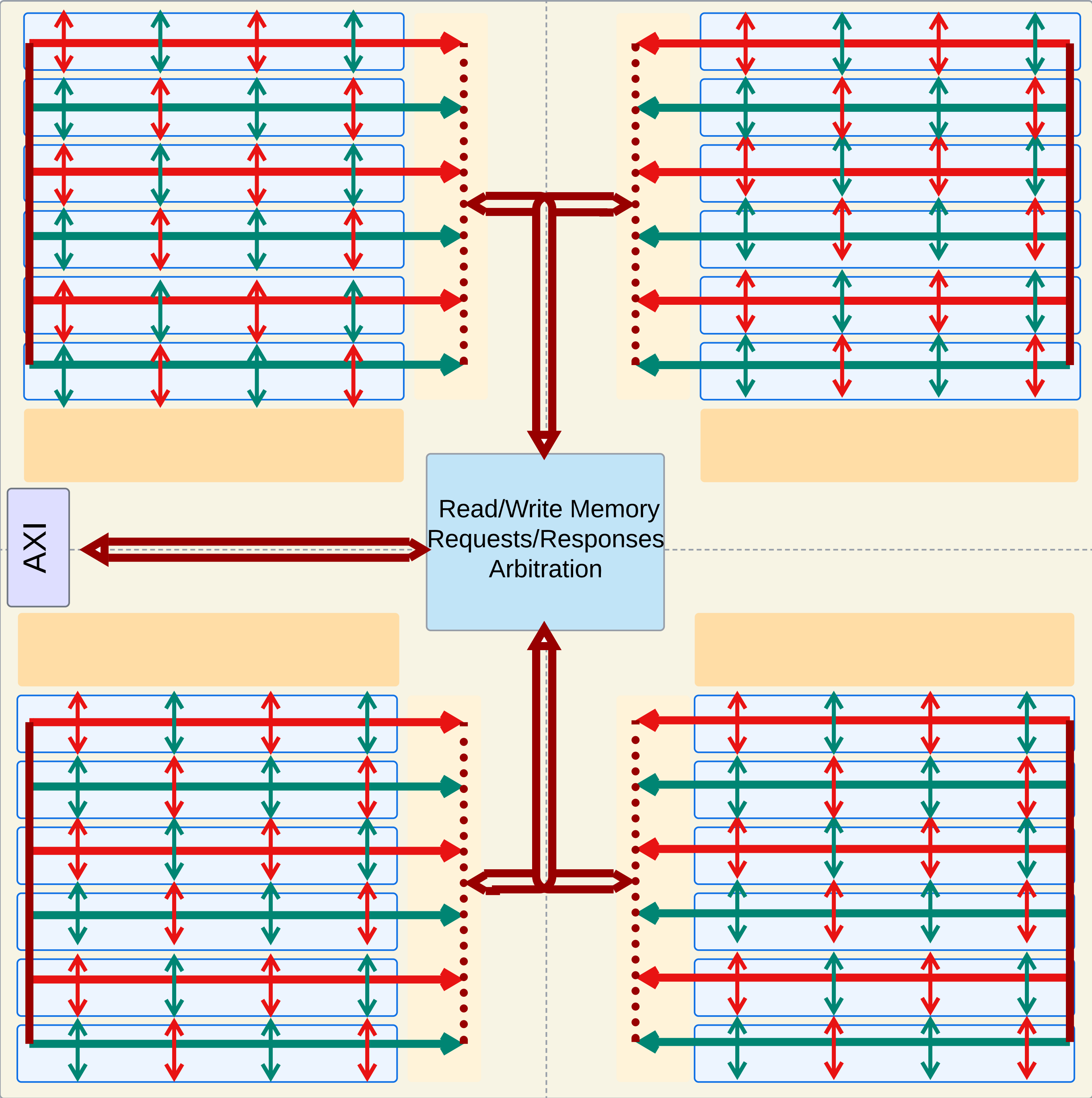


# Memory/Control Interconnect

Bundle 0

Bundle 1



Bundle 3

Bundle 2