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Supervised Theses

M.A.Sc. Theses:

- [1] Q. Sun, “Efficient dc analysis of nonlinear designs using model order reduction,” M.A.Sc. thesis, Department of Electronics, Carleton University, Ottawa, Canada, Jan. 2017.
- [2] Y. Tao, “Variability analysis via parametrized model order reduction,” M.A.Sc. thesis, Department of Electronics, Carleton University, Ottawa, Canada, Sep. 2016.
- [3] K. Guo, “Efficient stochastic collocation based variability analysis using model-order reduction techniques,” M.A.Sc. thesis, Department of Electronics, Carleton University, Ottawa, Canada, Sep. 2016. (Nominated for the University Medal)
- [4] X. Deng, “Efficient projection-based stability guaranteed model order reduction for active circuits,” M.A.Sc. thesis, Department of Electronics, Carleton University, Ottawa, Canada, Jan. 2016.

4th Year Projects:

- [1] F. Safavi, “Designing an advanced VLSI circuits simulator using model order reduction techniques,” 4th-year project report, Department of Electronics, Carleton University, Ottawa, Canada, May 2012.