





8x GPIOs per module (3.3V ONLY) Limited ESD protection (2kV HBM) on each I/O. 3.3v and 5v supplied by system DC/DC converters. At least 100mA available per module. 24v rail is actually 5-24v, up to 4A per module. 2 MOD4\_PIN4 6 MOD4\_PIN6 MOD2\_PIN2 MOD2\_PIN4 MOD2\_PIN1 1 MOD2\_PIN3 3 MOD1\_PIN1 3 2 MOD1\_PIN2 MOD1 PIN4 MOD3\_PIN1 \ \frac{1}{-} MOD3\_PIN2 (MOD4\_PIN1) 1 MOD3\_PIN2 MOD3\_PIN4 MOD4\_PIN3 3 6 MOD1\_PIN6 6 MOD3\_PIN6 6 MOD2\_PIN6 MOD4\_PIN5 8 MOD1\_PIN8 8 MOD2\_PIN8 8 MOD3\_PIN8 MOD1\_PIN7 (MOD2\_PIN7) MOD3\_PIN7 MOD4\_PIN7 9 10 GND 12 +3V3 14 +5V GND ( 11 +3V3**←** GNDPWR 17 GNDPWR 17 +24V 19 +24V 20 +24V MOD6\_PIN1 3 MOD6\_PIN3 5 MOD6\_PIN5 7 MOD6\_PIN7 7 2 MO<u>D7\_PIN2</u> 2 MOD5\_PIN2 MOD5\_PIN1> MOD6\_PIN2 MOD7\_PIN1 4 MOD7\_PIN4 6 MOD7\_PIN6 8 MOD7\_PIN8 10 GND 12 +3V3 MOD5\_PIN5\_5 MOD7\_PIN3 3 MOD7\_PIN5 5 MOD5\_PIN7 MOD7\_PIN7 GND 9 +3V3 11 wiggleport open source hardware Sheet: /Modules/ File: spine-modules.sch Title: Wiggle Spine board

Size: USLetter Date:

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