





8x GPIOs per module (3.3V ONLY) Limited ESD protection (2kV HBM) on each I/O. 3.3v and 5v supplied by system DC/DC converters. At least 100mA available per module. 24v rail is actually 5-24v, up to 4A per module. 2 MOD4_PIN4 6 MOD4_PIN6 MOD2_PIN2 MOD2_PIN4 MOD2_PIN1 1 MOD2_PIN3 3 MOD1_PIN1 3 2 MOD1_PIN2 MOD1 PIN4 MOD3_PIN1 \ \frac{1}{-} MOD3_PIN2 (MOD4_PIN1) 1 MOD3_PIN2 MOD3_PIN4 MOD4_PIN3 3 6 MOD1_PIN6 6 MOD3_PIN6 6 MOD2_PIN6 MOD4_PIN5 8 MOD1_PIN8 8 MOD2_PIN8 8 MOD3_PIN8 MOD1_PIN7 (MOD2_PIN7) MOD3_PIN7 MOD4_PIN7 9 10 GND 12 +3V3 14 +5V GND (11 +3V3**←** GNDPWR 17 GNDPWR 17 +24V 19 +24V 20 +24V MOD6_PIN1 3 MOD6_PIN3 5 MOD6_PIN5 7 MOD6_PIN7 7 2 MO<u>D7_PIN2</u> 2 MOD5_PIN2 MOD5_PIN1> MOD6_PIN2 MOD7_PIN1 4 MOD7_PIN4 6 MOD7_PIN6 8 MOD7_PIN8 10 GND 12 +3V3 MOD5_PIN5_5 MOD7_PIN3 3 MOD7_PIN5 5 MOD5_PIN7 MOD7_PIN7 GND 9 +3V3 11 wiggleport open source hardware Sheet: /Modules/ File: spine-modules.sch Title: Wiggle Spine board

Size: USLetter Date:

KiCad E.D.A. kicad 4.0.0-rc2-1-stable

Rev: A

ld: 4/14



















