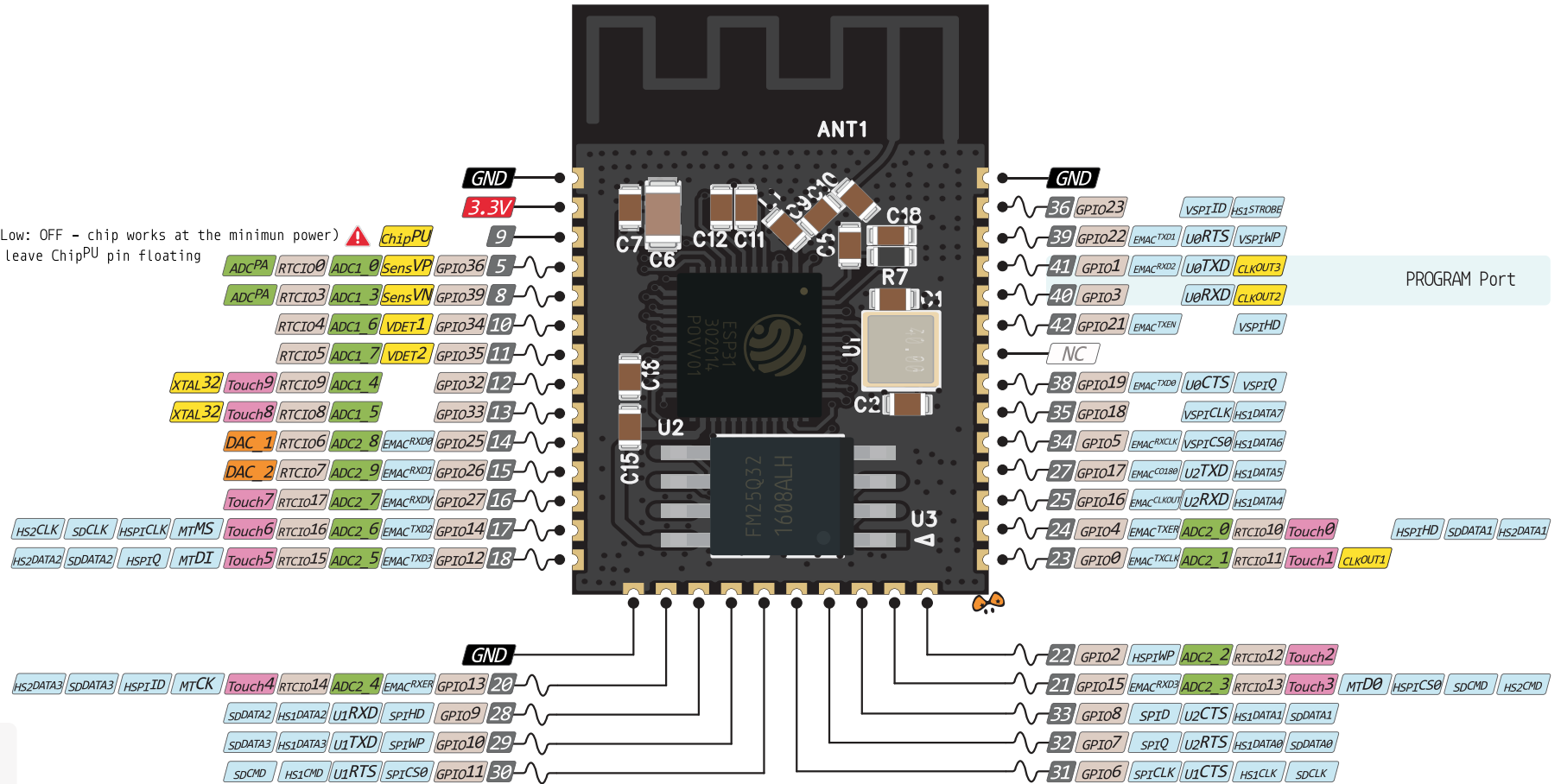# WROOM32
PINOUT

⚠ **Absolute MAX per pin 12mA**
recommended 6mA

ANT1

GND
3.3V

Chip Enabled (High:ON, Low: OFF – chip works at the minimun power) ⚠ ChipPU
Do not leave ChipPU pin floating

				ChipPU	9
ADCPA	RTCIO0	ADC1_0	SensVP	GPIO36	5
ADCPA	RTCIO3	ADC1_3	SensVN	GPIO39	8
	RTCIO4	ADC1_6	VDET1	GPIO34	10
	RTCIO5	ADC1_7	VDET2	GPIO35	11
XTAL32	Touch9	RTCIO9	ADC1_4	GPIO32	12
XTAL32	Touch8	RTCIO8	ADC1_5	GPIO33	13

ESP31
302014
POVV01

R7

U1

C2

U2

C15

FM25Q32
1608ALH

U3

| HS2CLK | SDCLK | HSPICLK | MTMS | Touch6 | RTCIO16 | ADC2_6 | EMACTXD2 | GPIO14 | 17 |
| HS2DATA2 | SDDATA2 | HSPIQ | MTDI | Touch5 | RTCIO15 | ADC2_5 | EMACTXD3 | GPIO12 | 18 |

DAC_1	RTCIO6	ADC2_8	EMACRXD0	GPIO25	14
DAC_2	RTCIO7	ADC2_9	EMACRXD1	GPIO26	15
Touch7	RTCIO17	ADC2_7	EMACRXDV	GPIO27	16

GND

HS2DATA3	SDDATA3	HSPIID	MTCK	Touch4	RTCIO14	ADC2_4	EMACRXER	GPIO13	20
SDDATA2	HS1DATA2	U1RXD	SPIHD					GPIO9	28
SDDATA3	HS1DATA3	U1TXD	SPIWP					GPIO10	29
SDCMD	HS1CMD	U1RTS	SPICS0					GPIO11	30

GND

36	GPIO23			VSPIID	HS1STROBE
39	GPIO22	EMACTXD1	U0RTS	VSPIWP	
41	GPIO1	EMACRXD2	U0TXD	CLKOUT3	
40	GPIO3		U0RXD	CLKOUT2	
42	GPIO21	EMACTXEN		VSPIHD	

PROGRAM Port

NC

38	GPIO19	EMACTXD0	U0CTS	VSPIQ	
35	GPIO18		VSPICLK	HS1DATA7	
34	GPIO5	EMACRXCLK	VSPICS0	HS1DATA6	
27	GPIO17	EMACCO180	U2TXD	HS1DATA5	
25	GPIO16	EMACCLKOUT	U2RXD	HS1DATA4	
24	GPIO4	EMACTXER	ADC2_0	RTCIO10	Touch0	HSPIHD	SDDATA1	HS2DATA1
23	GPIO0	EMACTXCLK	ADC2_1	RTCIO11	Touch1	CLKOUT1

22	GPIO2	HSPIWP	ADC2_2	RTCIO12	Touch2
21	GPIO15	EMACRXD3	ADC2_3	RTCIO13	Touch3	MTD0	HSPICS0	SDCMD	HS2CMD
33	GPIO8	SPID	U2CTS	HS1DATA1	SDDATA1
32	GPIO7	SPIQ	U2RTS	HS1DATA0	SDDATA0
31	GPIO6	SPICLK	U1CTS	HS1CLK	SDCLK

Legend

- 🟥 Power
- ⬛ GND
- 🟦 Serial Pin
- 🟩 Analog Pin
- 🟨 Control
- ⬜ Physical Pin
- 🟫 Port Pin
- 🟪 Touch Pin
- 🟧 DAC Pin
- ∿ PWM Pin