



The power sum for each pin's group should not exceed 100mA

SDse1

ESpse1

9

10

11

12

Absolute MAX per pin 12mA SD⁰³ CS 1010 12-\ SDD1 INT 13 10 MISO SDD0 SDCLK CLK 14 9 MOSI SDCMD <u>. 888888</u> GND 15 8 VCC TXD2 RTS0 HSPICS 1015 16-1 ◆ √ 7 <u>IO13</u> HSPI[®] CTSØ RXD2 •-√-6 <u>IO12</u> HSPIQ TXD1 102 17-\ HIGH Run, LOW Flash A CS2 100 18-0 • √ 5 [1014] HSPICE I04 19-\v• I05 20-\ **── As know as CHPD** RXD0 103 21-\ 2 ADC TOUT CS1 TXD0 IO1 22-1 RST On board Antenna When you use the <u>sleep mode</u>, IO16 and RST should be connected and IO16 will output LOW to reset the system at the time of wakeup. On every boot/reset/wakeup IO15 must keep LOW, IO2 must keep HIGH.

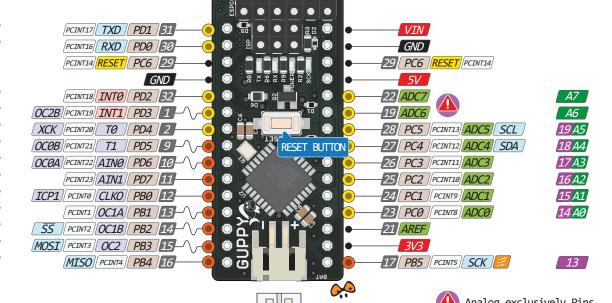
Master Reset (A) (RESET) (SID) (1 REST) (SID) (1 RESET) (SID) (1 RESET) (SID) (1 RESET) (SID) (S

Absolute MAX per pin 20mA recommended 10mA

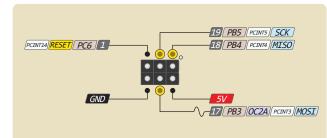
Absolute MAX 200mA for entire package

The output from 3.3V Regulator
Absolute MAX 800mA

VIN The input voltage to the board when it is running from external power. Not USB bus power.



TOOL





Analog exclusively Pins

