Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- · Logic level compatible
- · Very fast switching
- Trench MOSFET technology
- · Enhanced power dissipation capability of 1200 mW

3. Applications

- · Relay driver
- · High-speed line driver
- · Low-side load switch
- · Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j = 25 °C		-	-	30	V
V_{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-	7.6	Α
Static characte	Static characteristics						
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 6 \text{ A}; T_j = 25 \text{ °C}$		-	17	21	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².



30 V, N-channel Trench MOSFET

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D I
2	S	source		
3	D	drain		G 4
			1 2	
			TO-236AB (SOT23)	

6. Ordering information

Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
PMV20EN	TO-236AB	plastic surface-mounted package; 3 leads	SOT23				

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PMV20EN	%KC

[1] % = placeholder for manufacturing site code

30 V, N-channel Trench MOSFET

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V_{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	7.6	Α
		V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	6	А
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	3.8	Α
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \mu s$		-	24	Α
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	510	mW
			[1]	-	1200	mW
		T _{sp} = 25 °C		-	6940	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain d	liode					
I _S	source current	T _{amb} = 25 °C	[1]	-	1.1	А

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².
- [2] Device mounted on an FR4 Printed Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

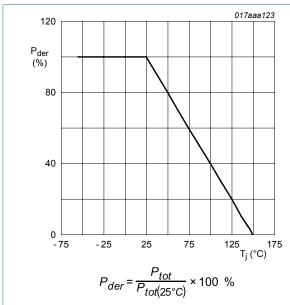


Fig. 1. Normalized total power dissipation as a function of junction temperature

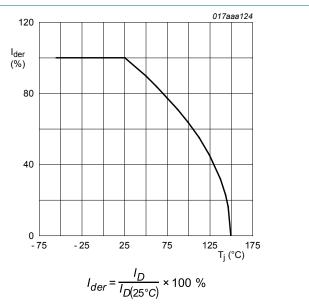


Fig. 2. Normalized continuous drain current as a function of junction temperature

30 V, N-channel Trench MOSFET

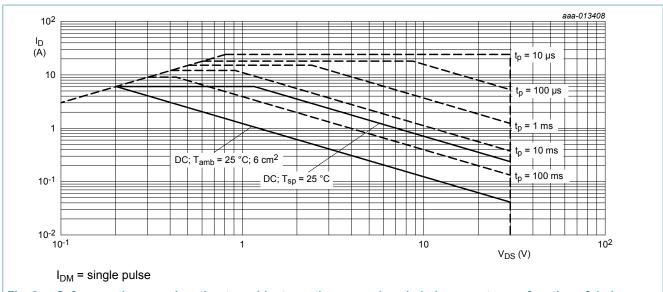


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

30 V, N-channel Trench MOSFET

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
from jui	thermal resistance	from junction to	[1]	-	208	245	K/W
	ambient		[2]	-	88	104	K/W
		in free air; t ≤ 5 s	[2]	-	55	65	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	13	18	K/W

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

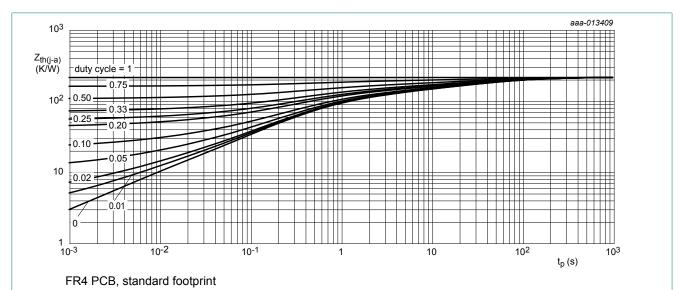


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

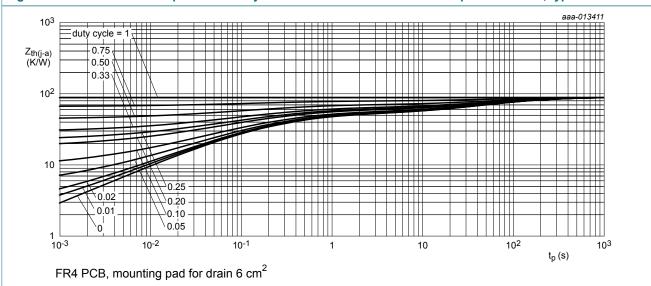


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

30 V, N-channel Trench MOSFET

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					'
V _{(BR)DSS}	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	30	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 °C$	1	1.5	2	V
I _{DSS}	drain leakage current	V _{DS} = 30 V; V _{GS} = 0 V; T _j = 25 °C	-	-	1	μA
I _{GSS} gate leakage current	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	100	nA
	V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-100	nA	
R _{DSon}	drain-source on-state	V _{GS} = 10 V; I _D = 6 A; T _j = 25 °C	-	17	21	mΩ
	resistance	V _{GS} = 10 V; I _D = 6 A; T _j = 150 °C	-	27	34	mΩ
		$V_{GS} = 4.5 \text{ V}; I_D = 5.4 \text{ A}; T_j = 25 \text{ °C}$	-	21	26	mΩ
9 _{fs}	forward transconductance	$V_{DS} = 10 \text{ V}; I_D = 2 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	13	-	S
R_G	gate resistance	f = 1 MHz; T _j = 25 °C	-	1.7	-	Ω
Dynamic ch	naracteristics				'	
Q _{G(tot)}	total gate charge	$V_{DS} = 15 \text{ V}; I_D = 5 \text{ A}; V_{GS} = 10 \text{ V};$	-	7.2	10.8	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	1	-	nC
Q_{GD}	gate-drain charge		-	0.7	-	nC
C _{iss}	input capacitance	$V_{DS} = 15 \text{ V}; f = 1 \text{ MHz}; V_{GS} = 0 \text{ V};$	-	435	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	90	-	pF
C _{rss}	reverse transfer capacitance		-	35	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 15 V; I _D = 5 A; V _{GS} = 10 V;	-	9	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	17	-	ns
t _{d(off)}	turn-off delay time]	-	9	-	ns
t _f	fall time		-	8	-	ns
Source-dra	in diode			•		
V_{SD}	source-drain voltage	I _S = 1.1 A; V _{GS} = 0 V; T _i = 25 °C	-	0.75	1.2	V

30 V, N-channel Trench MOSFET

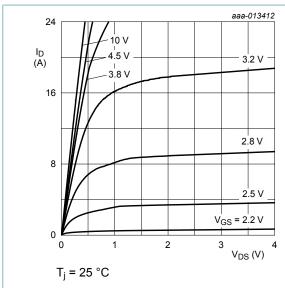


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

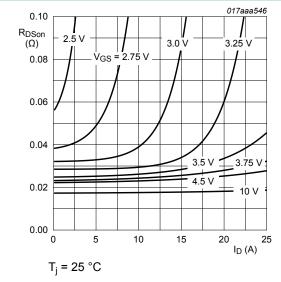


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

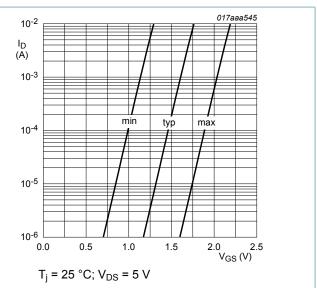


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

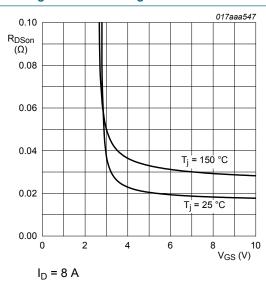


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

30 V, N-channel Trench MOSFET

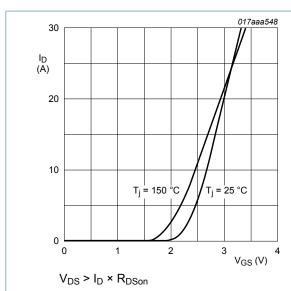


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

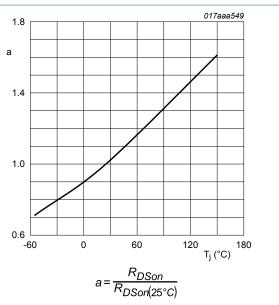


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

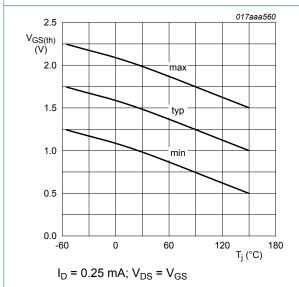


Fig. 12. Gate-source threshold voltage as a function of junction temperature

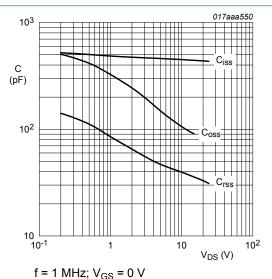


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

30 V, N-channel Trench MOSFET

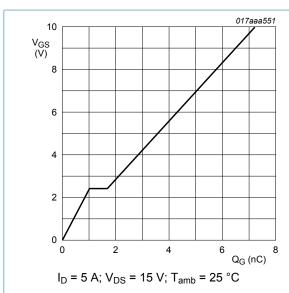


Fig. 14. Gate-source voltage as a function of gate charge; typical values

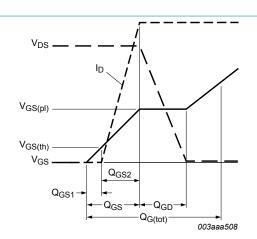


Fig. 15. Gate charge waveform definitions

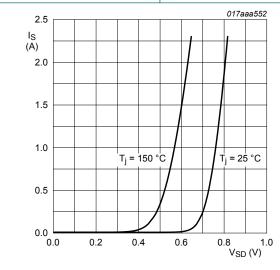
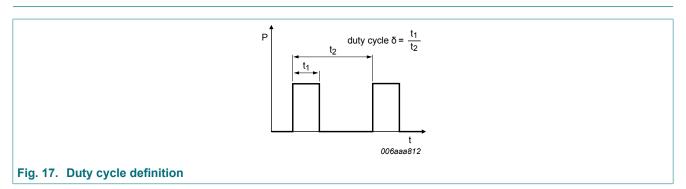


Fig. 16. Source current as a function of source-drain voltage; typical values

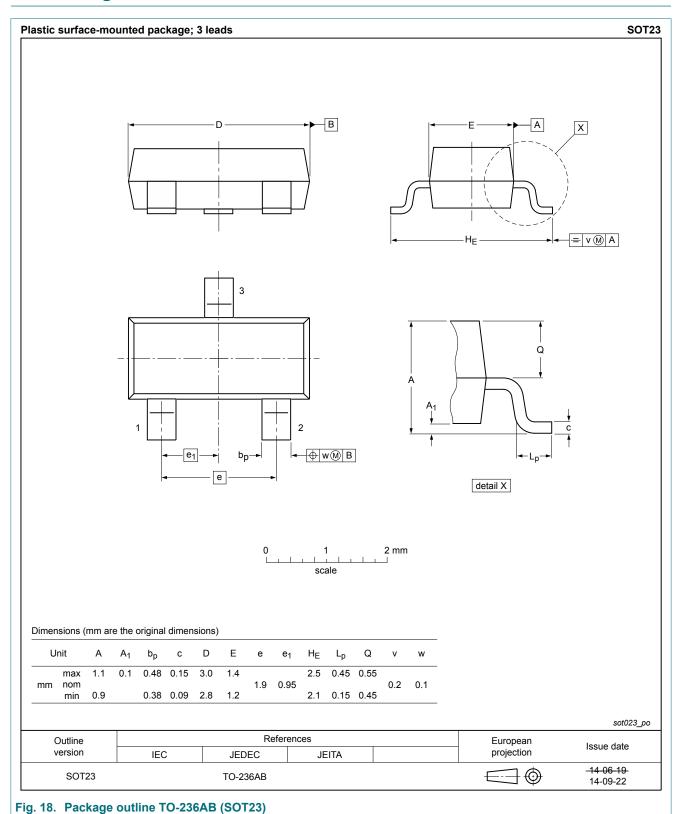
11. Test information

 $V_{GS} = 0 V$



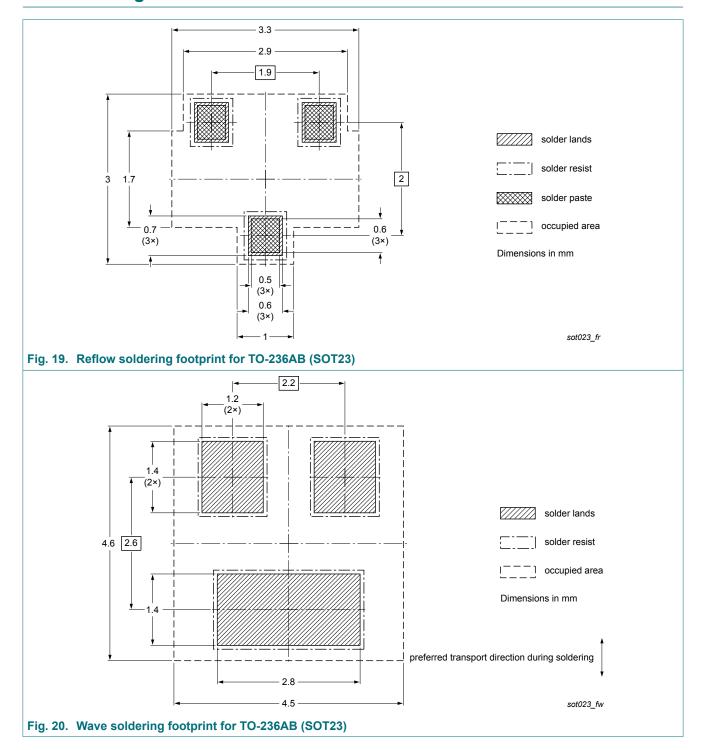
30 V, N-channel Trench MOSFET

12. Package outline



30 V, N-channel Trench MOSFET

13. Soldering



30 V, N-channel Trench MOSFET

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMV20EN v.2	20180705	Product data sheet	-	PMV20EN v.1			
Modifications:	Adaption of the	Adaption of the typical value of g _{fs} according to new wafer fab					
PMV20EN v.1	20140605	Product data sheet	-	-			

30 V, N-channel Trench MOSFET

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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30 V, N-channel Trench MOSFET

Contents

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	3
9.	Thermal characteristics	5
10.	. Characteristics	6
11.	Test information	9
12	Package outline	10
	Soldering	
	Revision history	
15.	Legal information	13
	_	

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15 / 15

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