

C:/Users/ASUS/.jenkins  
/workspace/testJob/Core  
/Src/stm32l4xx\_it.c

```
graph TD; A["C:/Users/ASUS/.jenkins  
/workspace/testJob/Core  
/Src/stm32l4xx_it.c"] --> B["main.h"]; A --> C["stm32l4xx_it.h"];
```

main.h

stm32l4xx\_it.h