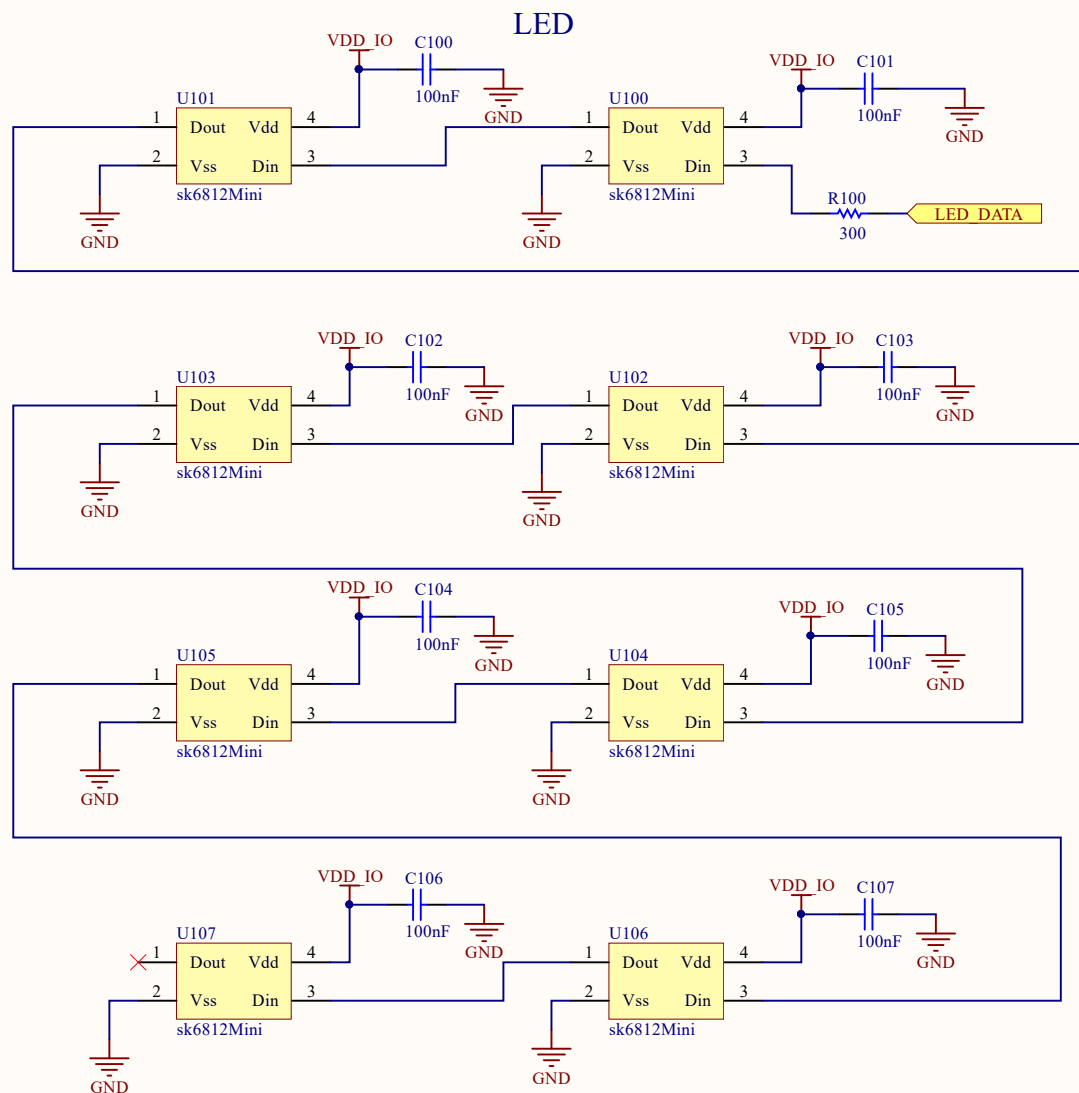
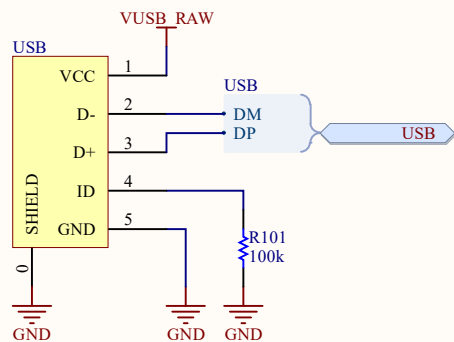
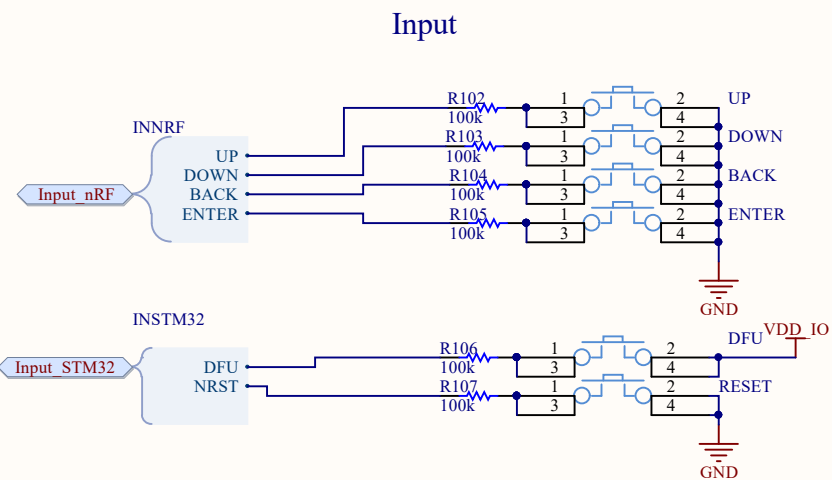
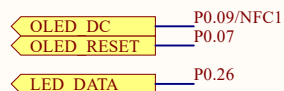
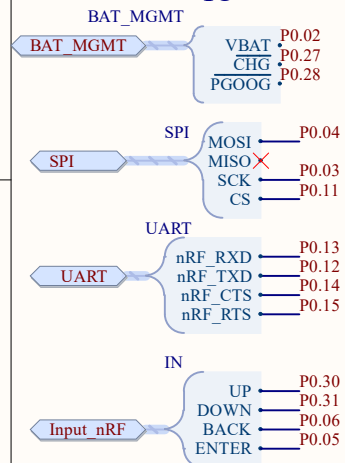


Title		
Size	Number	Revision
A4		
Date:	2018-03-10	Sheet of
File:	Badge.SchDoc	Drawn By:

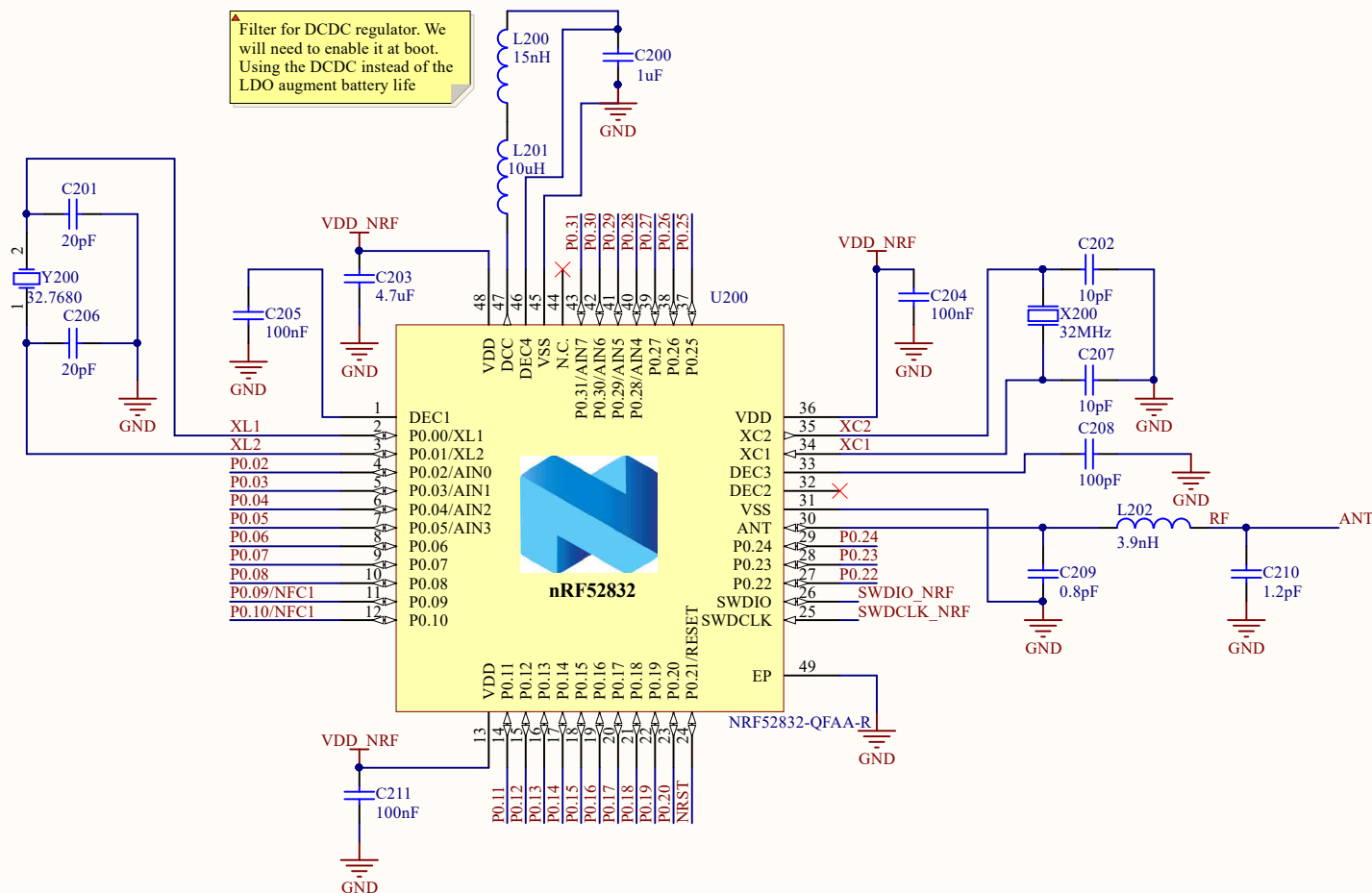


Title		
Size	Number	Revision
A4		
Date:	2018-03-10	Sheet of
File:	IO&Leds.SchDoc	Drawn By:

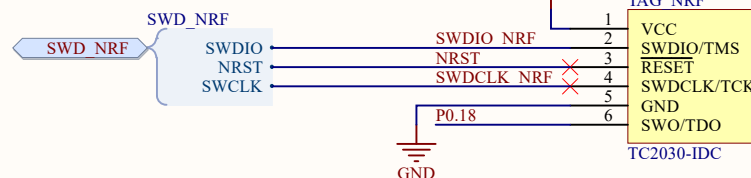
IO



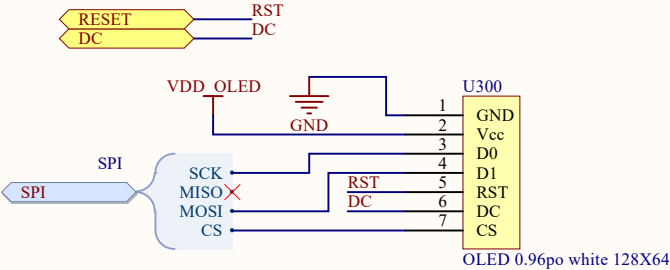
Filter for DCDC regulator. We will need to enable it at boot. Using the DCDC instead of the LDO augment battery life



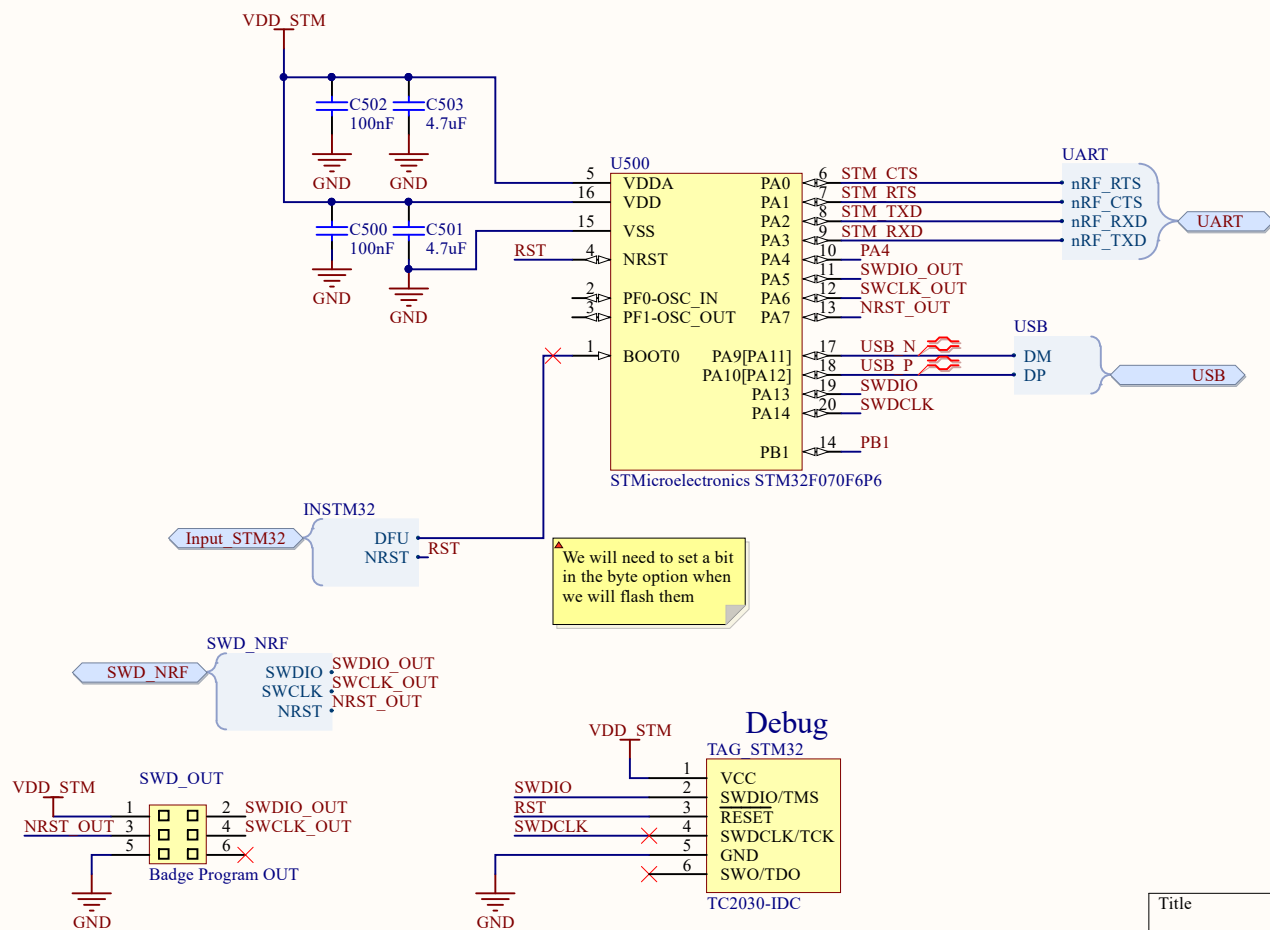
Debug



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Date:	2018-03-10	Sheet of
File:	OLED.SchDoc	Drawn By:



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Size	Number	Revision
A4		
Date:	2018-03-10	Sheet of
File:	STM32.SchDoc	Drawn By: