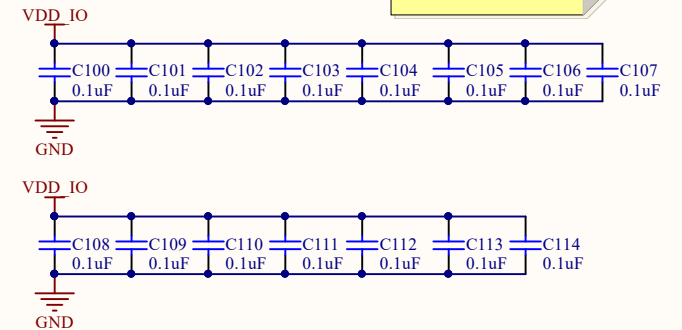
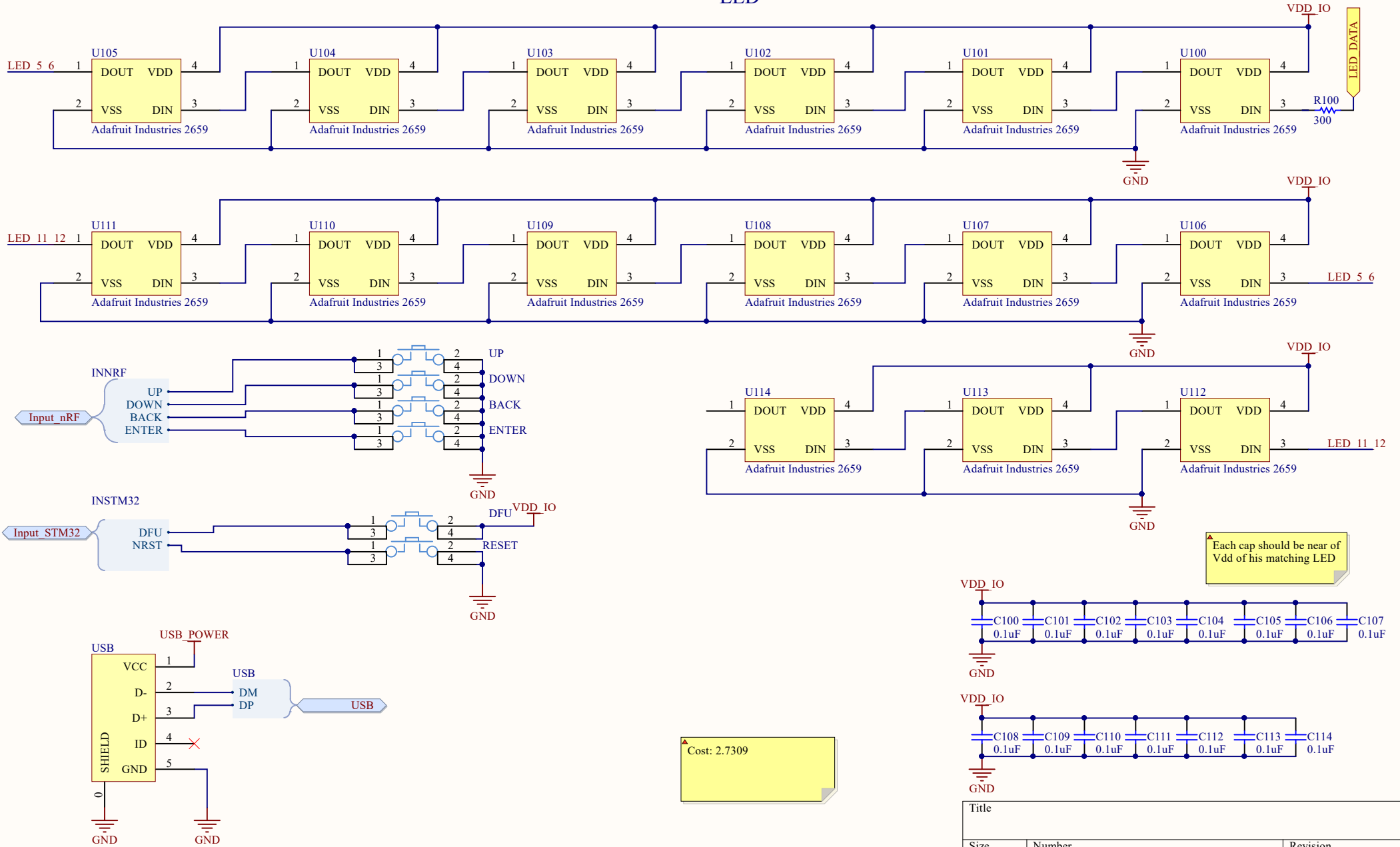
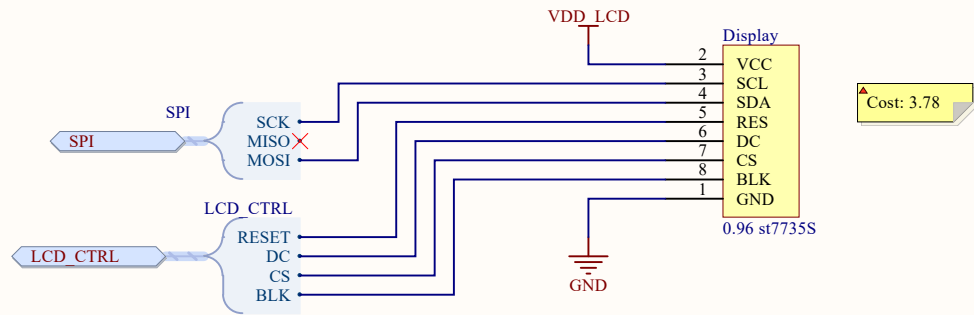


LED



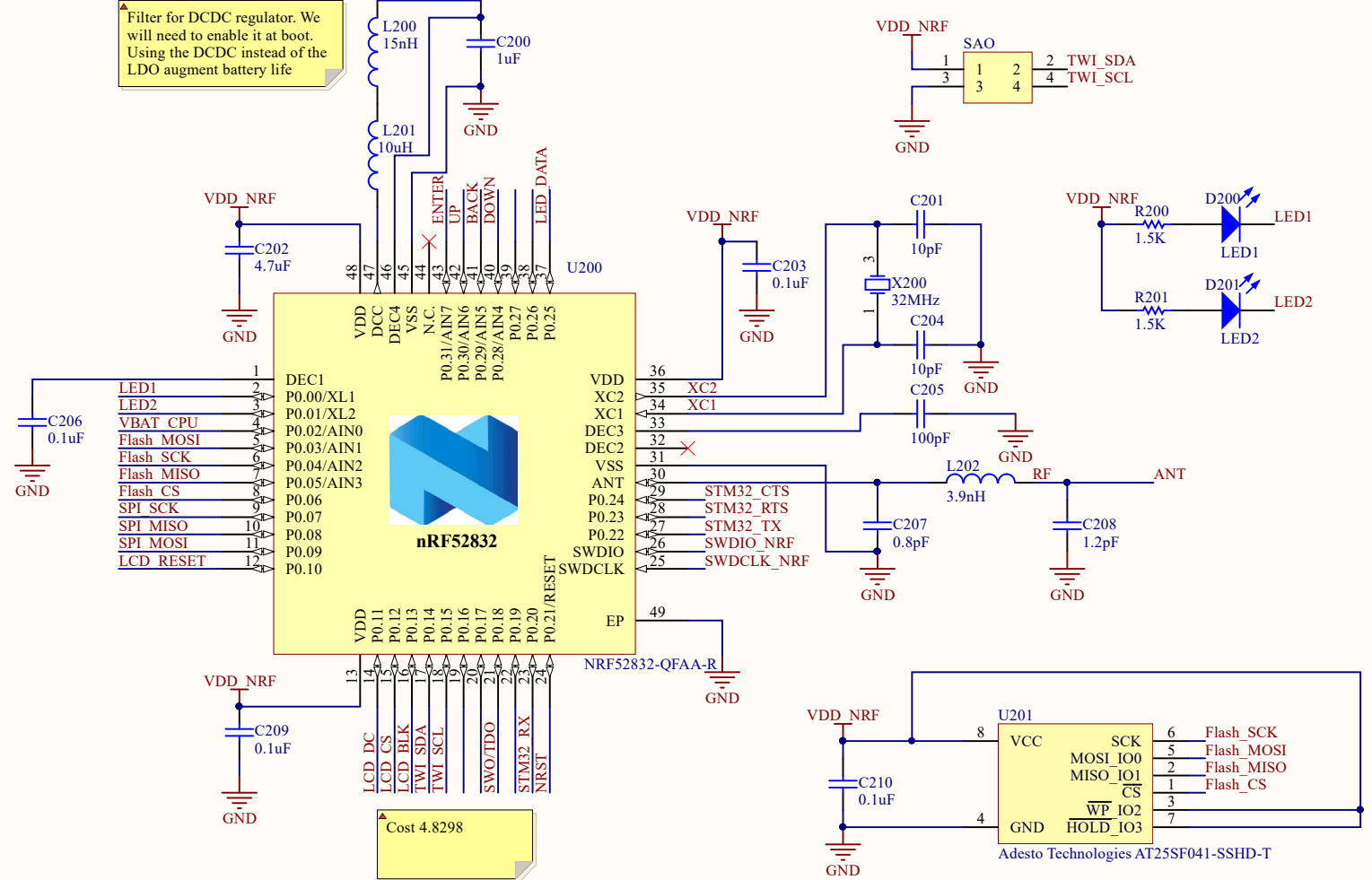
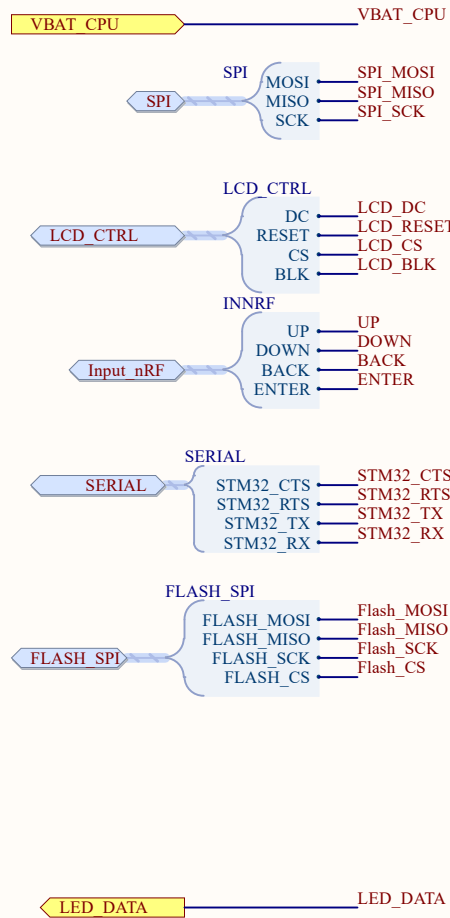
Title		
Size	Number	Revision
A4		
Date:	2019-05-26	Sheet of
File:	IO&Leds.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	2019-05-26	Sheet of
File:	LCD.SchDoc	Drawn By:

IO

▲ Filter for DCDC regulator. We will need to enable it at boot. Using the DCDC instead of the LDO augment battery life

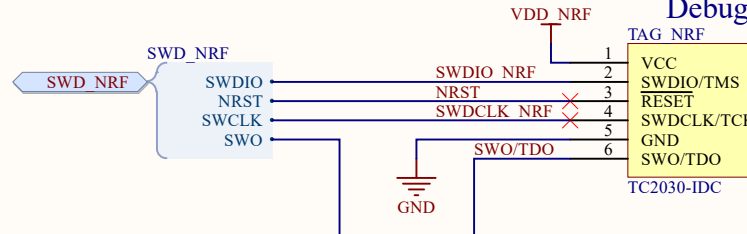


Cost 4.8298

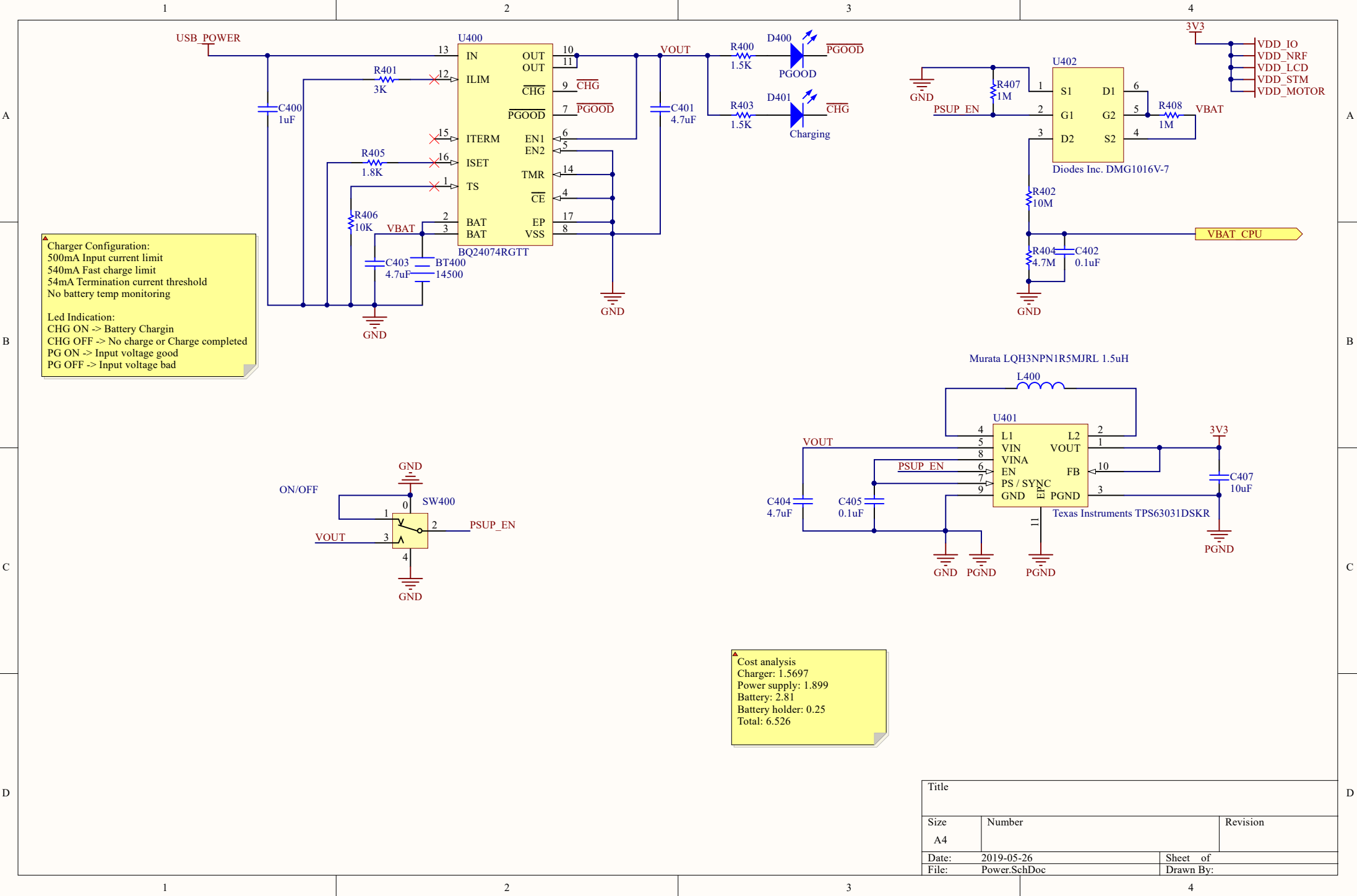
▲ With the high number of peripheral. We need to take care and avoid using two peripherals on the same memory mapping. Nordic call that instantiation. If two peripheral use the same ID they can't be use at the same time.

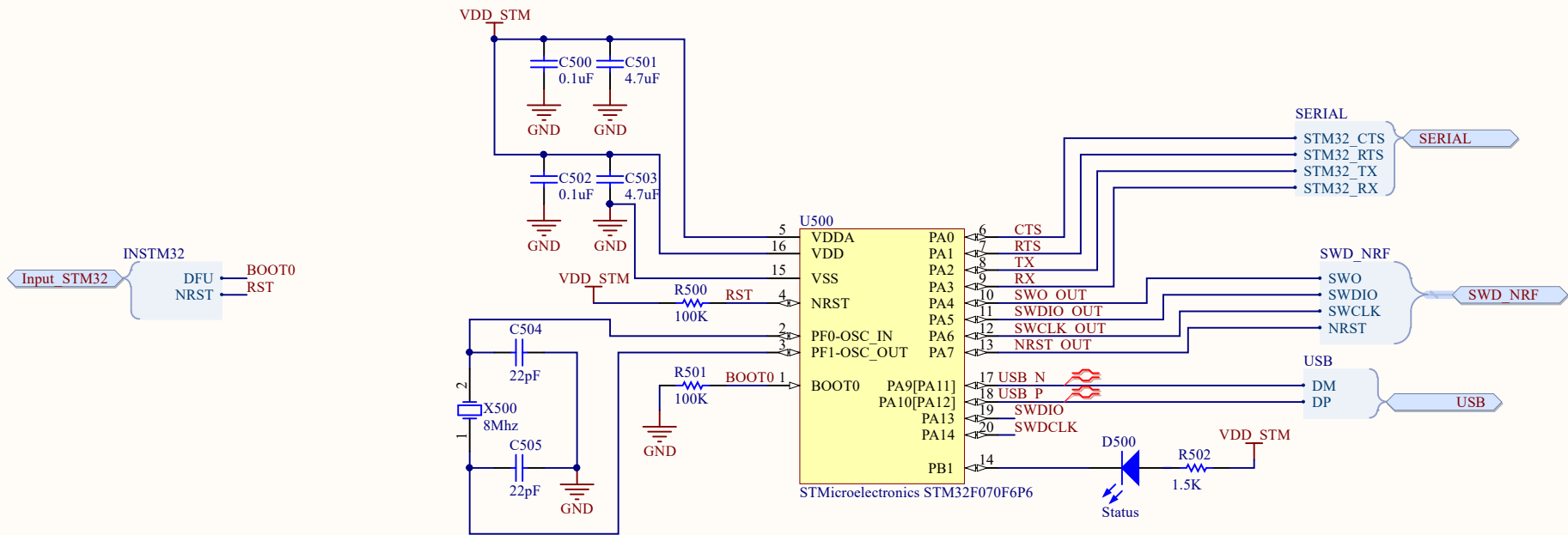
ID2 -> STM32 comm, UART0 or UART1
ID3 -> Flash SPI, SPIM0
ID4 -> SAO, TWI1
ID28 -> LED PWM, PWM0
ID34 -> LCD PWM, PWM2
ID35 -> LCD SPI, SPIM2

Debug

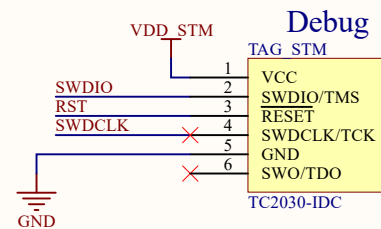


Title		
Size	Number	Revision
A4		
Date:	2019-05-26	Sheet of
File:	NRF.SchDoc	Drawn By:





Cost: 1.3181



Title		
Size	Number	Revision
A4		
Date:	2019-05-26	Sheet of
File:	STM32.SchDoc	Drawn By:

102,76

90,38

