

# Benchao Zhu

✉ bczhu22@m.fudan.edu.cn    📞 (+86) 15621546186

## RESEARCH INTEREST

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Electronic Design Automation

## EDUCATION

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### Fudan University

*M.E. in Electronics and Information Engineering*

– GPA: 3.46/4.0

– Rank: 14/160

Shanghai, CHN

September 2022 - June 2025 (expected)

### Fudan University

*B.E. in Microelectronics Science and Engineering*

– GPA: 3.50/4.0

– Rank: 24/146

Shanghai, CHN

September 2018 - June 2022

## AWARDS & HONORS

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- **Xiaomi Special Scholarship**, Fudan University (2023)
- **Outstanding Students**, Fudan University (2023)
- **National Encouragement Scholarship**, Fudan University (2019, 2020, 2021)

## SKILLS

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**Programming**      C++, LaTeX, Python, Perl, Shell  
**Languages**        Chinese, English

## COMPETITIONS

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2023 ISPD Contest (Security Closure of Physical Layouts)	First Place
2024 ISPD Contest (GPU/ML-Enhanced Large Scale Global Routing)	Fifth Place
2024 ICCAD Contest (Power and Timing Optimization Using Multibit Flip-Flop)	Fourth Place

## PUBLICATIONS

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- **Late Breaking Results: Mixed-Cell-Height Detailed Placement under Multi-Cell Spacing Constraints**  
Benchao Zhu, Zheng Zeng, and Jianli Chen  
*61th ACM/IEEE Design Automation Conference (DAC)*, 2024.
- **Effective Analytical Placement for Advanced Hybrid-Row-Height Circuit Designs**  
Yuan Wen, Benchao Zhu, Zhifeng Lin, and Jianli Chen  
*29th Asia and South Pacific Design Automation Conference (ASPDAC)*, 2024.
- **Mixed-cell-height Placement with Minimum-Implant-Area and Drain-to-Drain Abutment Constraints**  
Guohao Chen, Zheng Zeng, Benchao Zhu, Jiawei Li, Kun Wang, Jun Yu, and Jianli Chen  
*60th ACM/IEEE Design Automation Conference (DAC)*, 2023.

## SELECTED COURSES

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- **Postgraduate:**  
Discrete Mathematics & Optimal Decision (A), Digital Signal Processing VLSI Design (B)

- **Undergraduate:**

Analog Circuit (A), Fundamentals of Digital Logic (A), Signal and Communication System (A)