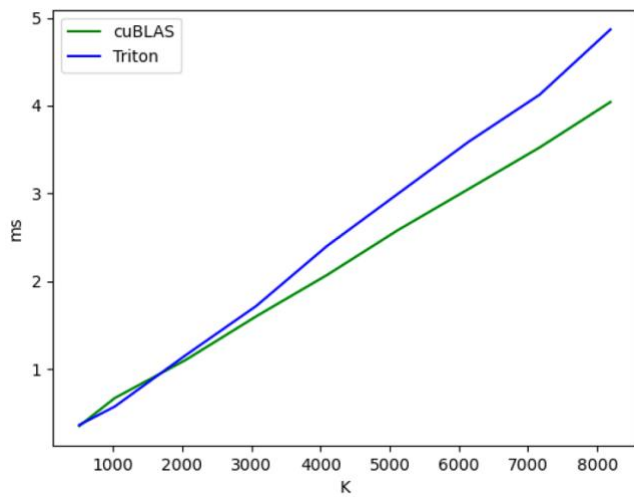
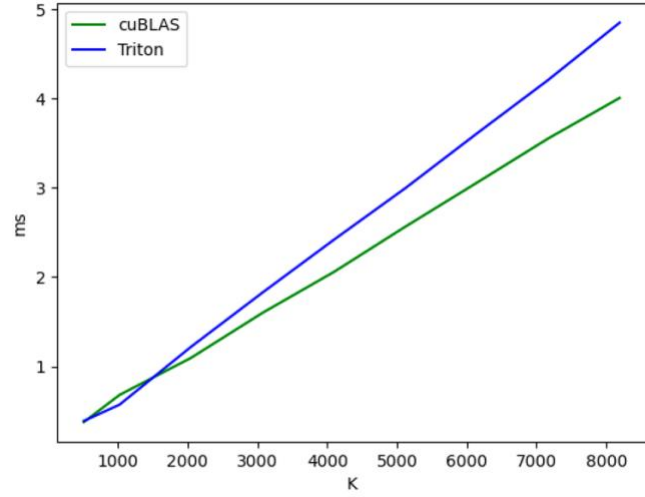


L2 Cache Patterns on A100

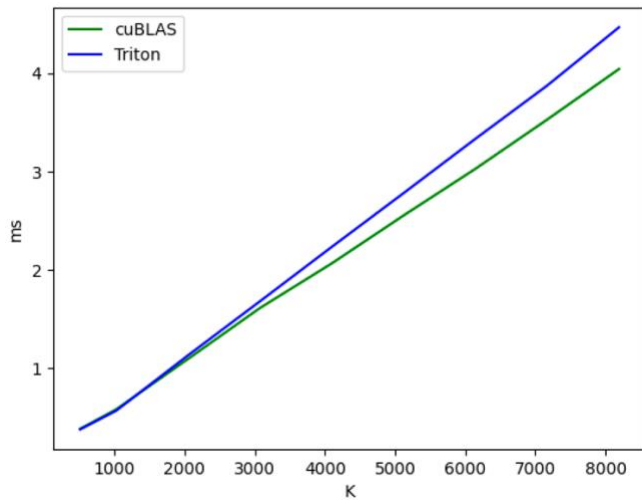
Fixed Auto-Tuner Configuration: BLOCK_SIZE_MNK=(128, 128, 32), num_warps=4, num_stages=4



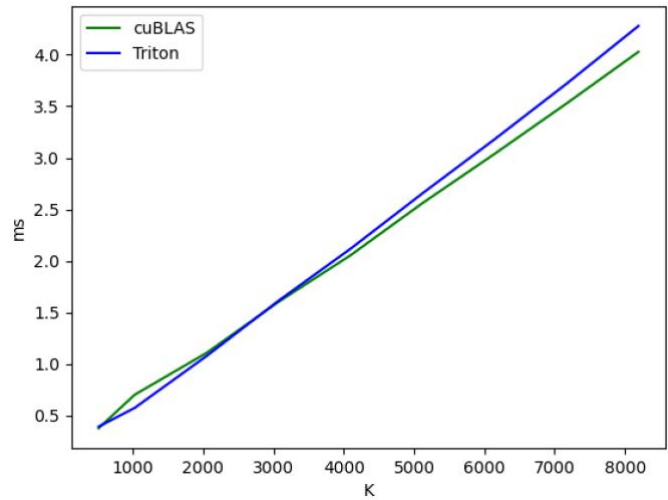
Row-major



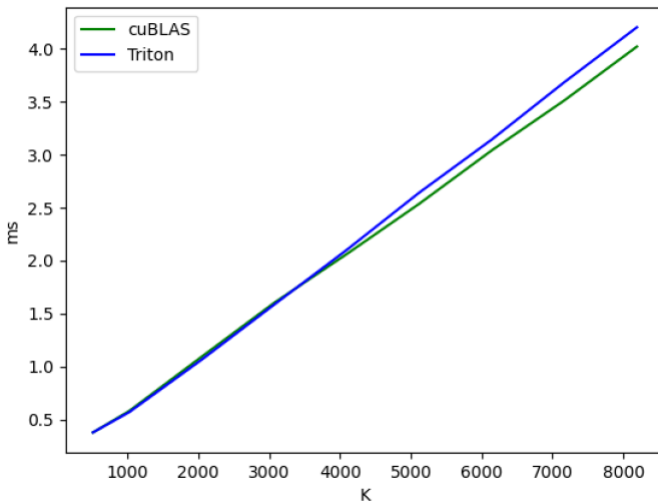
Column-major



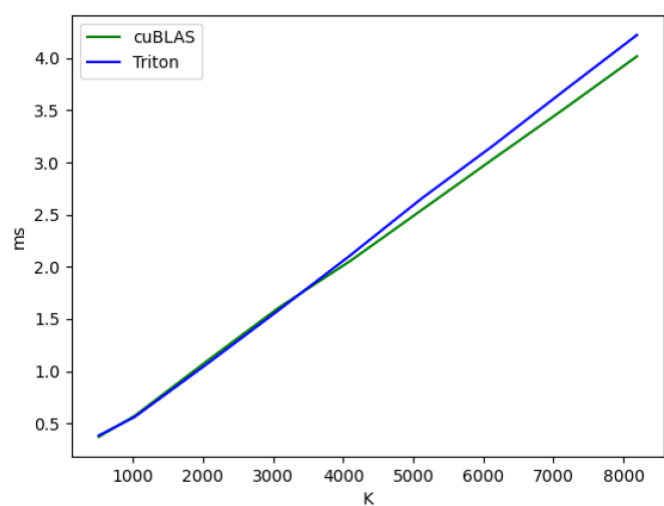
Tile size : 2



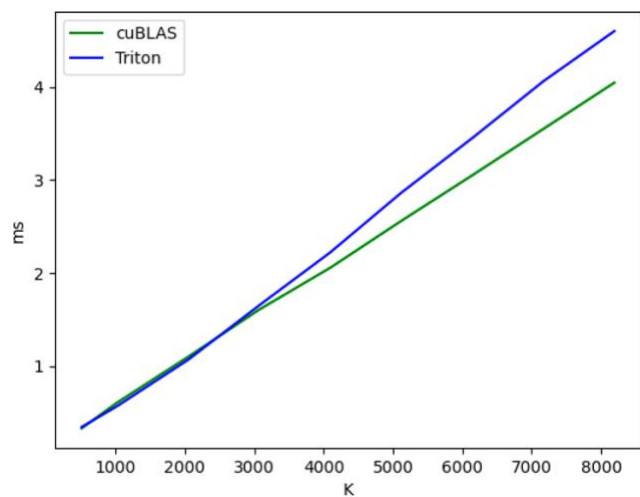
Tile size : 4



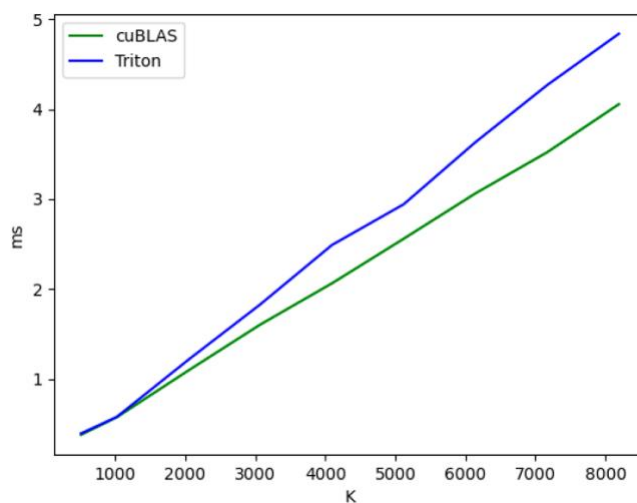
Tile size : 8



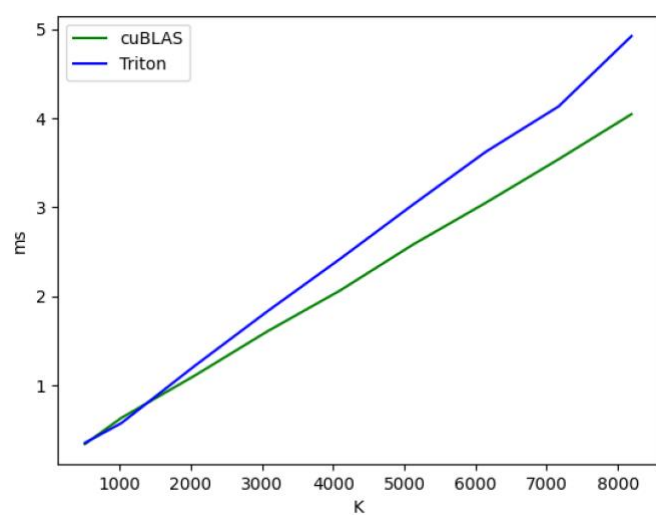
Tile size : 16



Tile size: 32



Tile size : 64



Tile size : 128