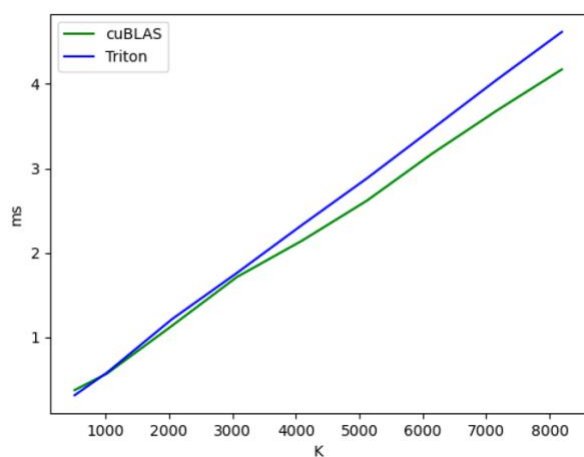
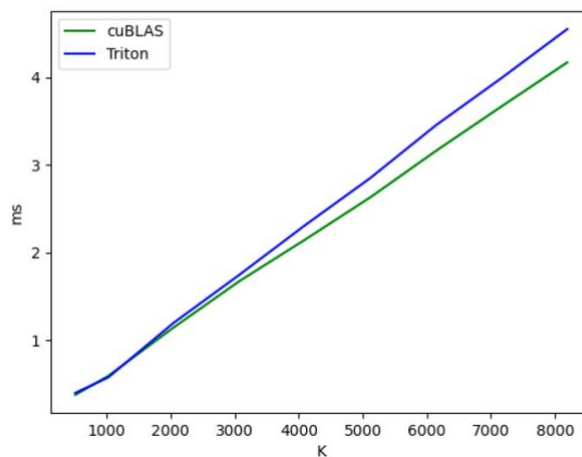


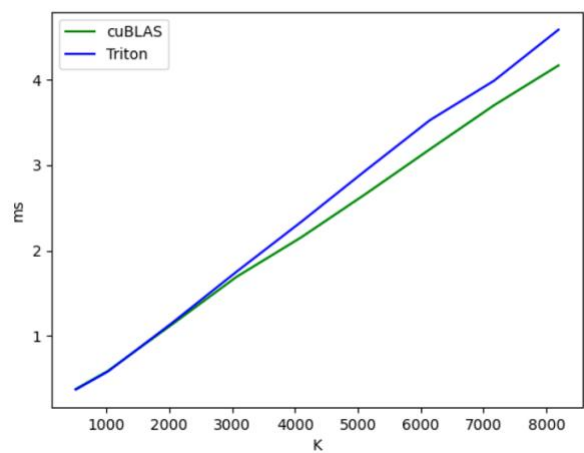
L2 Cache Patterns on A100



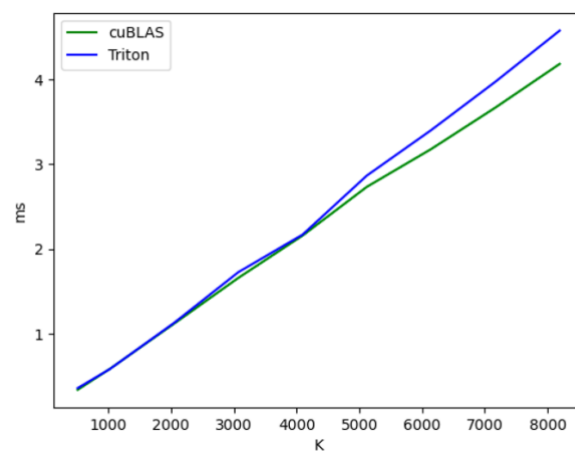
Row-major



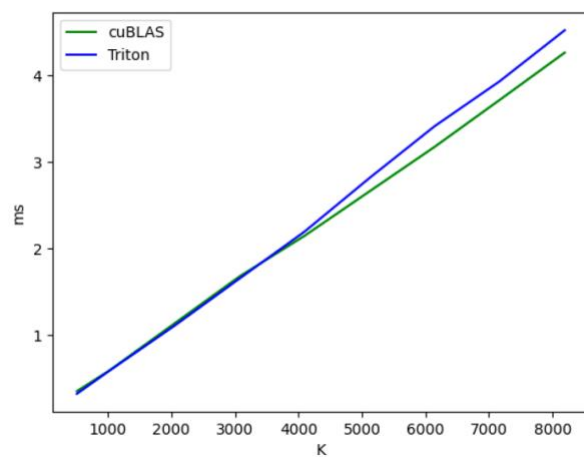
Column-major



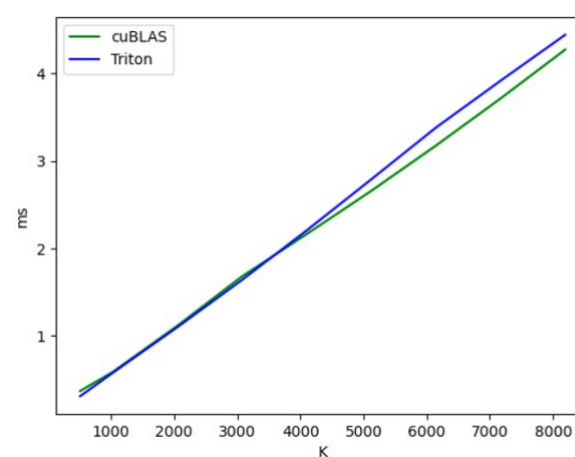
Tile size : 2



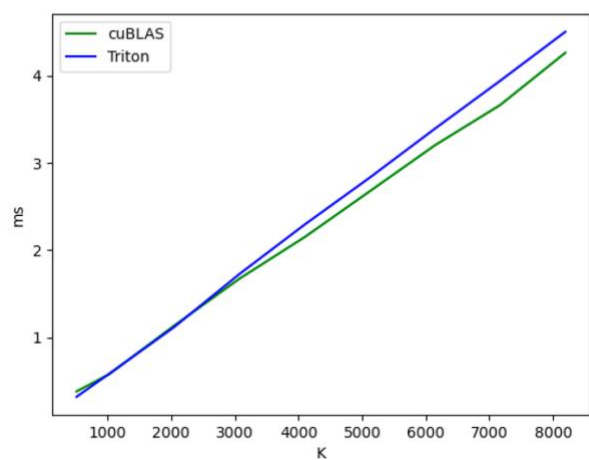
Tile size : 4



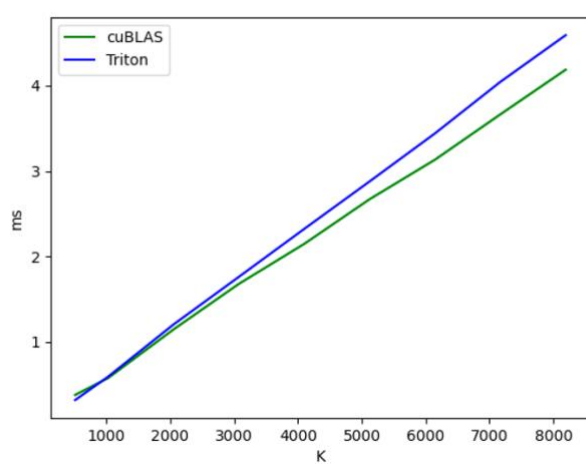
Tile size : 8



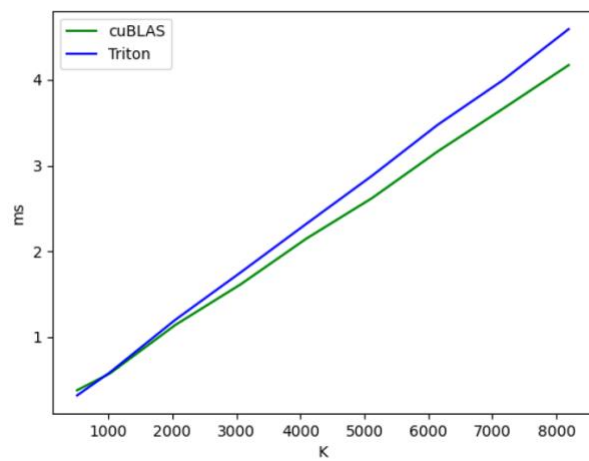
Tile size: 16



Tile size: 32



Tile size : 64



Tile size : 128