3.0V 50F (1840)



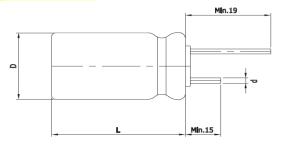
Features

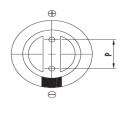
EDLC (Electric Double Layer Capacitor)

- High Power Density
- Over 500,000 cycle life (semi-permanent)
- Higher energy density compared with 2.7V caps
- RoHS compliant
- Long-term reliability improved at high temperature and humidity



Drawing





D (Φ)	18.0 +1.0 Max				
L (mm)	40.0 ±1.5				
d (Φ)	0.8 ±0.1				
P (mm)	7.5 ±0.5				

Specification

Item		Characteristics				
Product ser	ies	EDLC				
Rated Voltage	e (V _R)	3.0V				
Operating Temp	erature	-40 ~ +65°C				
Capacitance Tolerance		-10 ~ +30%				
High Temperat		After 1,000 hours at V_R loaded under +65°C, capacitors meet the following criteria.				
	ature	Capacitance Change	≤ 30% of initial value			
Load Life		ESR ≤ 2 times of specified value				
		85°C Temperature Max. 2.4V				
	Cycle	Over 500,000				
Cycle Life	ΔC	≤ 30% of initial value				
Characteristics	ESR	≤ 2 times of specified value				
	Method	Cycle of Charge/discharge from V_R to $1/2V_R$				
Shelf Life		3 Years No Electrical Charge, Temperature below 25°C (△C : ≤ 10% of initial value / △ESR : ≤ 50% of specified value)				

	Part Number	Rated Voltage	Capacitance	ESR (mΩ)		Max. Current	Leakage Current (mA, 72hr)		Size (mm)
		(V)	(F)	AC(1 kHz)	DC	(A)	@2.7V	@3.0V	DxL
	WEC 3R0 506 QG	3.0	50	13	20	37.0	0.100	0.150	18 x 40

^{*} Max. Current: 1 sec. discharge to 1/2V_R

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Web: www.vina.co.kr