

state	latency	residency
POLL	0 $\mu$ s	0 $\mu$ s
C1	2 $\mu$ s	2 $\mu$ s
C1E	10 $\mu$ s	20 $\mu$ s
C3	70 $\mu$ s	100 $\mu$ s
C6	85 $\mu$ s	200 $\mu$ s
C7s	124 $\mu$ s	800 $\mu$ s
C8	200 $\mu$ s	800 $\mu$ s
C9	480 $\mu$ s	5000 $\mu$ s
C10	890 $\mu$ s	5000 $\mu$ s