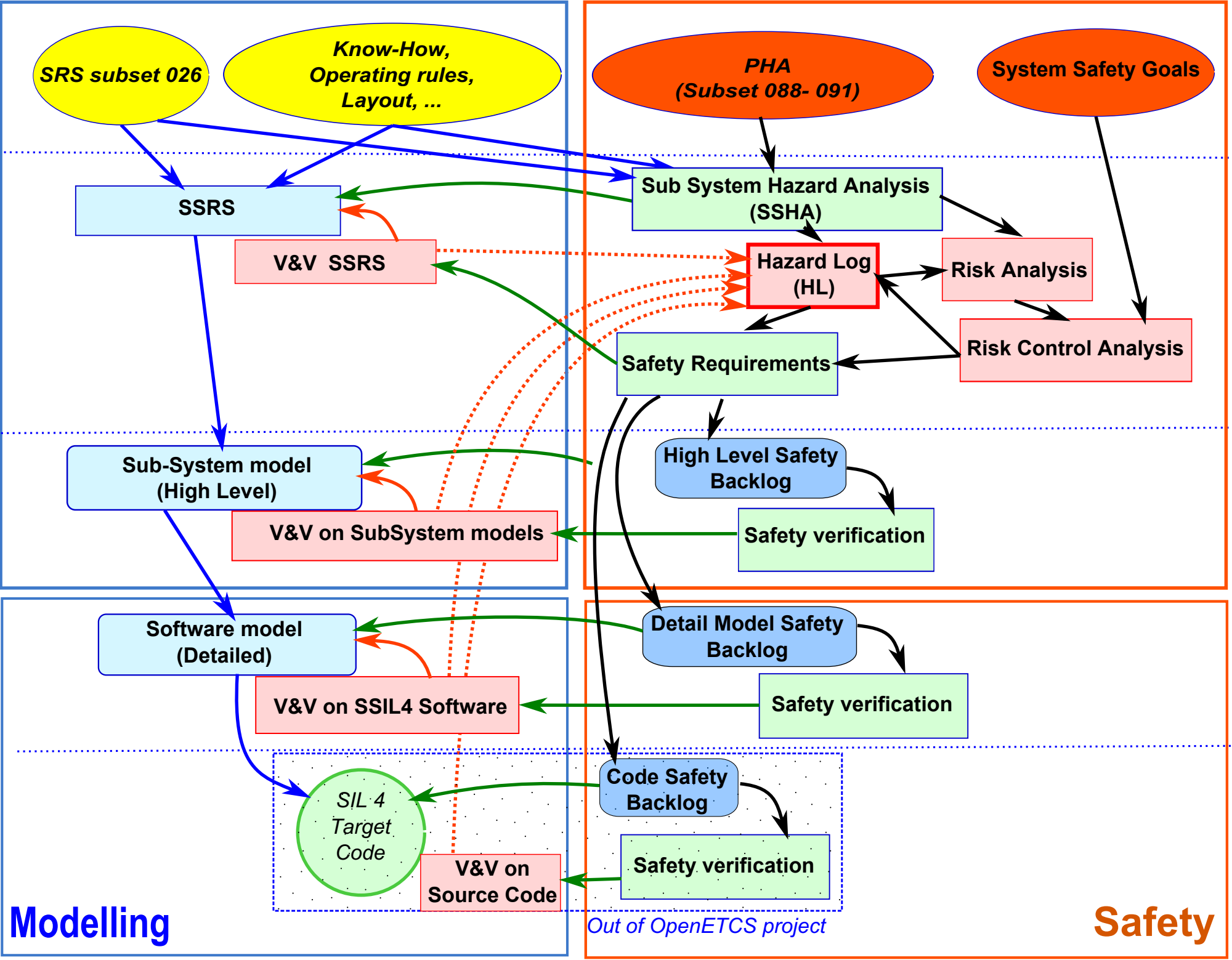


System phase

Software phase



OpenETCS inputs

System analysis

Sub-System formal design

Software design

Source code generation

Modelling

Out of OpenETCS project

Safety