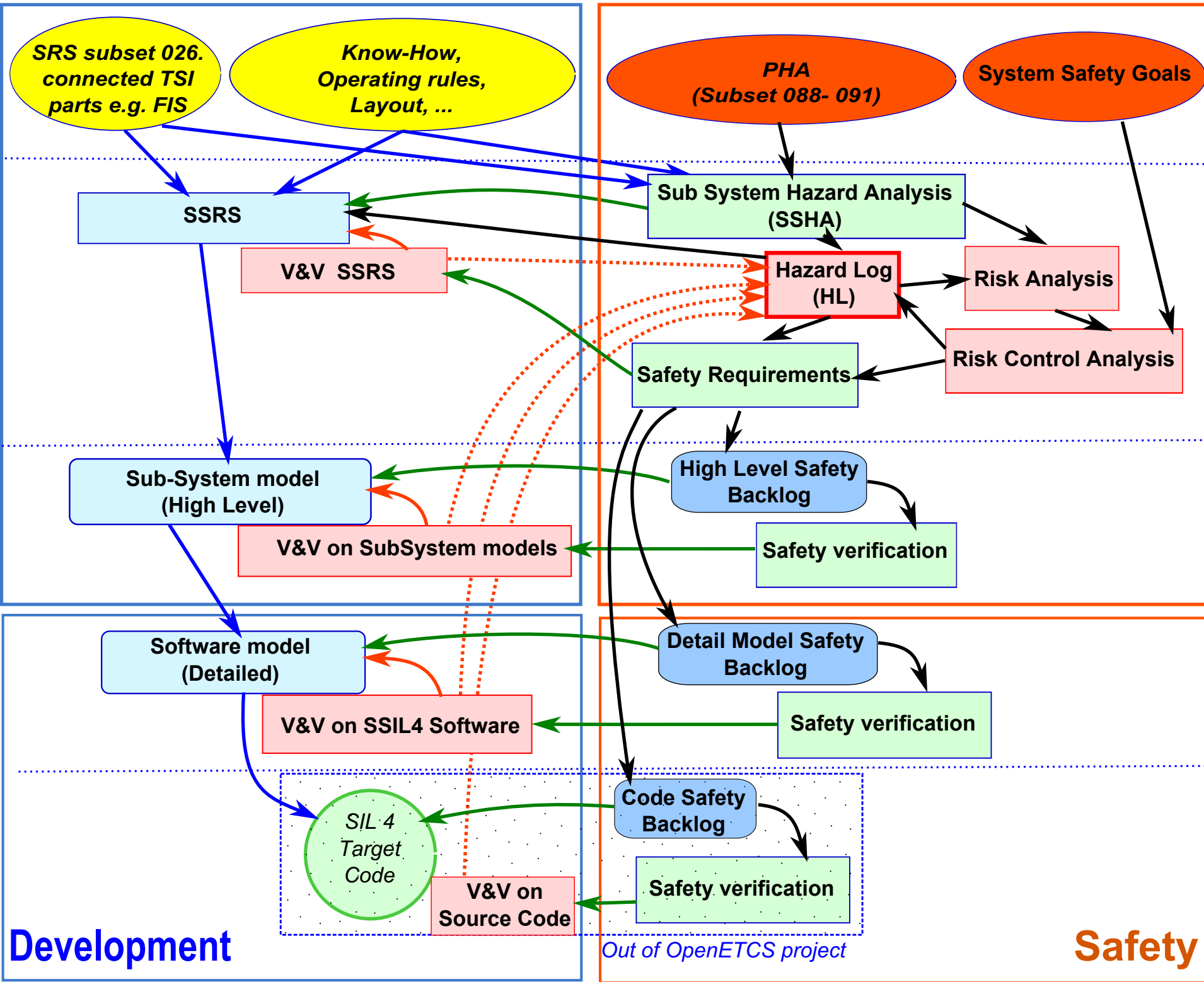


System phase

Software phase

Development



OpenETCS inputs

System analysis

Sub-System formal design

Software design

Source code generation

Design Process