





TWT GmbH Science & Innovation

SystemC and UPPAAL Two Verification Approaches

Frank Golatowski, Benjamin Beichler, Christian Haubelt (Uni Rostock)
Stefan Rieger (TWT)







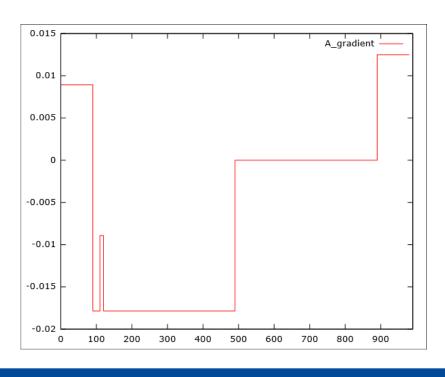


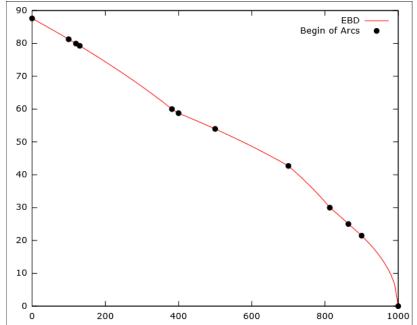


Verification/Testing with SystemC (Uni Rostock & TWT)

- Executable Model
- Integrated simulation kernel
- Real-time simulation
- Timing and performance analysis













Formal Verification of Timed Properties (TWT)

- Modelling time-critical system parts as timed automata
- Model checking TCTL properties
- Generation of counterexamples
- Counterexamples are test cases for the system

