ARM® Cortex®-M

32-bit Microcontroller

NuMicro® Family
NUC122 Series BSP
Revision History

For additional information or questions, please contact: Nuvoton Technology Corporation.

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Revision 3.00.003 (Released 2017-10-24)

1. Fixed PLL clock source selection bug in CLK_SetCoreClock().
2. Fixed clear Receive Line Status interrupt flag bug in UART_ClearIntFlag().
3. Disable debug message when enabling semihost without NuLink connection.
4. Added CLK_SysTickLongDelay() for long delay.

Revision 3.00.002 (Released 2015-07-24)

5. Fix the the reset vector handler to Reset_Handler of all sample codes.
6. Fix maximum USB endpoint from 8 to 6 in USB driver, because of NUC122 USBD Endpoint number is 6 only.
7. Fix UART transmit data bug in UART_TEST_HANDLE() of UART_TxRx_Function sample code.
8. Fix FMC_Erase() ISPFF flag clear to avoid ISP disable when error in FMC driver.
9. Remove APUEN enable or disable macro in FMC driver. NUC122AN doesn't support this function.
10. Remove ISPATA, VECMAP, UID, UCID from FMC driver, because they are not supported in NUC122.
11. Revise the following four macro definitions in SPI driver to avoid affecting another SPI_SS pin. SPI_SET_SS0_HIGH() SPI_SET_SS1_HIGH() SPI_SET_SS0_LOW() SPI_SET_SS1_LOW()
12. Update USBD driver for better compatibility
13. Add USBD_Billboard sample code to show the implementation of Billboard class.
14. Add Hard_Fault_Sample to show how to implement hard fault handler.
15. Add non-block printf supporting in retarget.c
16. Add UART FIFO size constants definitions with UART driver.
17. Add new function to control systick and select systick clock source, CLK_EnableSysTick() and CLK_DisableSysTick() in CLK driver.
18. Add UART_Wakeup sample code to demonstrate how to wakeup system by UART

Revision 3.00.001 (Released 2015-01-15)

1. Update to support new driver API

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