

# Séance

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## 1 Exercice 1

### 1.1 Question 1

## 2 Exercice 2

### 2.1 Question 1

### 2.2 Question 2

```
VMVI I,0  
VMVI D,1  
VLOAD B
```

```
VMVI D,2  
VLOAD C
```

```
VADD B,C
```

```
VMVI D,0  
VSTORE B
```

### 2.3 Question 3

```
MVI INX1 0  
MVI INX2 10  
VMVI I,0
```

```
boucle: VMVI D,10  
VLOAD B
```

```
VMVI D,20  
VLOAD C
```

```
VADD B,C
```

```
VMVI D,0  
VSTORE B
```

```
VADDI I,1
INC INX1
JLT INX1, INX1,boucle
```

## 2.4 Question 4

VMASK # où # est un masque de 64 bits, 64 étant le nombre de PE

```
VMASK
VMVI I,0
VMVI D,1
VLOAD B
```

```
VMVI D,2
VLOAD C
```

```
VADD B,C
```

```
VMVI D,0
VSTORE B
```

## 2.5 Question 6

```
MVI INX1 0
MVI INX2 6
```

```
VMVI R,0
```

```
VMVI I,0
VMVI D,0
VLOAD S
```

```
VMVI D,1
VLOAD A
```

```
VADD S,A
```

```
boucle:LCYCLE I
VADD S,R
VADDI I,1
INC INX1
JLT INX1, INX1,boucle
```



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