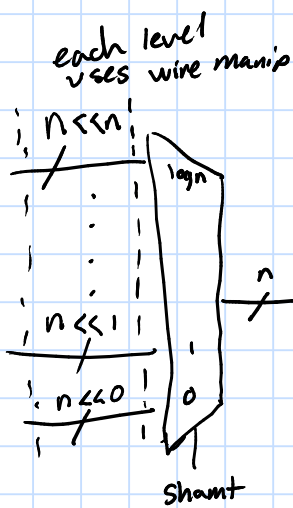


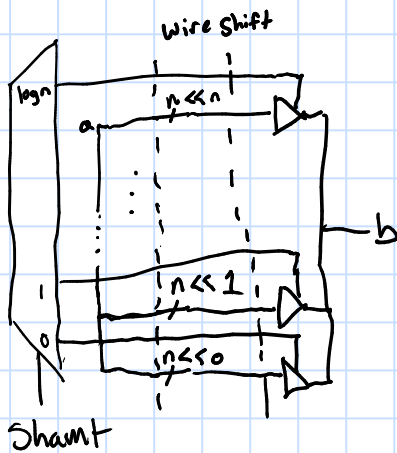
Comparch Hw6

SLL

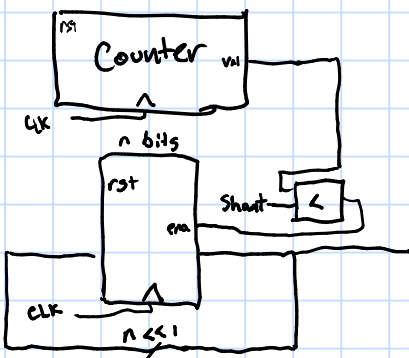
Idea 1



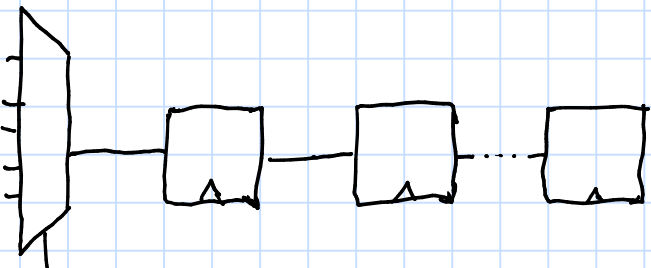
Idea #2 decoder to tristate



Idea #3 Sequential



Parallel to Serial



$$q[0:N-2] \leftarrow q[1:N-1]$$