Hardware. Rxer / tracking channel:

$$00 \to 0$$
$$10 \to 1$$
$$11 \to 0$$

$\frac{0.12}{9}$

△ | Signal-type-u-info

(Subcarr_bit = 0
  subcarr-replica-b)

code_subcarr (96-0) & (PRN_bit . xor ~~subcarr_bit~~)
                        0...1...0   subcarr_replica_b
                                            0
                  _____
                                    0   9

| very_early == early ✓

code_subcarr (96-0) & PRN_bit
   97   96:0   0   P
                          97   . 49   E   0
                               50:97   0:48

Code_subcarr

code_subcarr   PRN_bit

97. | next cycle   95:0 code_subcarr

                                    [1:0] PRN_bit

accm_lms_array. type:

                                      → [31:0]

                                      ∞

Code_chip_count_u.
        vs.
code_chip_count_lms_u
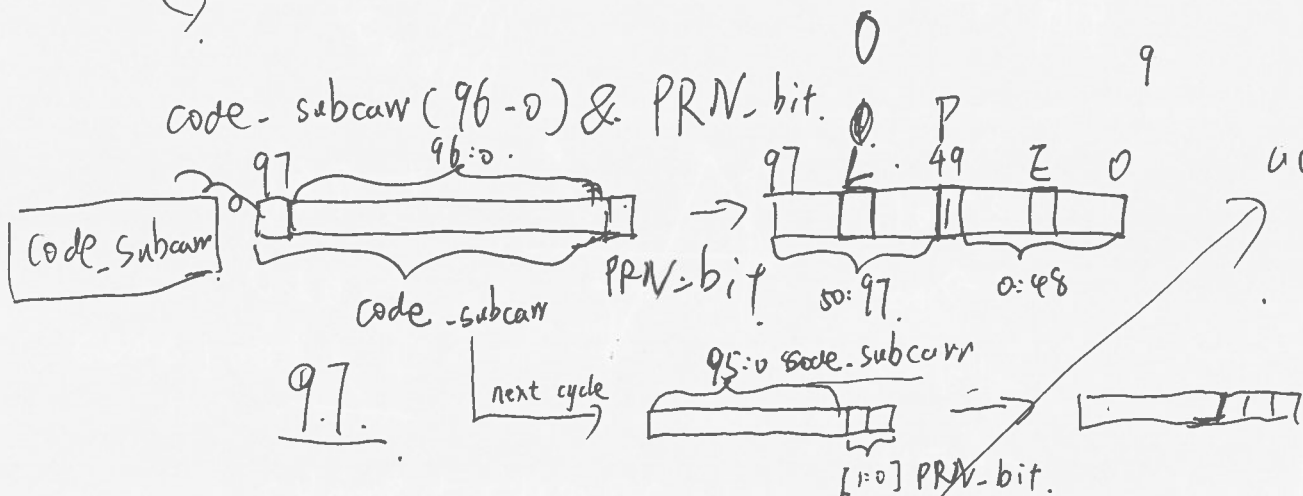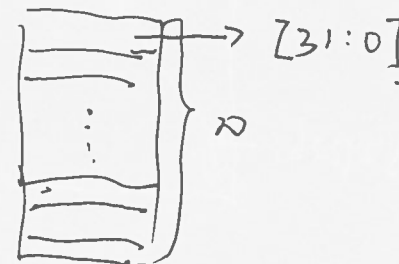
acc_lms_P_I_reg_a_s: accm_lms_array. type.
acc_lms_P_I_s: [5:2]
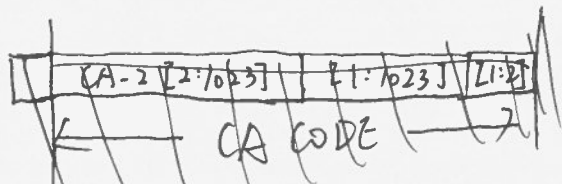code_epoch_lms_count_u : [4:0]

cc.

accumulation_P_I_s.
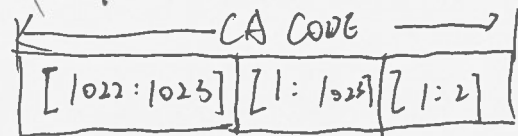accumulation_P_Q_s.

14580000
   2200.

$$\frac{f_i \times 2^{32}}{f_s} + f$$

$$\frac{(f_1 + f_2) \times 2^{32}}{f_s} \Rightarrow (f_1 + f_2) \cdot \frac{2^{32}}{f_s} = f_1 \frac{2^{32}}{f_s} + f_2 \frac{2^{32}}{f_s}$$

SW Rxey / run tracking:

2243

2.243 mS (skip:=162):

CA-2 [2:1023]   [1:1023]   [1:2]

CA CODE

1 1 1 0 0 1 0 0 0 0.

162-1
761

CA CODE

[1022:1023] [1:1023] [1:2]

X: 1 1 1 0 0 1 0 0 0 0 ....   99.315 MHz.

46500 + 162 - 1

$$\frac{16500.}{16321}.$$

99375 bit . 99375 bit