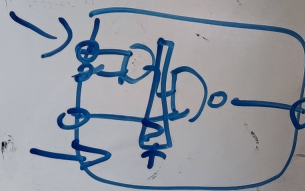


Sim → Behavioural + VHDL Functions (No Hardware)

→ Post Synthesis → Map to logic element + Pins

→ Post Layout

Chroscope
Hard work



Size
-50°C to +85°C
0.50
Timing
Power