

Standby power:

+3.3V_STB \rightarrow +3.3V_STB
+5V_STB \rightarrow +5V_STB

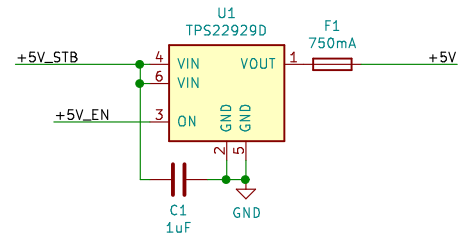
PSOne peripheral power (controlled by STM32):

+5V \rightarrow +5V
+7.5V \rightarrow +7.5V

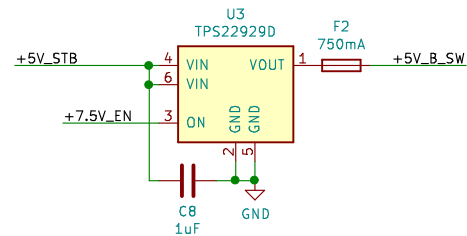
Control inputs from STM32

+5V_END \rightarrow +5V_EN
+7.5V_END \rightarrow +7.5V_EN

+5V power switch for PSOne peripherals

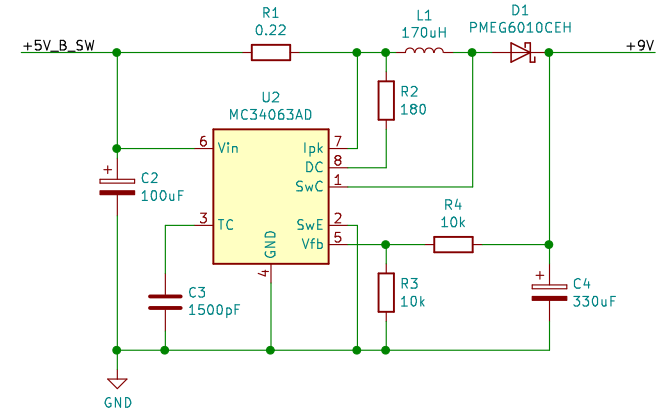


+5V power switch to +7.5V boost converter for cd drive and rumble



+9V boost converter

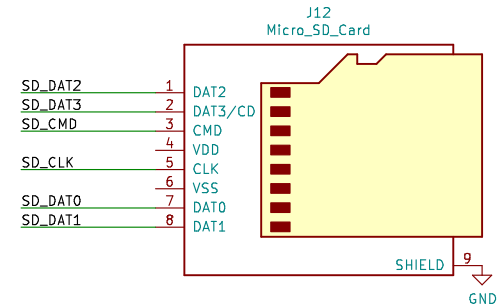
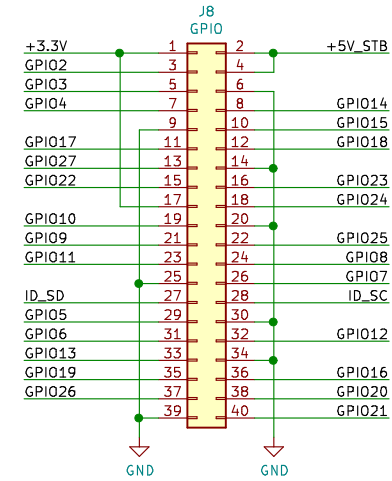
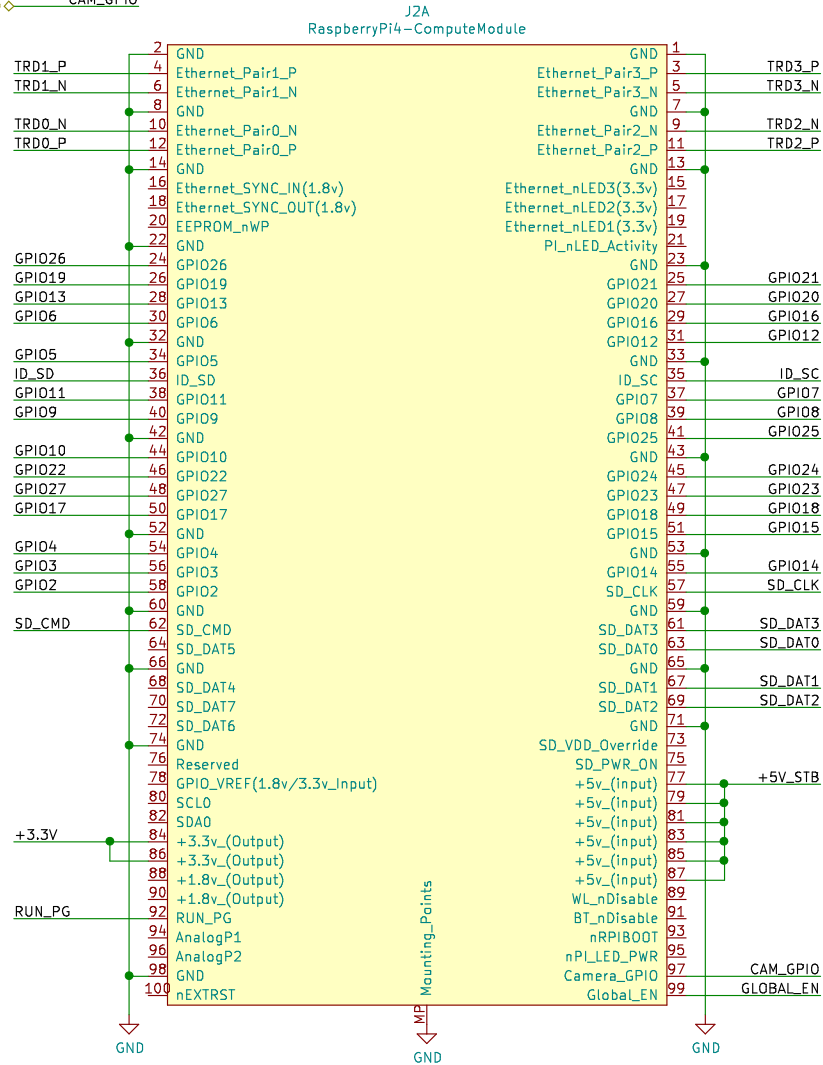
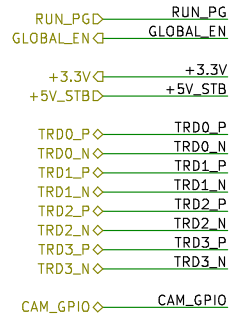
TODD: R3, R4 and output should be changed for 7.5V (Playstation 1 uses 9V, PSOne only 7.5V)



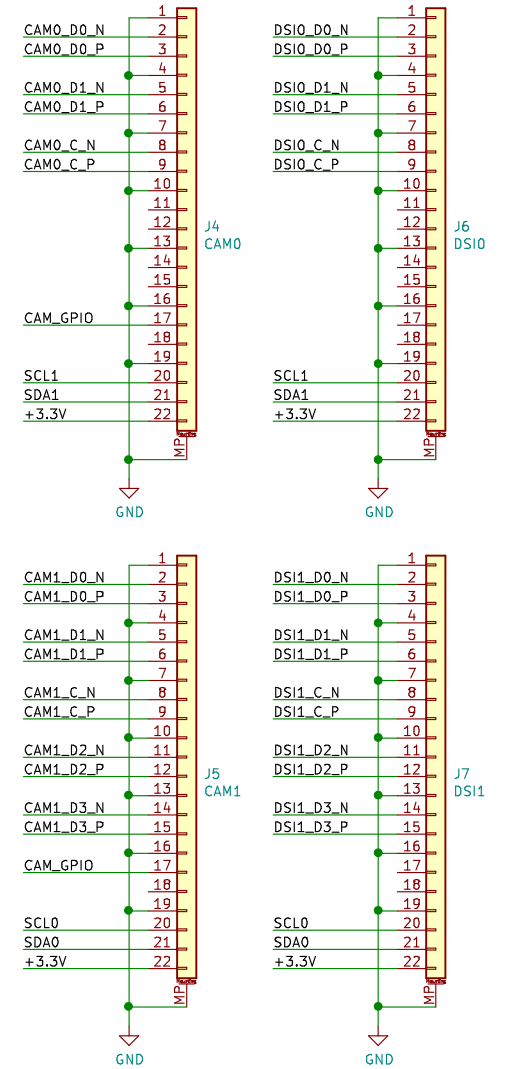
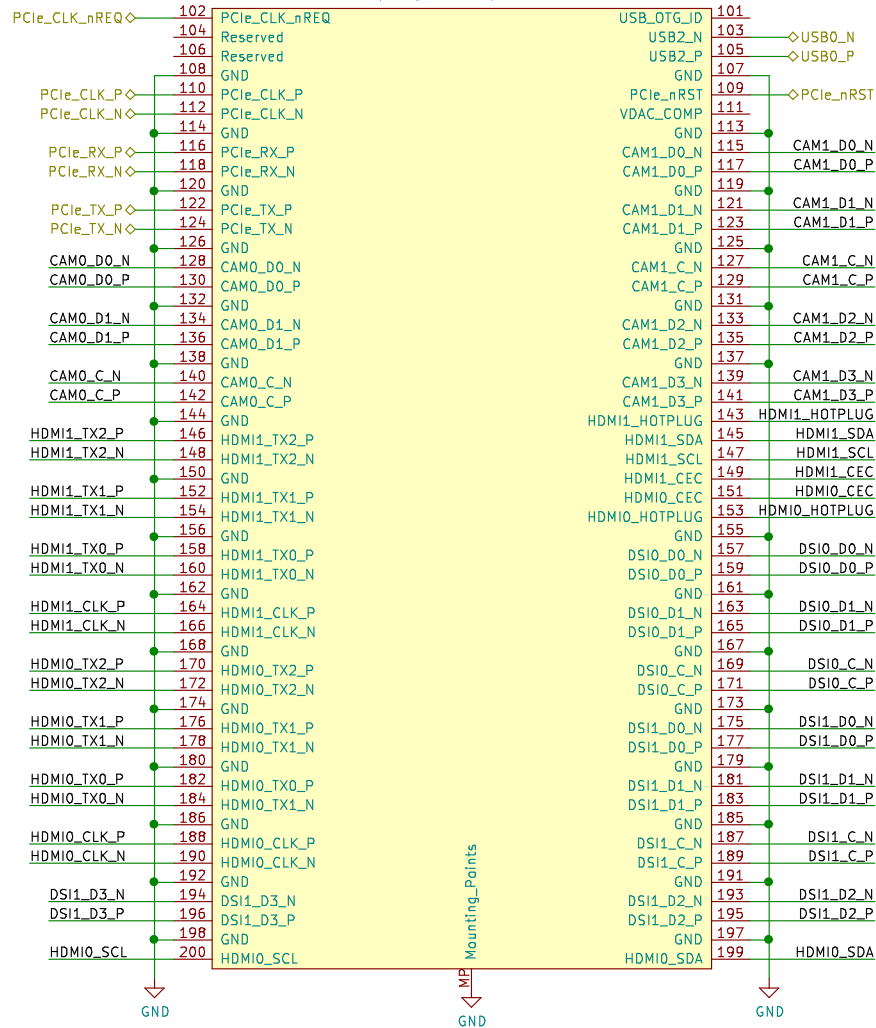
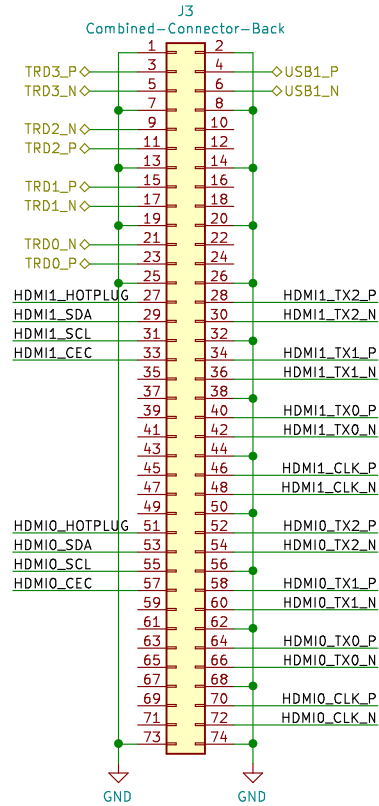
TODD: +5V_STB to +3.3V_STB converter for STM32F103 and eventually M.2
Or use the +3.3V from the Compute Module?

+5V_STB

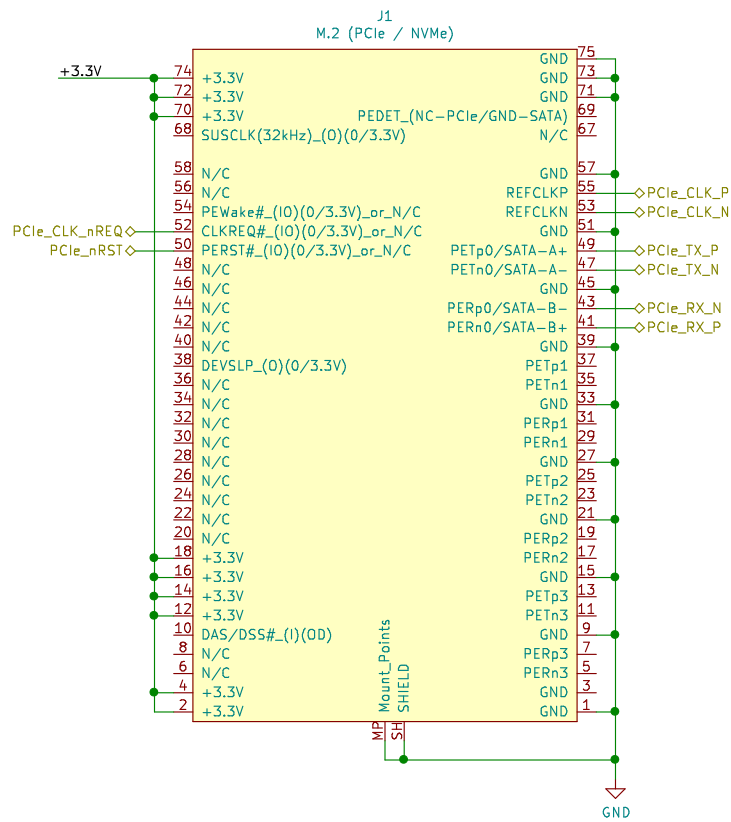
+3.3V_STB



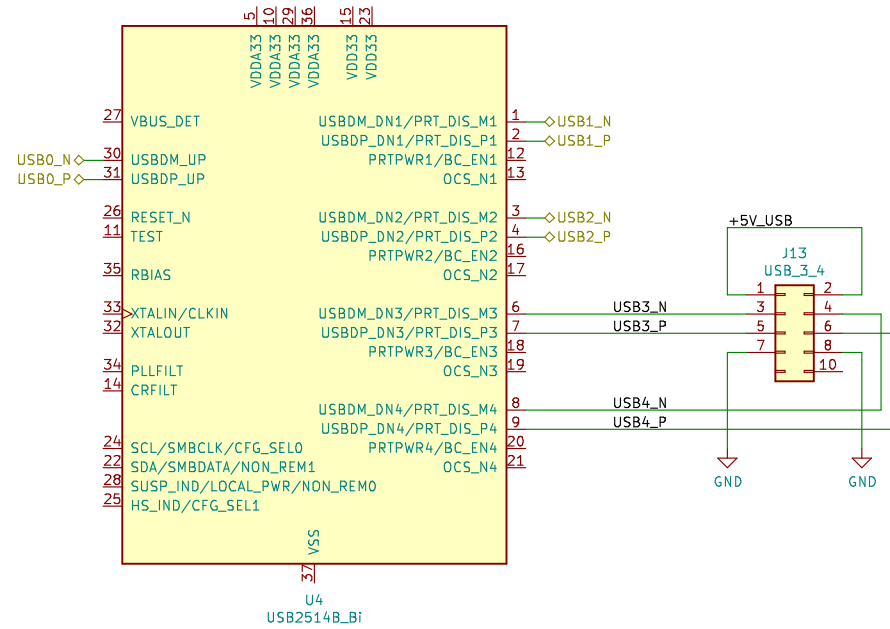
+3.3V \rightarrow +3.3V
+5V_STB \rightarrow +5V_STB
CAM_GPIO \rightarrow CAM_GPIO



+3.3V +3.3V



+3.3V  +3.3V



+3.3V_STB D +3.3V_STB
 +5V D +5V
 +7.5V D +7.5V
 +3.3V_CP D +3.3V_CP

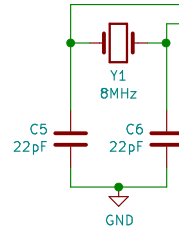
+5V_EN D +5V_EN
 +7.5V_EN D +7.5V_EN
 +3.3V_CP_EN D +3.3V_CP_EN

GLOBAL_EN D GLOBAL_EN
 RUN_PG D RUN_PG

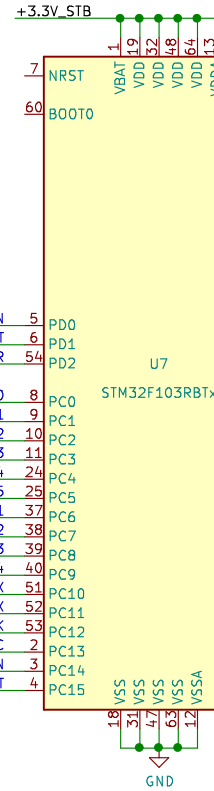
USB2_N D USB2_N
 USB2_P D USB2_P

CD_RD_AD D CD_RD_A
 CD_RD_B D CD_RD_B
 CD_RD_C D CD_RD_C
 CD_RD_D D CD_RD_D

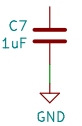
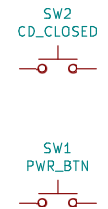
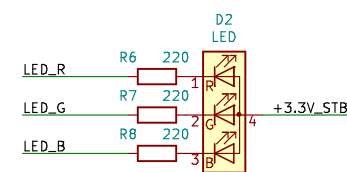
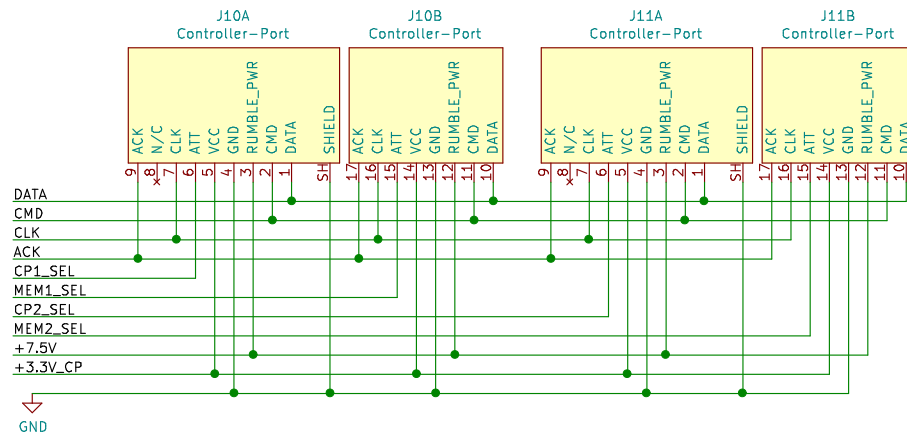
CD_FSC_A D CD_FSC_A
 CD_FSC_B D CD_FSC_B
 CD_TRC_A D CD_TRC_A
 CD_TRC_B D CD_TRC_B
 CD_OPT_A D CD_OPT_A
 CD_OPT_B D CD_OPT_B
 CD_DISK D CD_DISK
 PWM_FAN D PWM_FAN



OSC_IN 5
 OSC_OUT 6
 TIM3_ETR 54
 CD_RD_A ADC12_IN10 8
 CD_RD_B ADC12_IN11 9
 CD_RD_C ADC12_IN12 10
 CD_RD_D ADC12_IN13 11
 ADC12_IN14 24
 ADC12_IN15 25
 TIM3_CH1 37
 TIM3_CH2 38
 TIM3_CH3 39
 TIM3_CH4 40
 USART3_TX 51
 USART3_RX 52
 USART3_CK 53
 TAMPER-RTC 2
 OSC32_IN 3
 OSC32_OUT 4



PA0 14 WKUP/USART2_CTS/ADC12_IN0/TIM2_CH1_ETR PWR_BTN
 PA1 15 USART2_RTS/ADC12_IN1/TIM2_CH2 CD_DISK
 PA2 16 USART2_TX/ADC12_IN2/TIM2_CH3 CD_OPT_A
 PA3 17 USART2_RX/ADC12_IN3/TIM2_CH4 CD_OPT_B
 PA4 20 SPI1_NSS/ USART2_CK/ADC12_IN4
 PA5 21 SPI1_SCK/ADC12_IN5
 PA6 22 SPI1_MISO/ADC12_IN6/TIM3_CH1/TIM1_BKIN
 PA7 23 SPI1_MOSI/ADC12_IN7/TIM3_CH2/TIM1_CH1N
 PA8 41 USART1_CK/TIM1_CH1/MCO LED_B
 PA9 42 USART1_TX/TIM1_CH2 LED_G TX
 PA10 43 USART1_RX/TIM1_CH3 LED_R RX
 PA11 44 USART1_CTS/CANRX/USBDM/TIM1_CH4 USB2_N
 PA12 45 USART1_RTS/CANTX/USBDP/TIM1_ETR USB2_P
 PA13 46 JTMS/SWDIO +3.3V_CP_EN
 PA14 49 JTCK/SWCLK +5V_EN
 PA15 50 JTDI/TIM2_CH1_ETR/SPI1_NSS +7.5V_EN
 PB0 26 ADC12_IN8/TIM3_CH3/TIM1_CH2N TEMP
 PB1 27 ADC12_IN9/TIM3_CH4/TIM1_CH3N FAN
 PB2 28 BOOT1 ACK
 PB3 55 JTD0/TIM2_CH2/TRACESW/SPI1_SCK CLK
 PB4 56 JNTRST/TIM3_CH/SPI1_MISO DATA
 PB5 57 I2C1_SMBAL/TIM3_CH2/SPI1_MOSI CMD
 PB6 58 I2C1_SCL/TIM4_CH1/USART1_TX
 PB7 59 I2C1_SDA/TIM4_CH2/USART1_RX
 PB8 61 TIM4_CH3/I2C1_SCL/CANRX
 PB9 62 TIM4_CH4/I2C1_SDA/CANTX
 PB10 29 I2C2_SCL/USART3_TX/TIM2_CH3
 PB11 30 I2C2_SDA/USART3_RX/TIM2_CH4
 PB12 33 SPI2_NSS/I2C2_SMBAL/USART3_CK/TIM1_BKIN CP1_SEL
 PB13 34 SPI2_SCK/USART3_CTS/TIM1_CH1N MEM1_SEL
 PB14 35 SPI2_MISO/USART3_RTS/TIM1_CH2N CP2_SEL
 PB15 36 SPI2_MOSI/TIM1_CH3N MEM2_SEL



+5V — +5V
 +3.3V_STB — +3.3V_STB
 +3.3V_CP — +3.3V_CP
 +3.3V_CP_END — +3.3V_CP_EN

CD Sensor readings (ADC)

CD_RD_A — CD_RD_A
 CD_RD_B — CD_RD_B
 CD_RD_C — CD_RD_C
 CD_RD_D — CD_RD_D

Focus coil PWMs

CD_FSC_AD — CD_FSC_A
 CD_FSC_B — CD_FSC_B

Tracking coil PWMs

CD_TRC_AD — CD_TRC_A
 CD_TRC_B — CD_TRC_B

CD laser/sensor sled motor PWMs

CD_OPT_AD — CD_OPT_A
 CD_OPT_B — CD_OPT_B

Disk spin motor PWM

CD_DISK — CD_DISK

PWM_FAN — PWM_FAN

