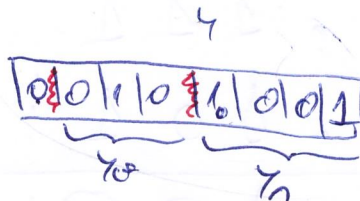


$z_c = 4$



$x \rightarrow x'$  (1)  
 $x + y = -x' + y$   
 $x', y \geq 0$

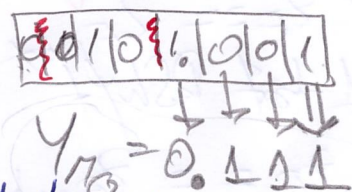
Recall  $-10 \equiv +6$   
 $1010 \xrightarrow{C_3} 0110 = 6$

Step 3 if  $\text{sign}(x) \neq \text{sign}(y)$  Complement  $y_n$  of 2

- only  $y_n$  has 2's complement capabilities

$(-x') + y$        $x' = \text{Comp of } x$   
 $x + y \rightarrow x - y$

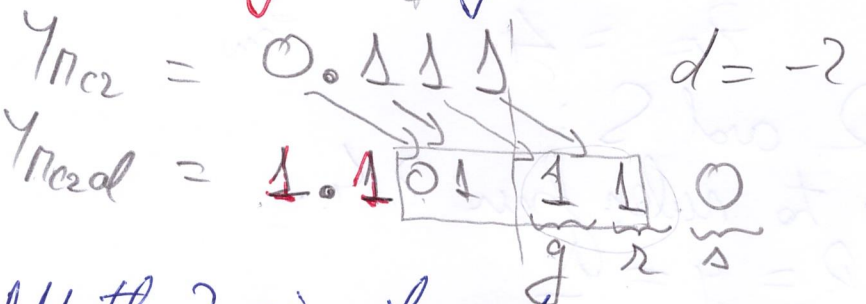
$\left. \begin{array}{l} \text{sign}(x) = 1 \quad (-) \\ \text{sign}(y) = 0 \quad (+) \end{array} \right\} \Rightarrow \text{Complement } y_n$



Step 4 Align  $y_n$  by RShift with 1st bits.

$\Rightarrow$  if  $y_n$  was complemented of 2 in Step 3, while RShifting, introduce bits of 1s into 17's mob

! preserve sticky bits:  $g, r, \Delta$



Step 5 Add the 2 significands

- if  $\text{sign}(x) = \text{sign}(y)$   
 - preserve Carry if generated

- if  $\text{sign}(x) \neq \text{sign}(y)$

- if no carry was generated

$\Rightarrow z_n$  is negative  $\Rightarrow$  it needs to be Complemented of 2

- if carry was generated

$\Rightarrow z_n$  is positive  $\Rightarrow$  carry is ignored

$$\begin{array}{r}
 X_n = 1.111 \\
 Y_{\text{real}} = 1.101 \mid \overset{g}{1} \overset{2}{1} \overset{1}{0} \mid + \\
 \hline
 Z_n = \cancel{1}.1000 \mid 110
 \end{array}$$

*cant*  $Z_n$  is positive

### Step 6 Prenormalisation

- according to rules from 2.4.  
 → determine  $z_{rn}$ , update  $z_e$

- **Exception checking:** (no of bits of  $z_e$ )

- if  $z_e == z_{e_{max}}$  ( $2-2=6$ ) and  $Z_n$  requires 1 bit RShift  $\Rightarrow$  **Overflow**

- if  $z_e == z_{e_{min}}$  ( $1$ ) and  $Z_n$  requires 1 or more bits LShift  $\Rightarrow$  **Underflow**

$$Z_n = \cancel{0}.1100 \mid \overset{g}{1} \overset{2}{1} \overset{1}{0}$$

$$\Rightarrow \text{Rule 2) } \rightarrow Z_{nn} = 1.100 \quad z_e = 4$$

$z_{\text{low}}$

### Step 7 Calculate R and S:

- according to rules from 2.4.

$$\text{Rule 2) } \rightarrow R = g = 1$$

$$S = r \oplus s = 1 + 0 = 1$$

### Step 8 Round the $Z_{nn}$ selected.

- according to the IEEE 754 rounding mode.

- according to the rounding rules from 2.4.

- if rounding generates carry out  $\Rightarrow$  **postnormalization**

**Postnormalization:**

-  $Z_n^*$  is RShifted by 1 bit

-  $z_e++$



Pre-normalization  $\rightarrow$  exception checking (like in Step 6) <sup>②</sup>  
 if  $z_n = z_{max} (=6)$  and  $z_n^*$  requires 1-bit Shift  
 $\Rightarrow$  **Overflow**

- Consider the round to nearest even mode.

if (R and (S or  $z_{om}$ )) then  $z_{nn} + 1$

$$R = 1, S = 1, z_{om} = 0.$$

$$R \cdot (S + z_{om}) = 1 \cdot (1 + 0) = 1 \Rightarrow z_{nn} + 1$$

$$z_{nn} = 1.1001$$

$$z_n^* = 1.101 \Rightarrow \text{no carry generated} \Rightarrow z_n \text{ not modified.}$$

Stop 9 Calculate sign of result.

- if  $\text{sign}(x) = \text{sign}(y) \Rightarrow \text{sign}(z) = \text{sign}(x)$   
 - if  $\text{sign}(x) \neq \text{sign}(y) \Rightarrow$

	Swap (Step 2)	Complement (Step 5)	sign(x)	sign(y)	sign(z)
$y > x$	YES		+	-	-
	YES		-	+	+
$x > y$	NO	YES	+	-	-
	NO	YES	-	+	+
	NO	NO	+	-	+
	NO	NO	-	+	-

Swap (Step 2): YES  $\Rightarrow \text{sign}(z) = \text{sign}(y)$  before the swap  
 $\text{sign}(z) = 1 (-)$

Stop 10 Pack the result

$$z: \boxed{1 \ 1 \ 0 \ 0 \ 1 \ 0 \ 1}$$

Verify the result:

$$X = 0.5625$$

$$Y = -3.75$$

$$X + Y = -3.1875 \text{ (infinite precision)}$$

$$Z: \boxed{1 \ 1 \ 1 \ 0 \ 1 \ 0 \ 1 \ 1}$$

$$n_{\text{sig}} = 4$$

$$\text{bias} = 2^{e-1} - 1 = 3$$

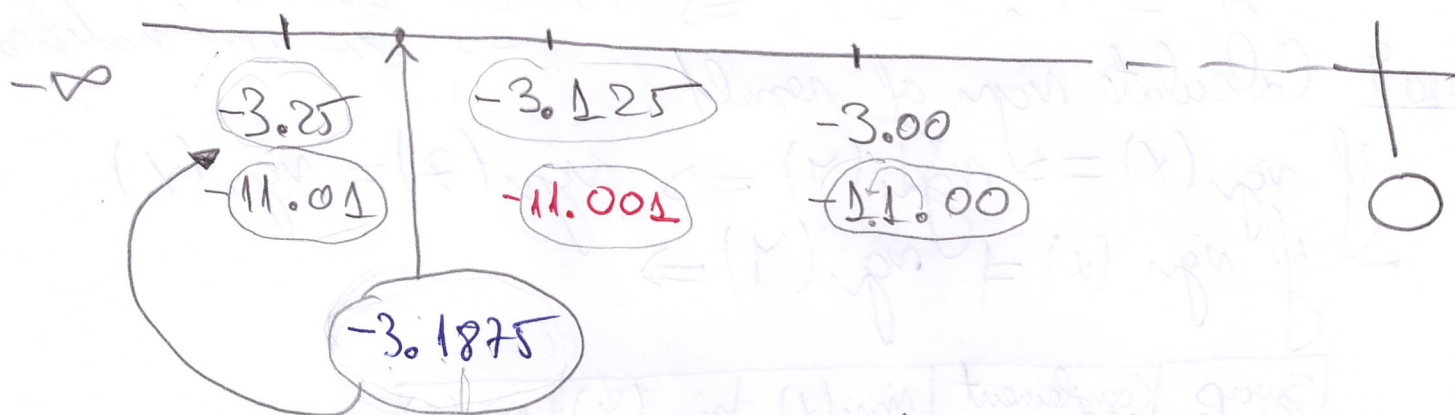
$$z_E = 100 = 4$$

$$z_n = .101$$

$$Z = (-1)^{n_{\text{sig}}} \times 2^{z_E - \text{bias}} \times (1.z_n)$$

$$= (-1) \times 2^{4-3} \times (1.101_2)$$

$$= -11.01_2 = -3.25$$



Design of the normalisation shifts.

- covers Step 6 and Step 7

$\Rightarrow$  according to the rules from 2.4.

- purely combinatorial.

From Step 5

$$z_n = \overbrace{z_4 \ z_3 \ z_2 \ z_1 \ z_0}^{\text{const}} | g \ r \ s$$

Output of Step 6 and Step 7

$$z_n = 1 . z_{n_m} z_{1_m} z_{0_m} | R \ S$$



# Normalization cases:

$$z_{2m} = 1 \cdot \begin{matrix} z_2 \\ z_1 \\ z_0 \end{matrix} \begin{matrix} z_1 \\ z_0 \end{matrix} \begin{matrix} z_0 \end{matrix} \mid \begin{matrix} R \\ S \end{matrix} \quad (3)$$

$$\begin{matrix} 1 \cdot z_2 & z_1 & z_0 & \mid & g & (gear) \\ 1 \cdot z_1 & z_0 & g & \mid & r & s \\ 1 \cdot z_0 & g & 0 & \mid & 0 & 0 \\ 1 \cdot g & 0 & 0 & \mid & 0 & 0 \\ 1 \cdot z_3 & z_2 & z_1 & \mid & r_0 & (gear) \end{matrix}$$

- A)  $z_1$  is normalized  $(l/r_0)$
- B)  $z_1$  needs 1-bit LShift  $(l_1)$
- C)  $z_1$  needs 2-bit LShift  $(l_2)$
- D)  $z_1$  needs 3-bit LShift  $(l_3)$
- E)  $z_1$  need 1-bit RShift  $(r_1)$

Associate a boolean variable to each of the 5 cases

$$\begin{aligned} z_{2m} &= z_2 \cdot l/r_0 + z_1 \cdot l_1 + z_0 \cdot l_2 + g \cdot l_3 + z_3 \cdot r_1 \\ z_{1m} &= z_2 \cdot l/r_0 + z_0 \cdot l_1 + g \cdot l_2 + 0 \cdot l_3 + z_1 \cdot r_1 \\ z_{0m} &= z_0 \cdot l/r_0 + g \cdot l_1 + z_1 \cdot r_1 \\ R &= g \cdot l/r_0 + r \cdot l_1 + z_0 \cdot r_1 \\ S &= (gear) \cdot l/r_0 + s \cdot l_1 + (gear) \cdot r_1 \end{aligned}$$

Variables  $l/r_0, l_1, l_2, l_3$  and  $r_1$ :

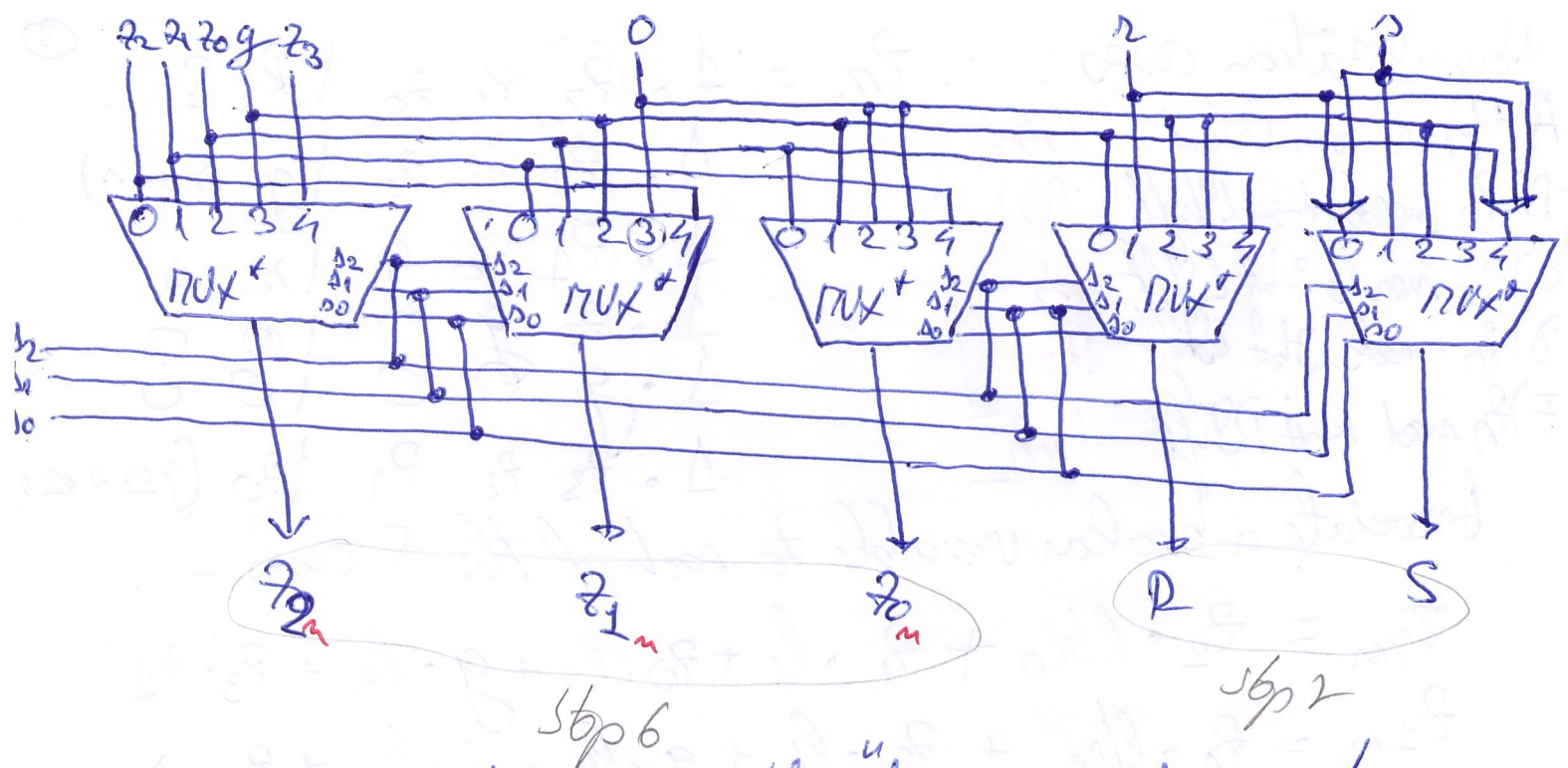
- only one can be active for a given gear/s point  
 $\Rightarrow$  encode the 5 variables on the minimum no of bits.

$$\lceil \log_2 5 \rceil = 3$$

$\Rightarrow$  use 3 signals  $\Delta_2, \Delta_1, \Delta_0$

Inputs					Outputs		
$r_1$	$l_3$	$l_2$	$l_1$	$l/r_0$	$\Delta_2$	$\Delta_1$	$\Delta_0$
0	0	0	0	1	0	0	0
0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	0
0	1	0	0	0	0	1	1
1	0	0	0	0	1	0	0





MUX\* = multiplexer with "degenerate" inputs.

## Chapter III Functional Analysis & Synthesis of Binary Multiplication Devices

3.1. Multiplication methods.

a) paper & pencil

$X = \text{multiplier}$   
 $Y = \text{multiplicand}$

4-bit, unsigned

$$\begin{array}{r}
 1100 \text{ --- } Y \\
 1011 = X_3X_2X_1X_0 \text{ --- } X
 \end{array}$$

$$\begin{array}{r}
 1100 \text{ --- } X_0 \cdot Y \cdot 2^0 \\
 1100 \text{ --- } X_1 \cdot Y \cdot 2^1 \\
 0000 \text{ --- } X_2 \cdot Y \cdot 2^2 \\
 1100 \text{ --- } X_3 \cdot Y \cdot 2^3 \\
 \hline
 10000100 \text{ --- } P = \sum_{i=0}^3 X_i \cdot Y \cdot 2^i \\
 = 2^7 + 2^2 = 128 + 4 = 132
 \end{array}$$