

2)

Block size = 32 bytes

Physical Address width = 33

Index width = 7

a)

$$\text{Offset width} = \log_2 32 = 5$$

$$\text{Tag width} = 33 - 1 - 7 - 5 = 20$$

$$\text{Tag} = \underline{\underline{1110 \ 0101 \ 0110 \ 1110 \ 1011}}$$

b)

$$\text{Block Address} = \underline{\underline{0001 \ 001}}$$

c)

$$\text{Set Address} = \text{Block Address Mod Set Size} = 11000 \% 2 = \underline{\underline{0}}$$

d)

$$32 \cdot 2^7 = 2^{12} = \underline{\underline{4 \text{ KB}}}$$

e)

Hit time L_1 4ns

Hit time L_2 10ns

miss penalty L_2 600ns

$$T = \text{Hit time}_{L_1} + \text{Miss Rate}_{L_1} \cdot (\text{Hit time}_{L_2} + \text{Miss Rate}_{L_2} \cdot \text{Miss Pen.}_{L_2})$$

Hit Rate L_1 90% \rightarrow Miss Rate L_1 10%

Hit Rate L_2 60% \rightarrow Miss Rate L_2 40%

$$T = 4 + \frac{10}{100} \cdot \left(10 + 600 \cdot \frac{40}{100} \right) = 4 + \frac{250}{10} = \underline{\underline{29 \text{ ns}}}$$