

Since philed-cond-walt is used inside critical regions (mules locked regions) it needs to unlock mules first to not cause a deadlock by preventing other threads to continue and walking for a signal itself. So inside pthread-cond-wast it unlocks mules, walts for a signal then locks it again.

1: \$+0, 0x ADDD 1: \$+1, 0x ADDD 1: \$+2, 0 MV \$+3, \$+0 1: \$+5, 0 1: \$+6, 10 bg+ \$+4, \$+2, swap 100pc: add; \$+3, \$+3, 1 add; \$+5, \$+5, 1 bl+ \$+5, \$+6, 100p

5wop: 1w 5+2, 5+4 5 100pc

c) for PI only there is 126B free rom n = 12/2 = 6CPU u = 1-p^ where n = 6, $1 - \left(\frac{6}{10}\right)^6 = 12/2 = 6$ for P2 only it would be

CPU u = 1-p^ where n = 2CPU u = 1-p^ where n = 2 n = 12/6 = 2

1-(3)2=

2725