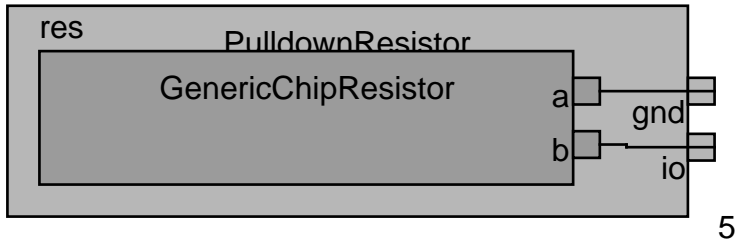


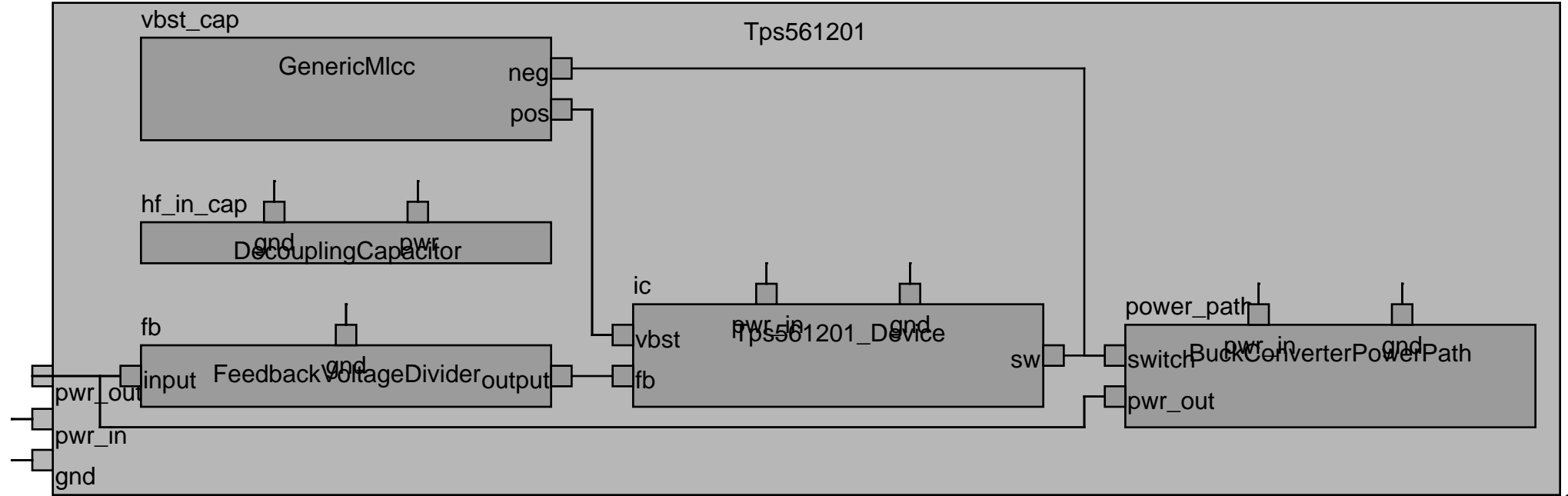
cc1



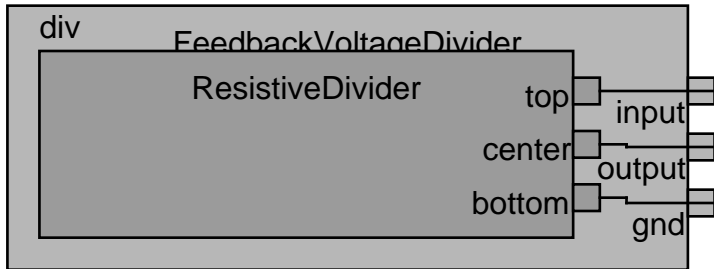
cc2

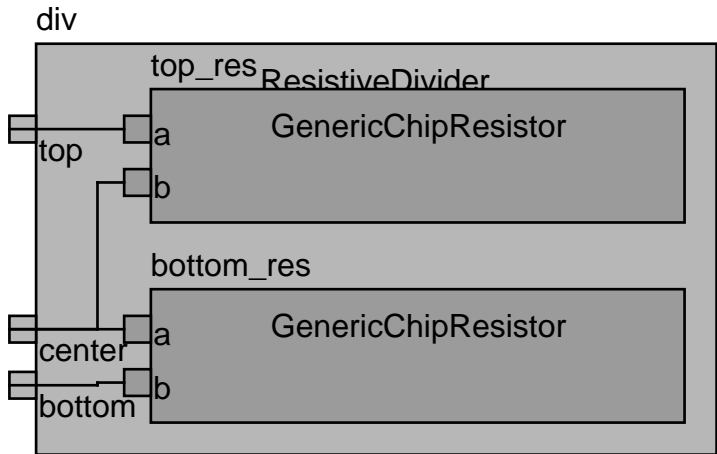


buck



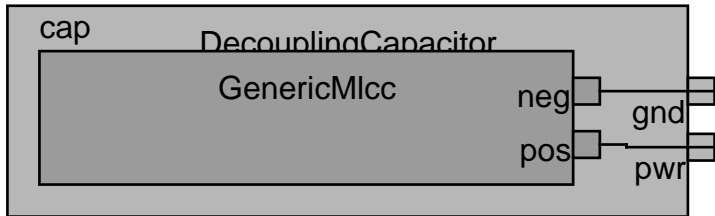
fb

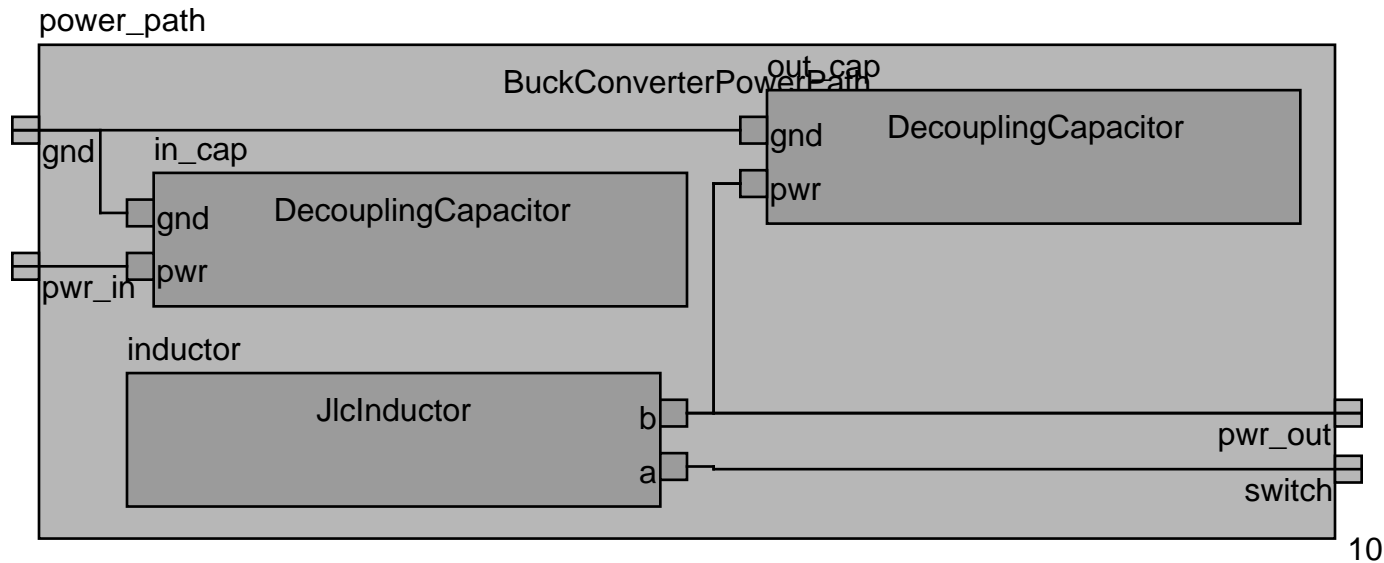




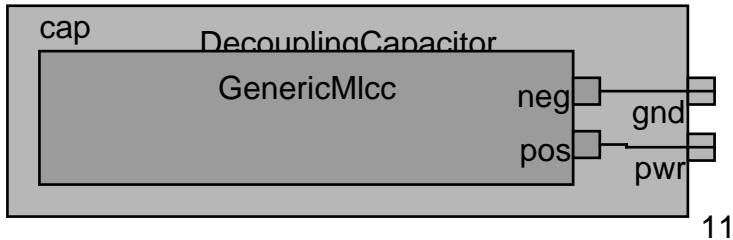


hf\_in\_cap

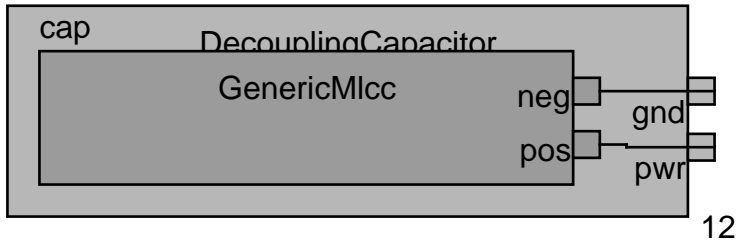




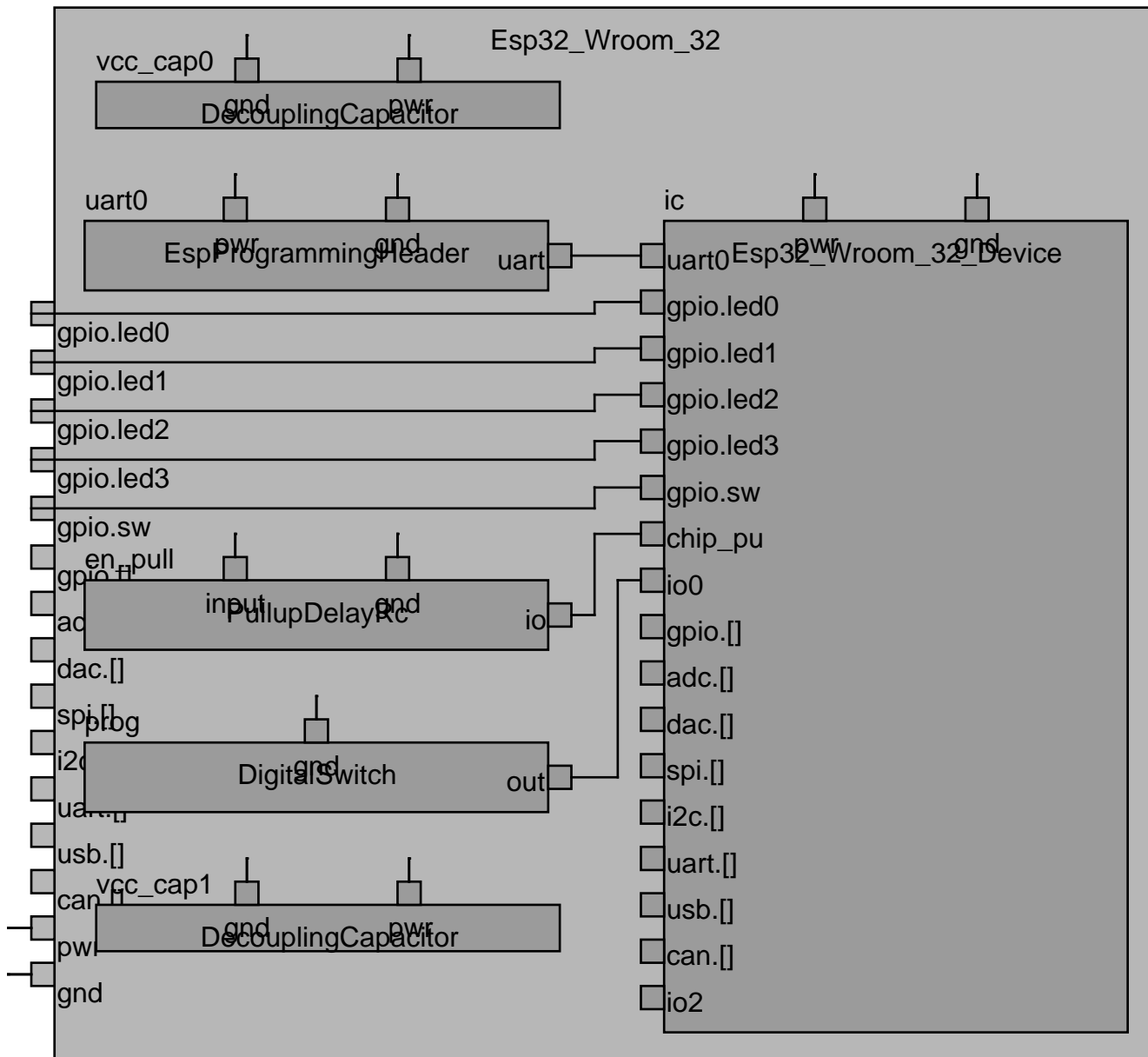
in\_cap



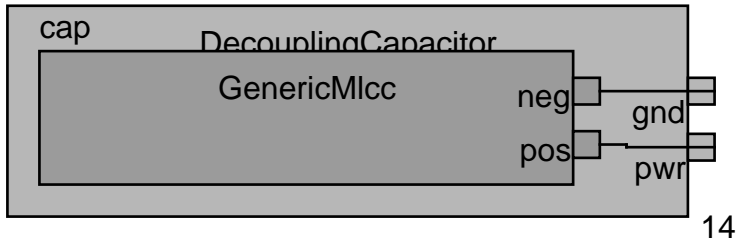
out\_cap



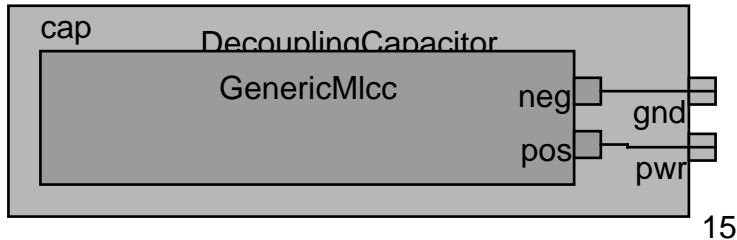
mcu



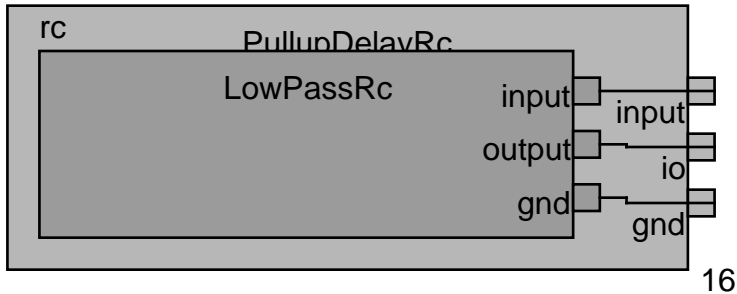
vcc\_cap0



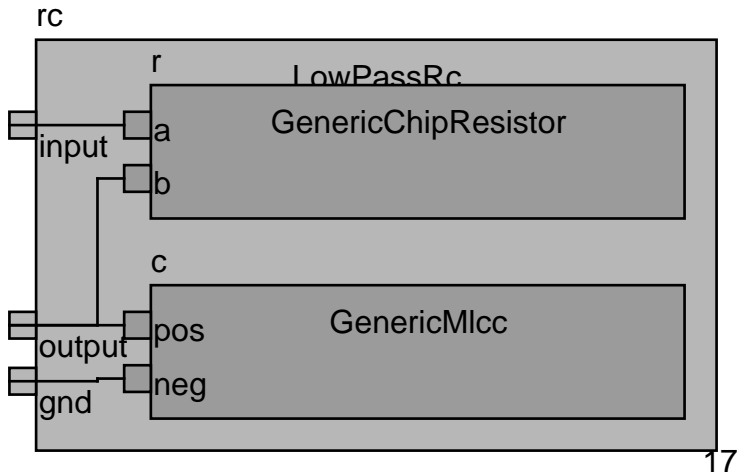
vcc\_cap1



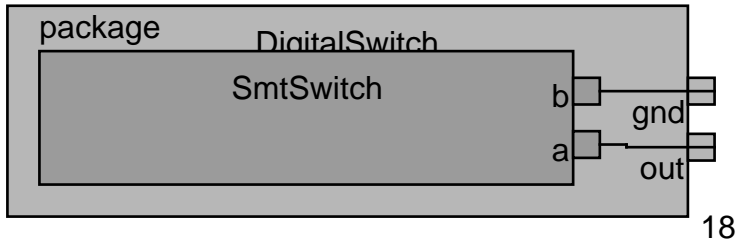
en\_pull



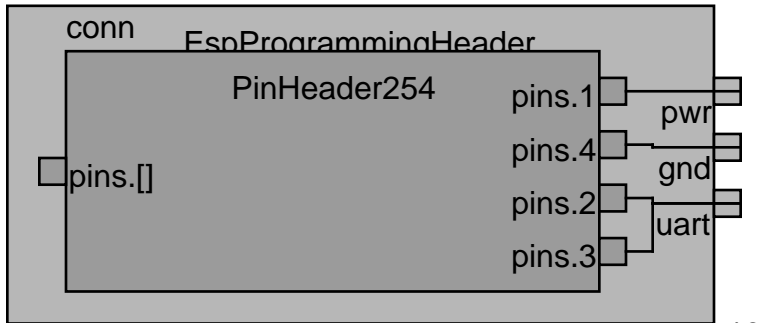




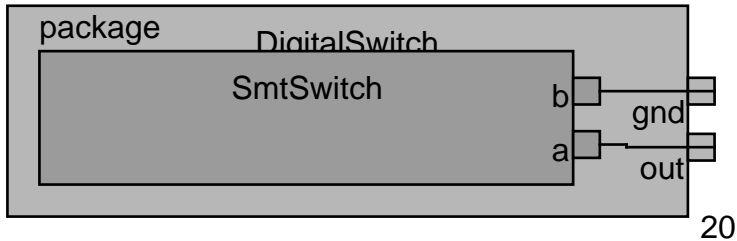
prog



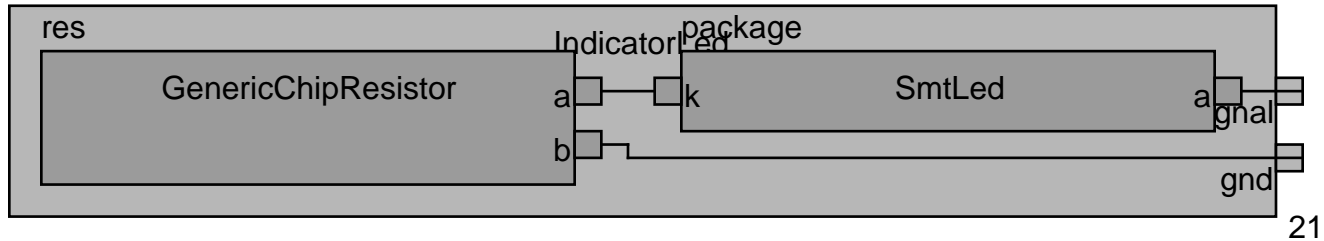
uart0



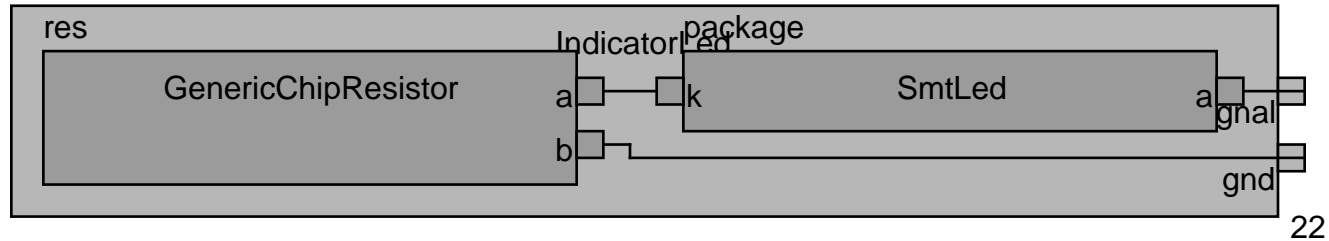
SW



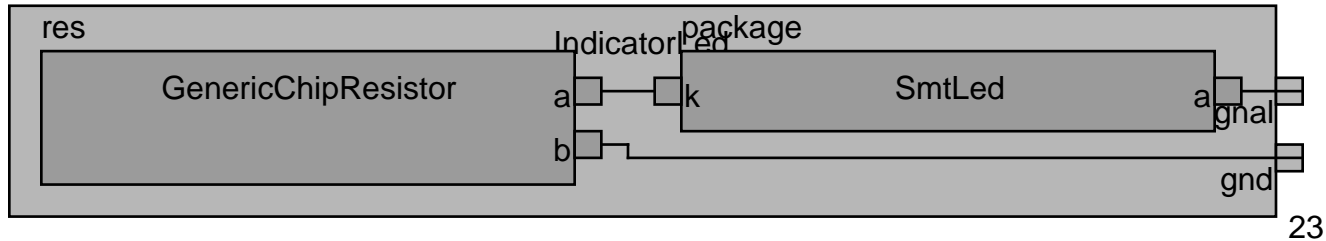
led[0]



led[1]



led[2]



led[3]

