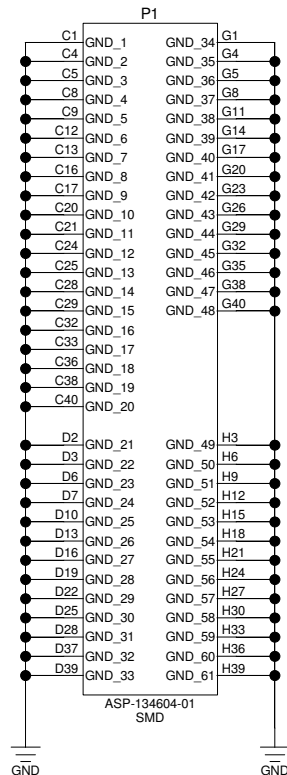
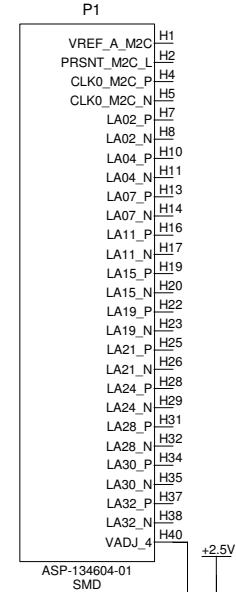
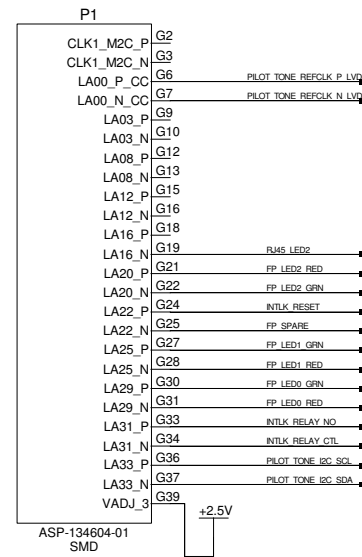
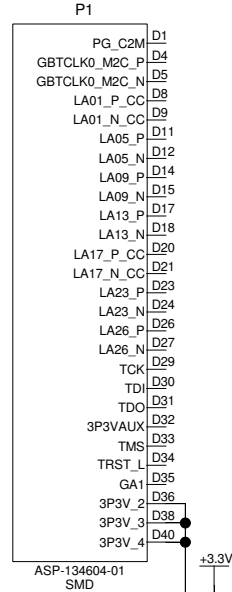
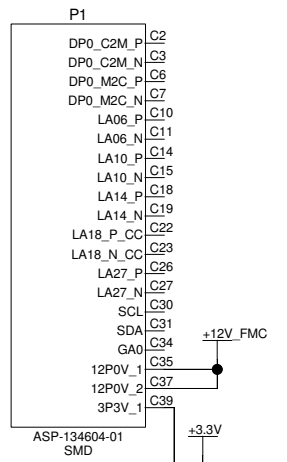
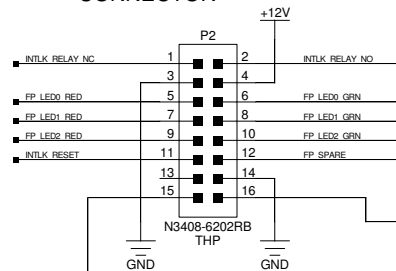


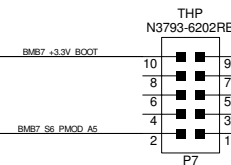
FMC LPC CONNECTOR



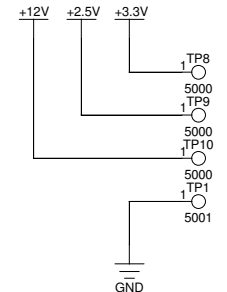
FRONT PANEL INTERFACE CONNECTOR



FP NET ADDR SWITCH TO BMB7 PMOD CONNECTOR



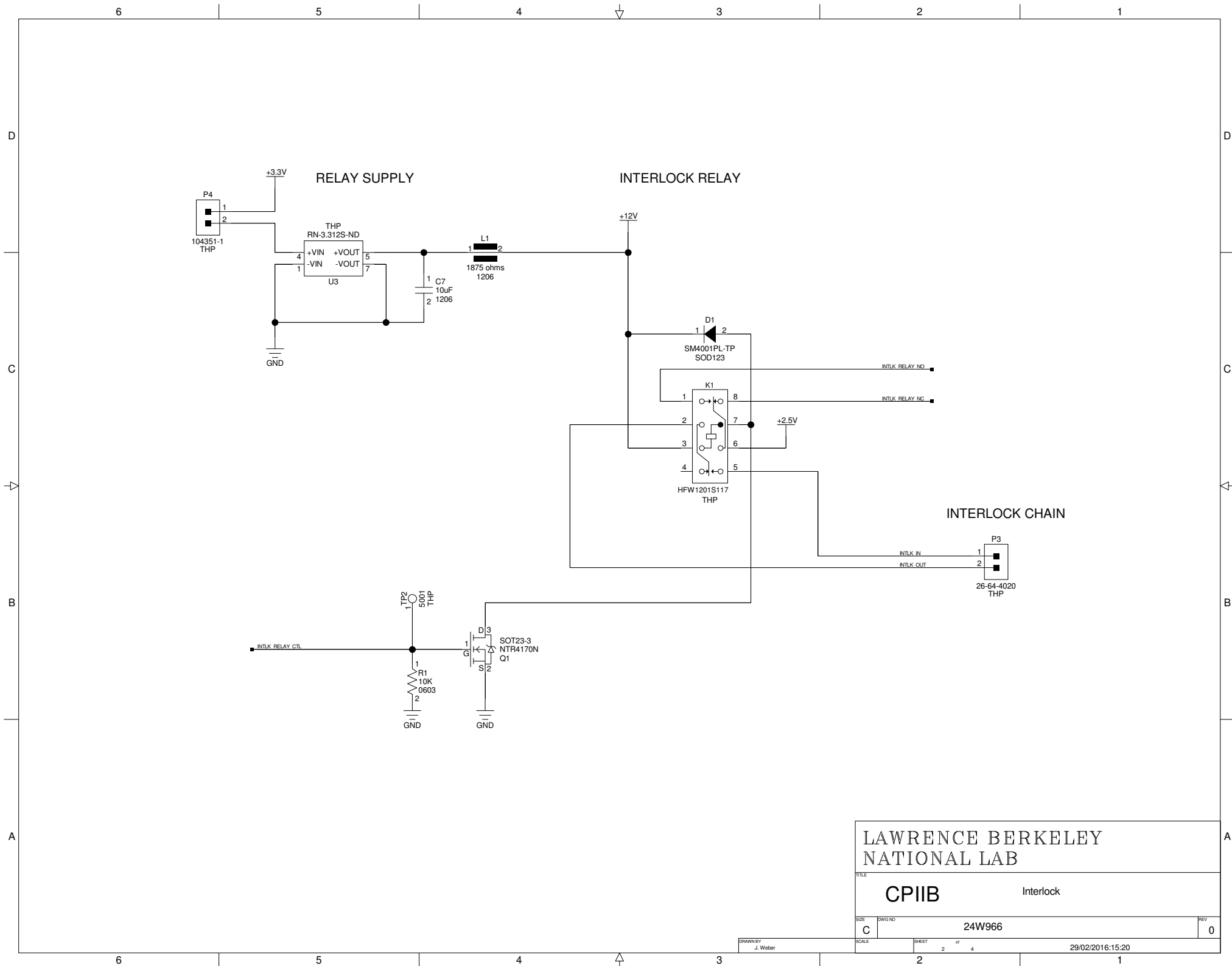
TEST POINTS

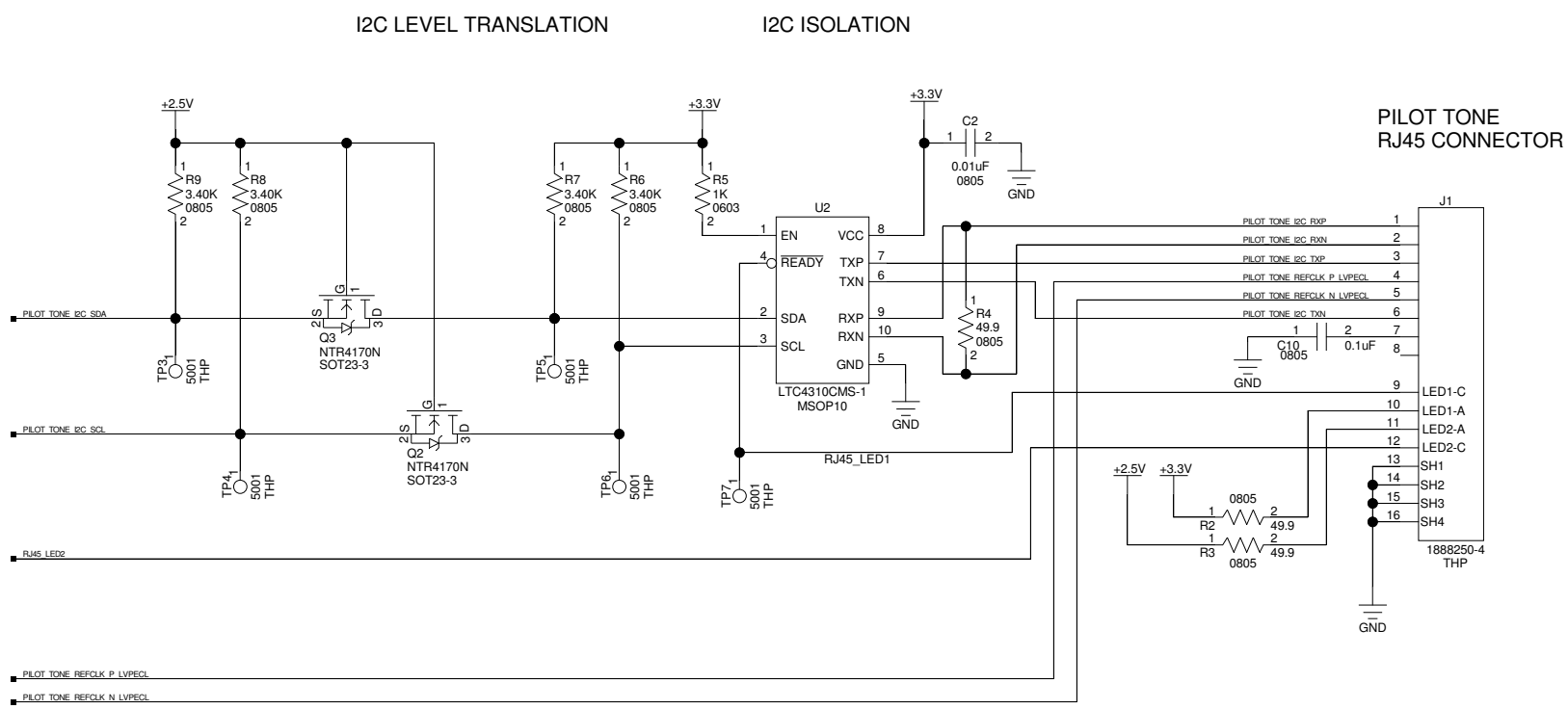


LAWRENCE BERKELEY
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CPIIB

FMC LPC, FP, CC I/F Connectors





LAWRENCE BERKELEY
NATIONAL LAB

TITLE

CPIIB

Pilot Tone Interface

SIZE

C

DWG NO

24W966

REV

0

DRAWN BY

J. Weber

SHEET

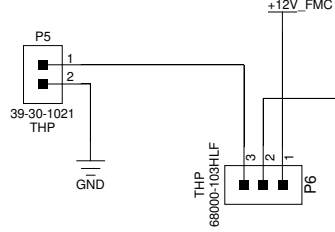
3

of

4

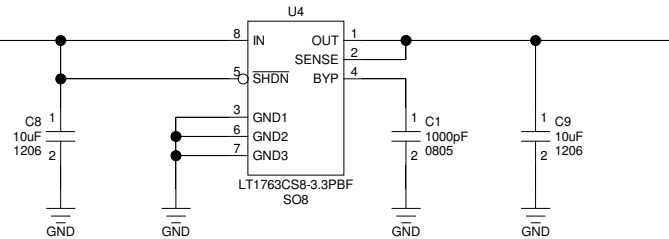
29/02/2016:15:20

EXTERNAL ANALOG
SUPPLY INPUT
(+5V TO +12V)

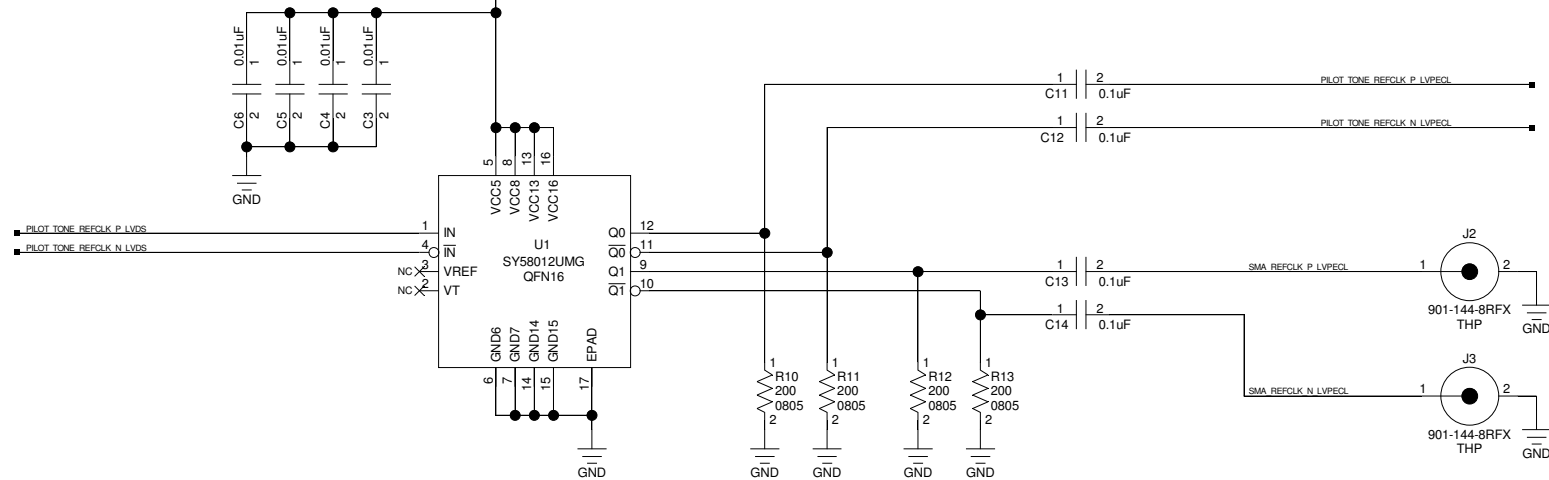


SUPPLY SELECT JUMPER
1-2 = FMC
2-3 = EXT

ANALOG +3.3V LINEAR REGULATOR



REFERENCE CLOCK BUFFER/FANOUT



REFERENCE CLOCK
SMA CONNECTORS

LAWRENCE BERKELEY
NATIONAL LAB

CPIIB Reference Clock

SIZE	DWG NO	REV
C	24W966	0
SCALE	SHEET 4 of 4	29/02/2016:15:20

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J. Weber