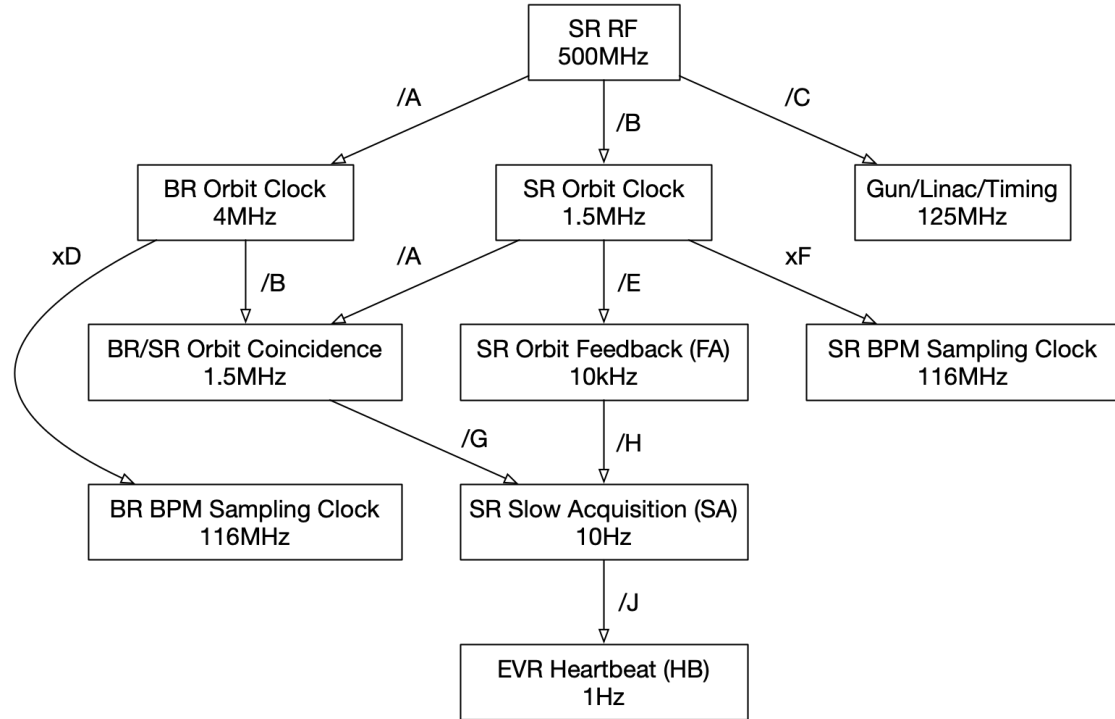
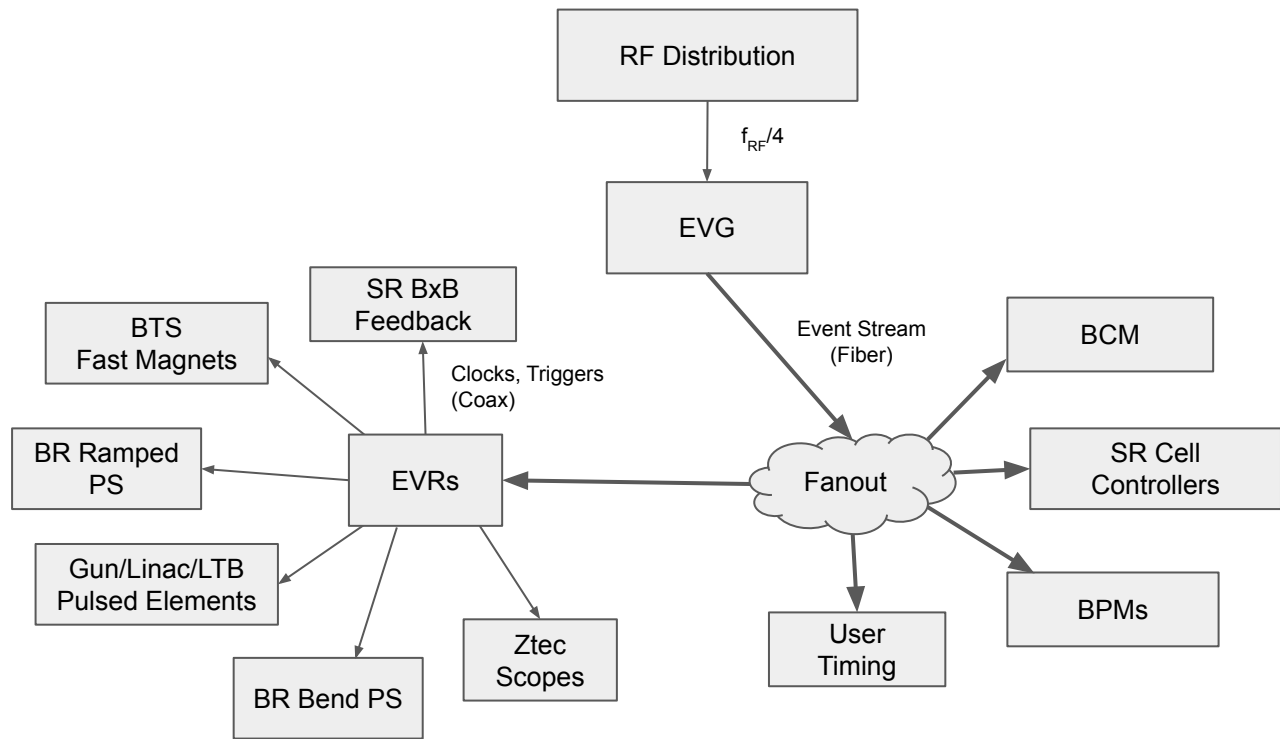


# ALS Clock Tree

Variable	Description	Value
A	BR harmonic number	125
B	SR harmonic number	328
C	Fixed RF frequency ratio	4
D	Integer yielding $\sim 117\text{MHz}$	29
E	Integer yielding $\sim 10\text{kHz}$	152
F	Integer yielding $\sim 117\text{MHz}$	77
G	Integer yielding $\sim 10\text{Hz}$	1216
H	Integer yielding $\sim 10\text{Hz}$	1000
J	Integer yielding $\sim 1\text{Hz}$	10





# Proposed ALS-U Clock Tree

Variable	Description	Value
A	AR harmonic number x2	608
B	AR harmonic number x2+1	609
C	AR harmonic number	304
D	SR harmonic number	328
E	Fixed RF frequency ratio	4
F	Integer yielding ~117MHz	71
G	SR harmonic number /8	41
H	AR harmonic number /8	38
J	Integer yielding ~117MHz	77
K	Integer yielding ~10kHz	4
L	Integer yielding ~10Hz	1000
M	Integer yielding ~1Hz	10
N	$2 \times (\text{AR harmonic number})^2$	184832
P	BR harmonic number	125
R	Integer yielding ~117MHz	29
S	Integer yielding ~1Hz	21

