

FMC2_PG_C2M D FMC2_PG_C2M

FMC2_PRSNT_M2C_L D FMC2_PRSNT_M2C_L

 FMC2_TCK
 JFMC2_TCK

 FMC2_TDI
 JFMC2_TDI

 JFMC2_TDI
 JFMC2_TDI

 JFMC2_TDI
 JFMC2_TDI

 JFMC2_TMS
 JFMC2_TMS

 JFMC2_TRST
 JFMC2_TRST

FMC2_GBTCLK0_M2C_P
FMC2_GBTCLK0_M2C_N
FMC2_GBTCLK0_M2C_N
FMC2_CLK1_M2C_P
FMC2_CLK1_M2C_N
FMC2_CLK1_M2C_P
FMC2_CLK0_M2C_P
FMC2_CLK0_M2C_N
FMC2_CLK0_M2C_N
D FMC2_CLK0_M2C_N

GA0_2 D GA0_2 GA1_2 D GA1_2

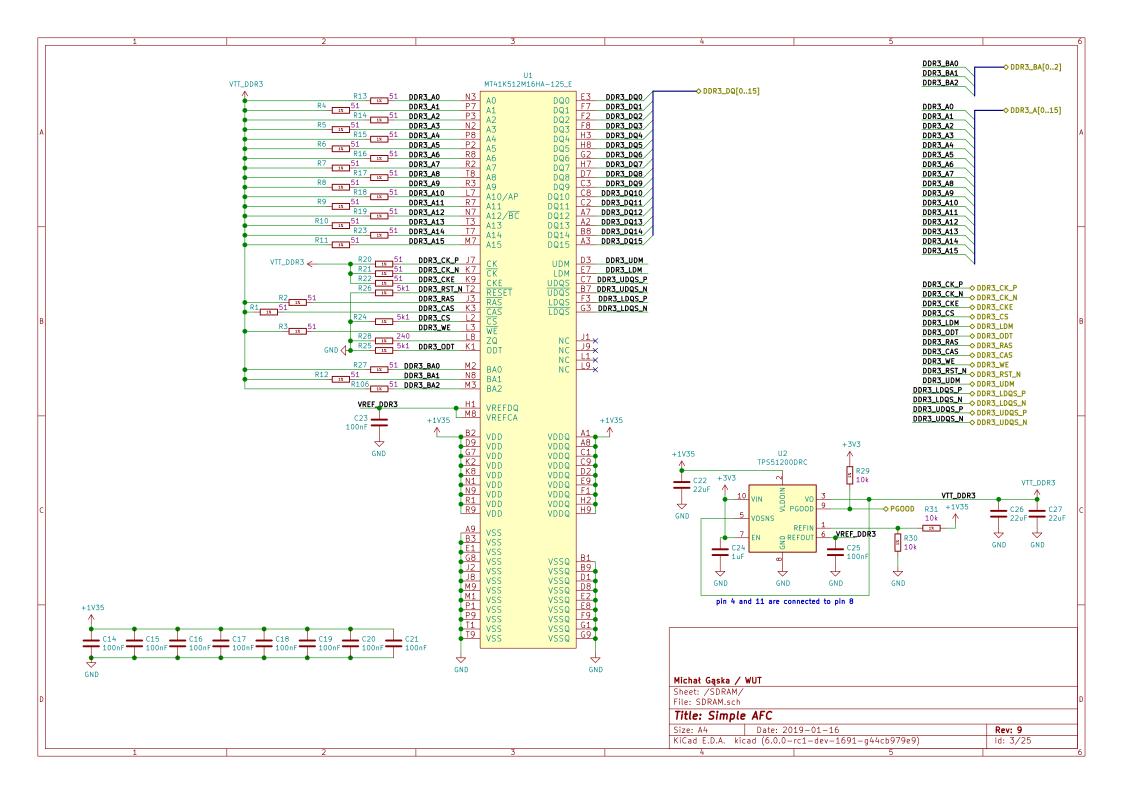
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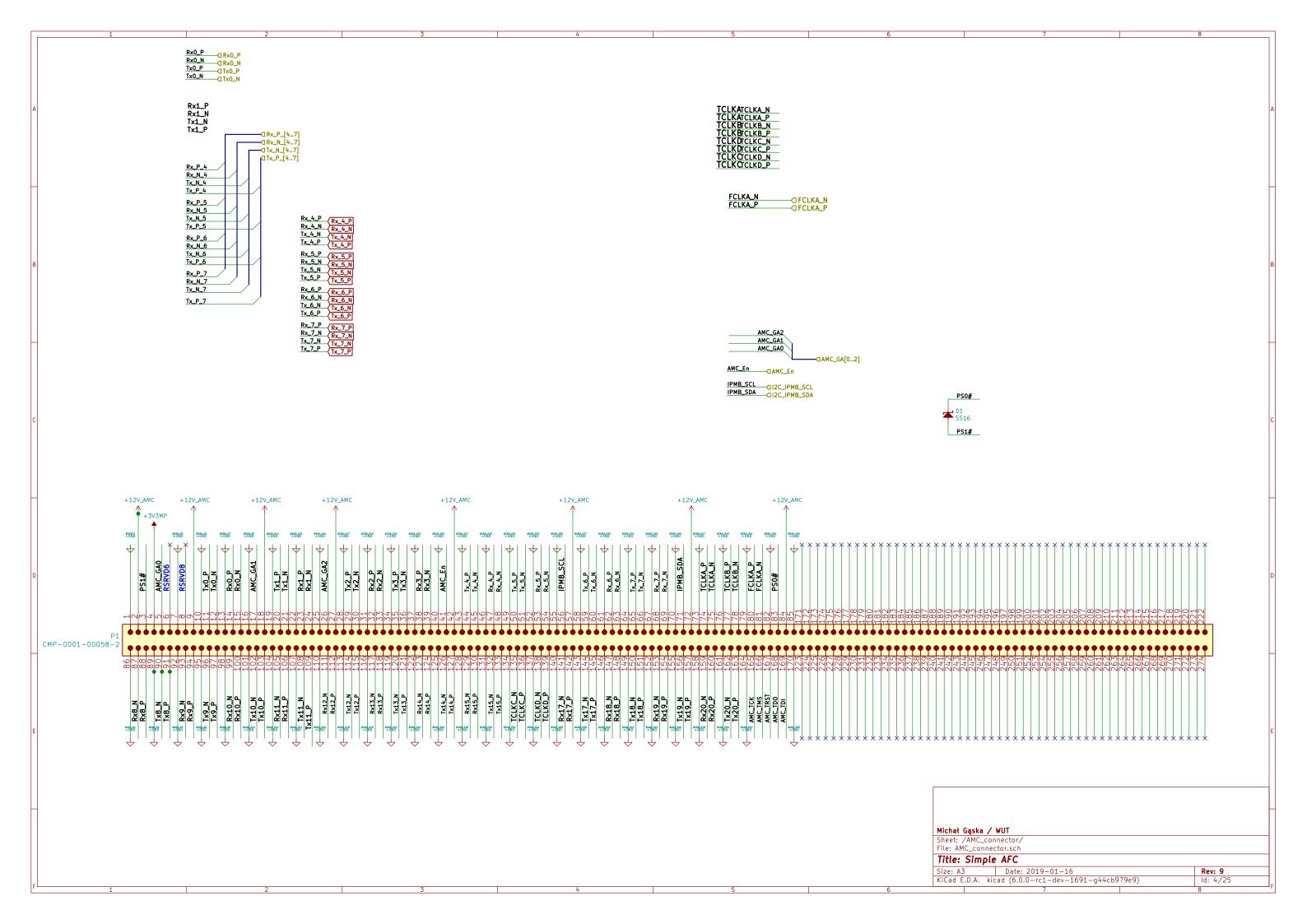
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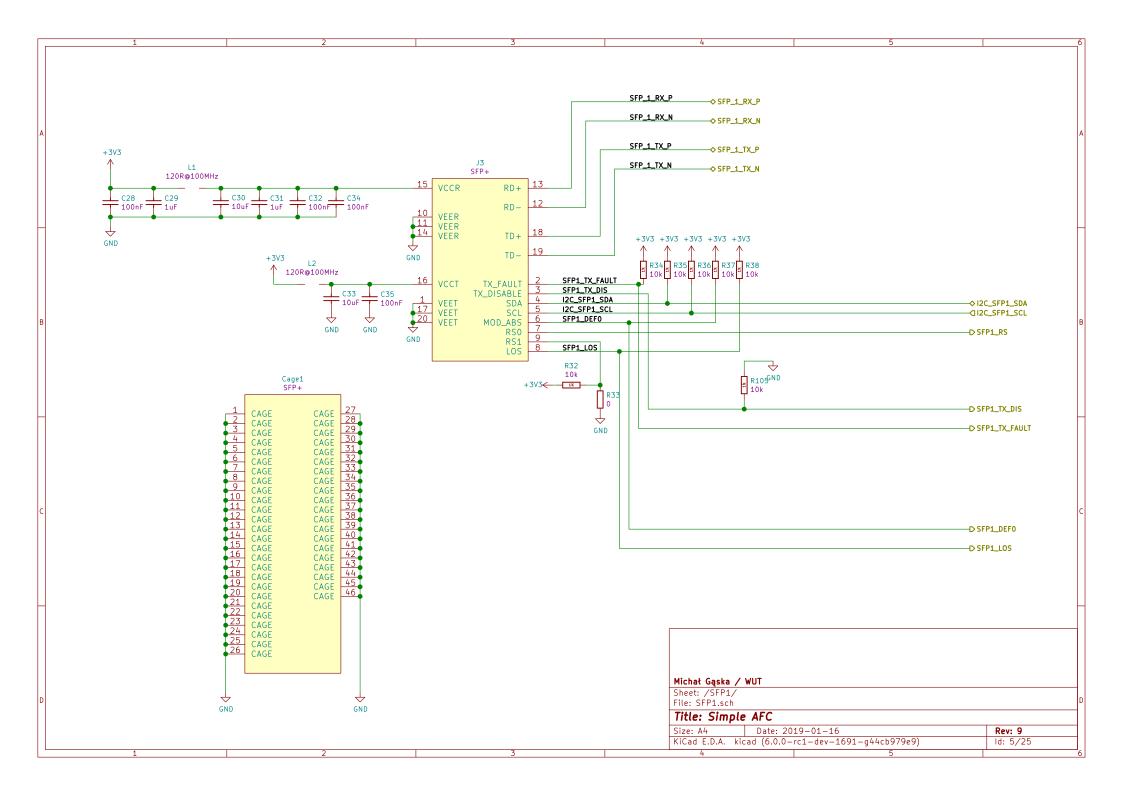
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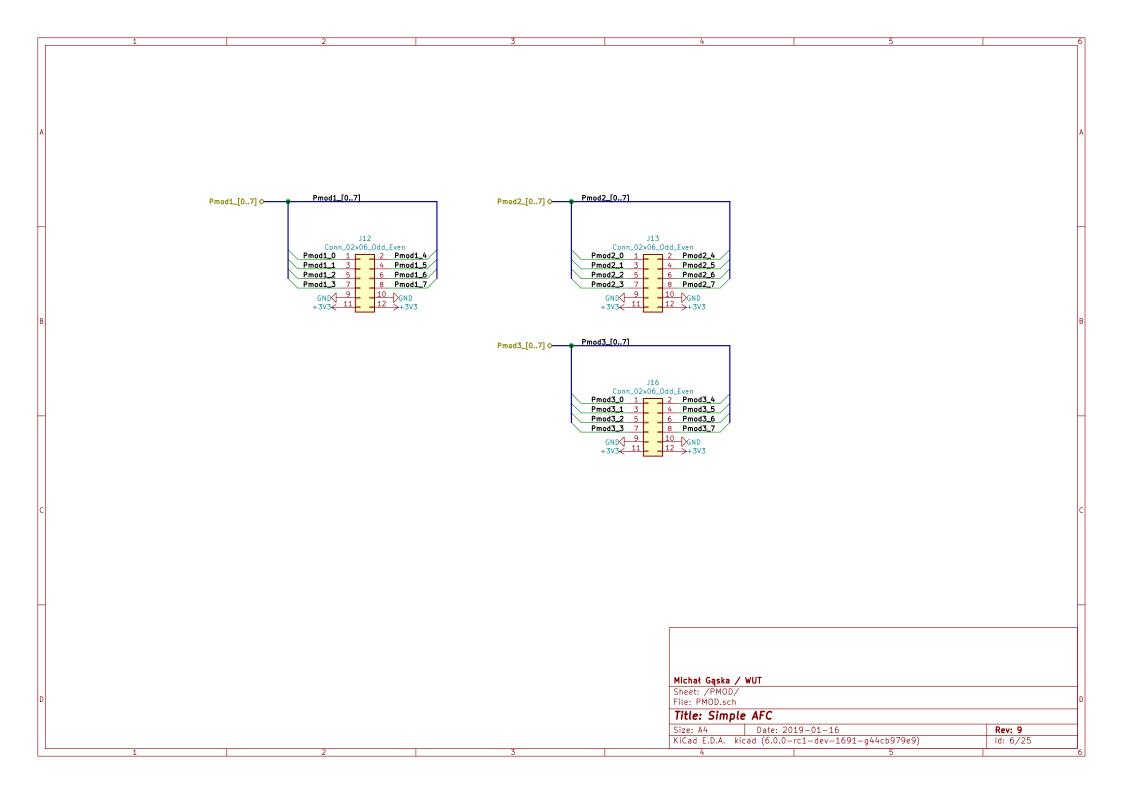
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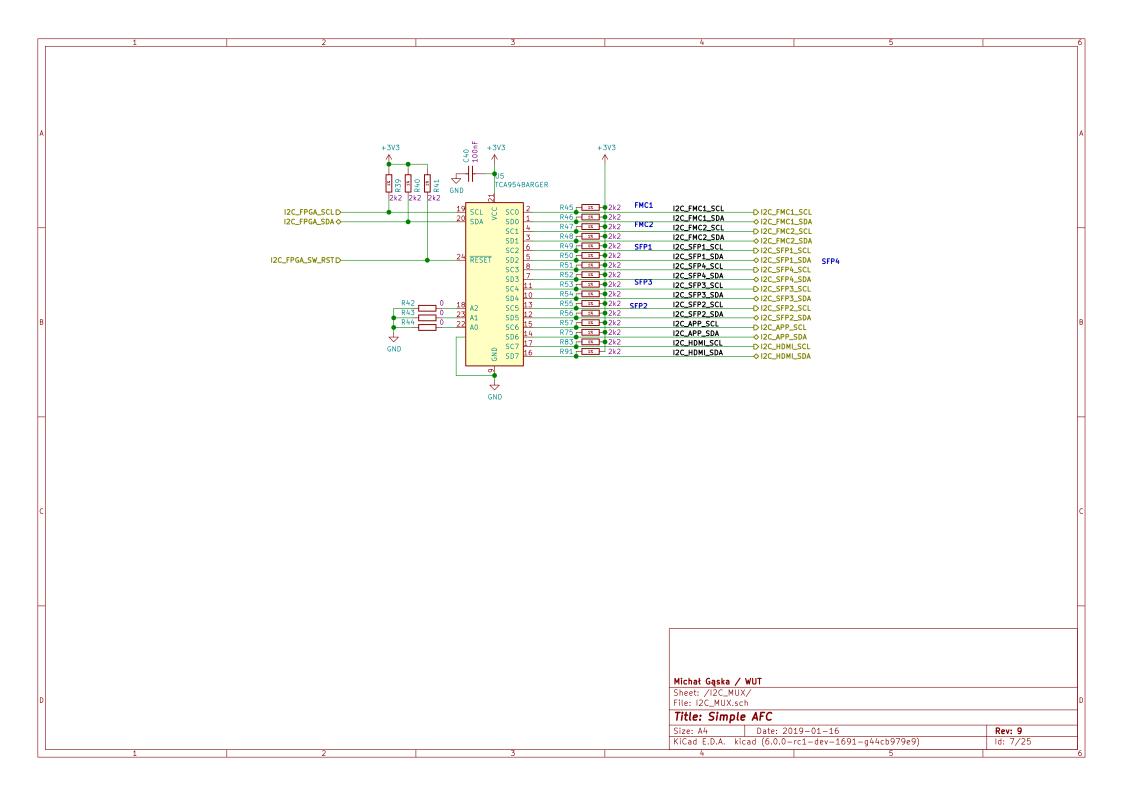
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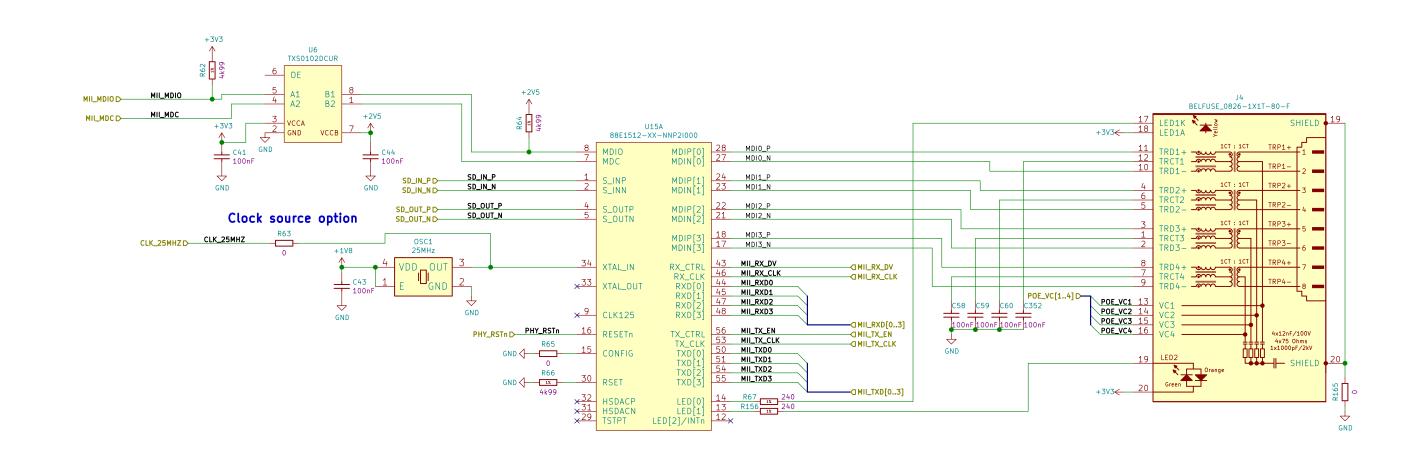


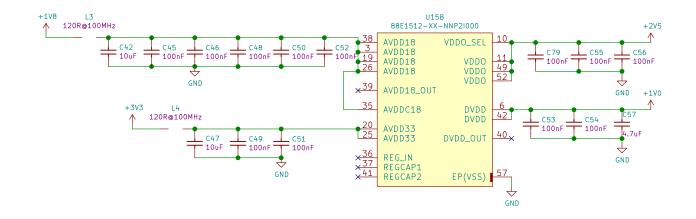












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 Sheet: /ETH_PHY/

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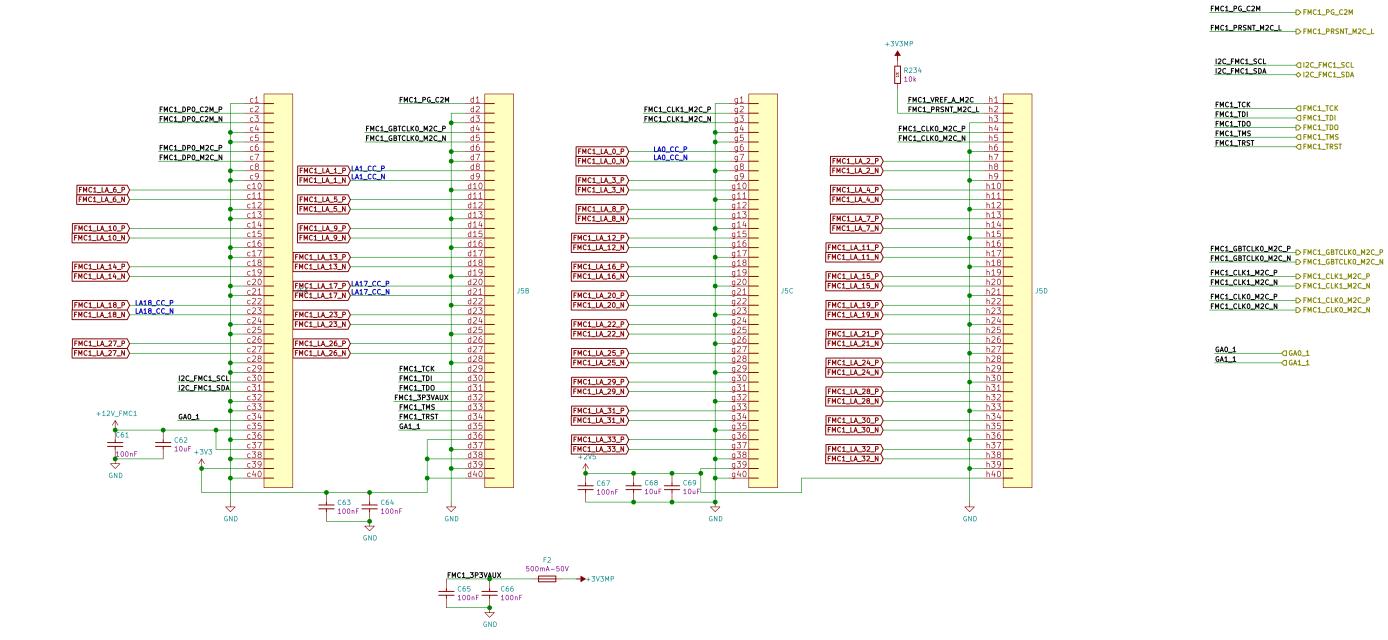
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 Date: 2019-01-16
 Rev: 9

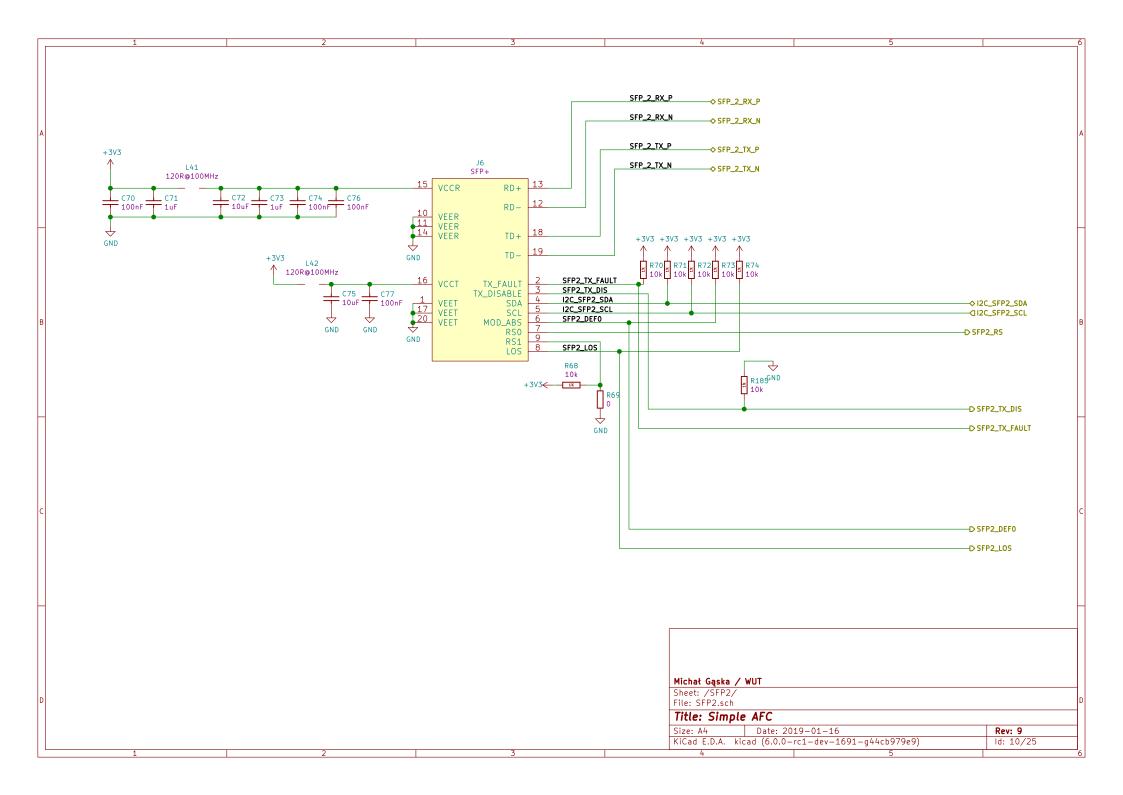
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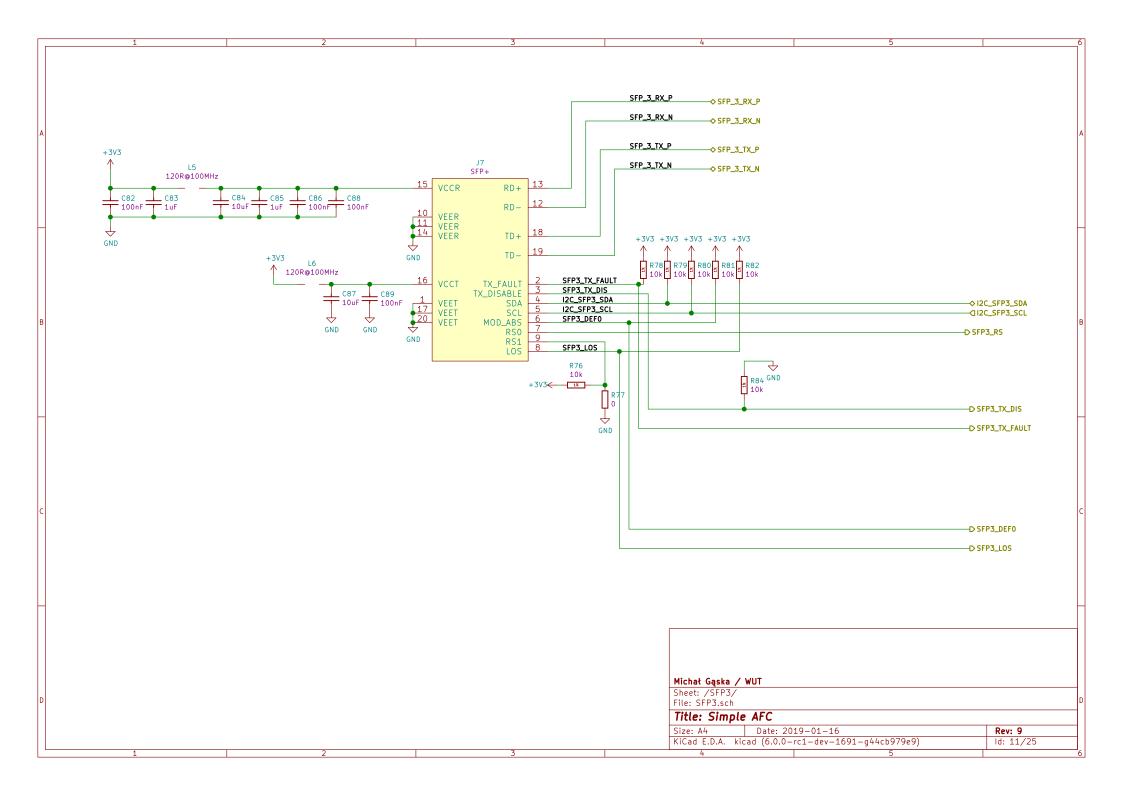
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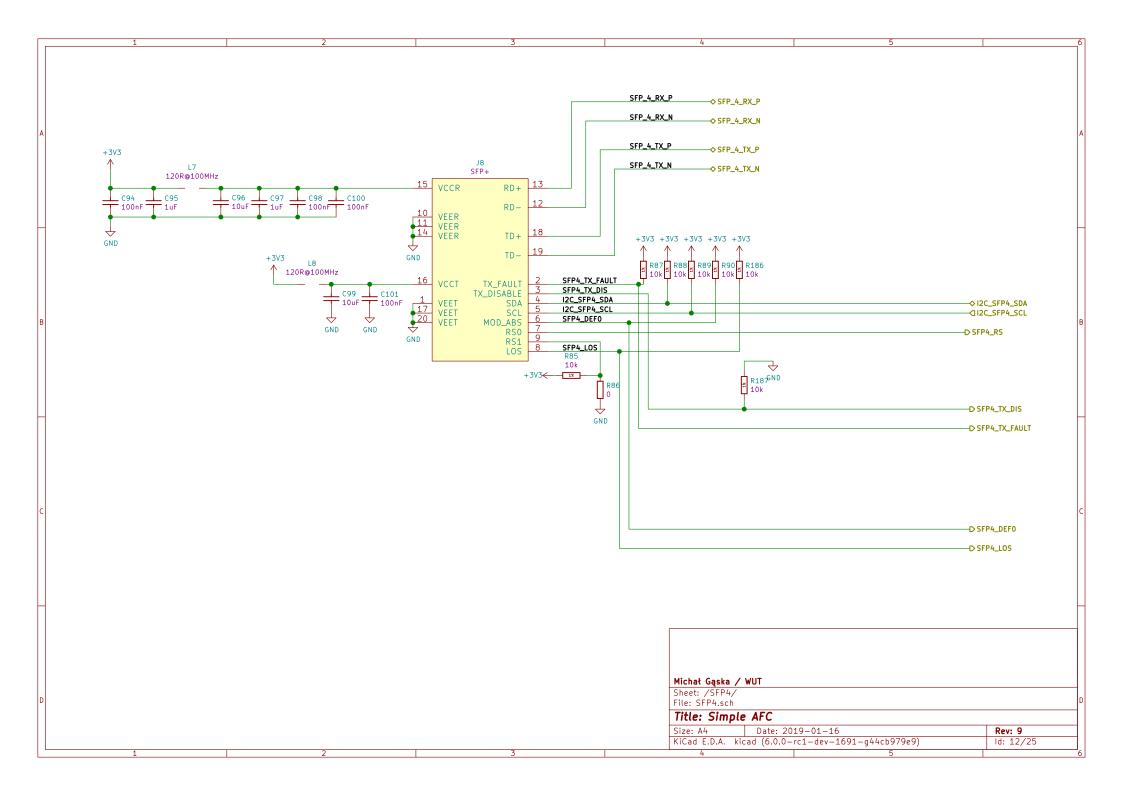
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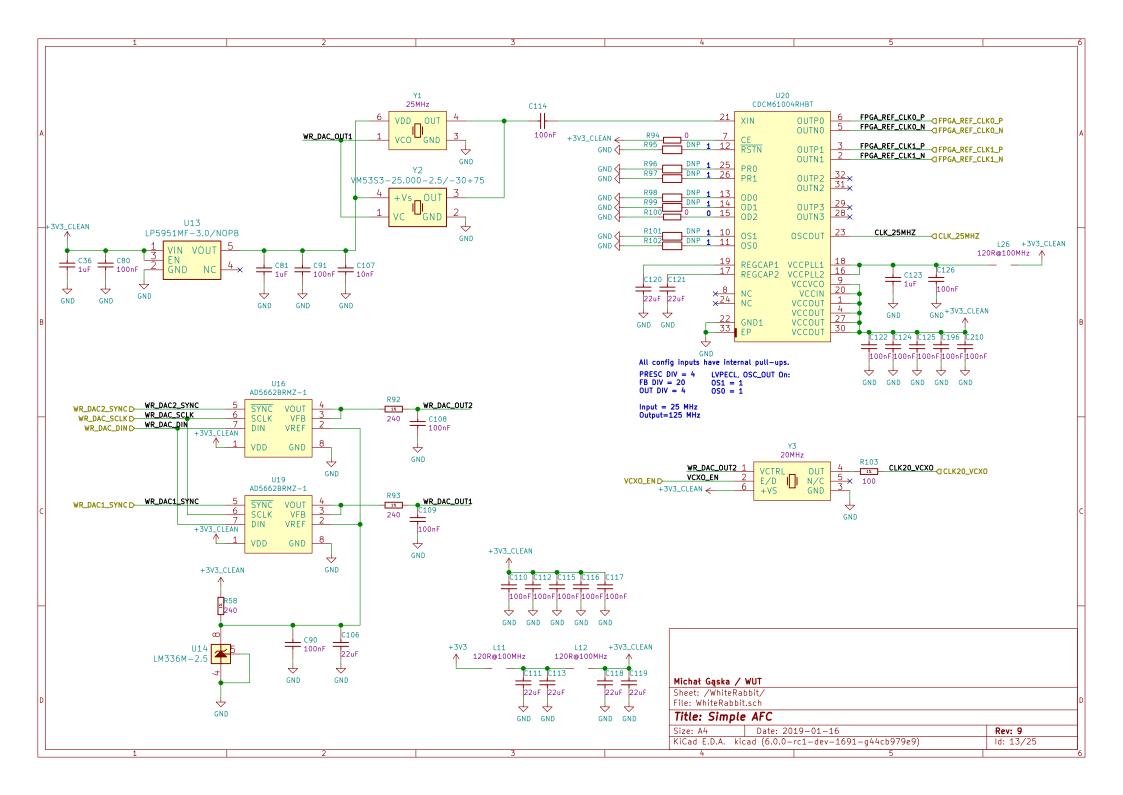


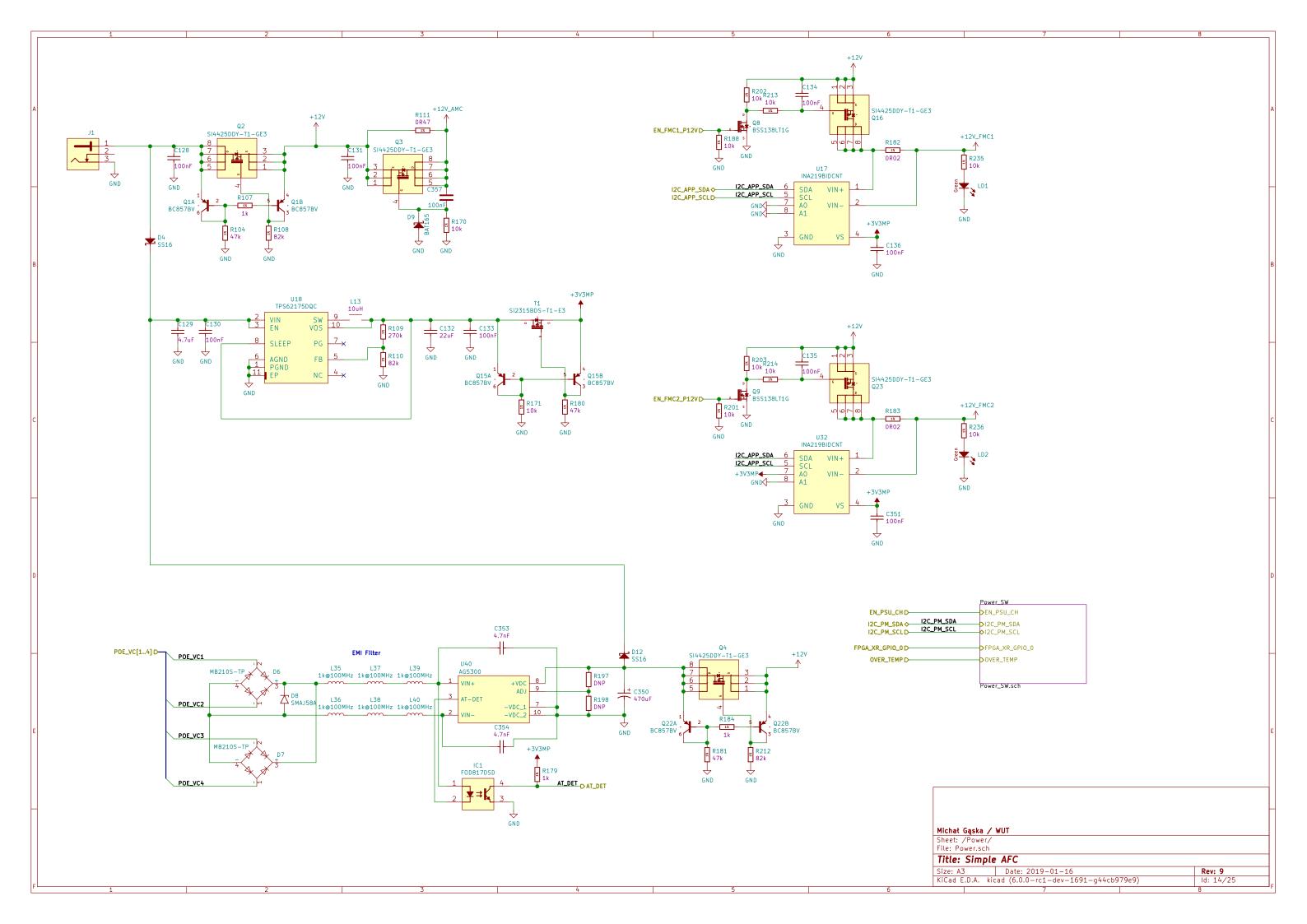
Michał Gąska / WUT Sheet: /FMC_1/ File: FMC_1.sch Title: Simple AFC Size: A3 Date: 2019-01-16
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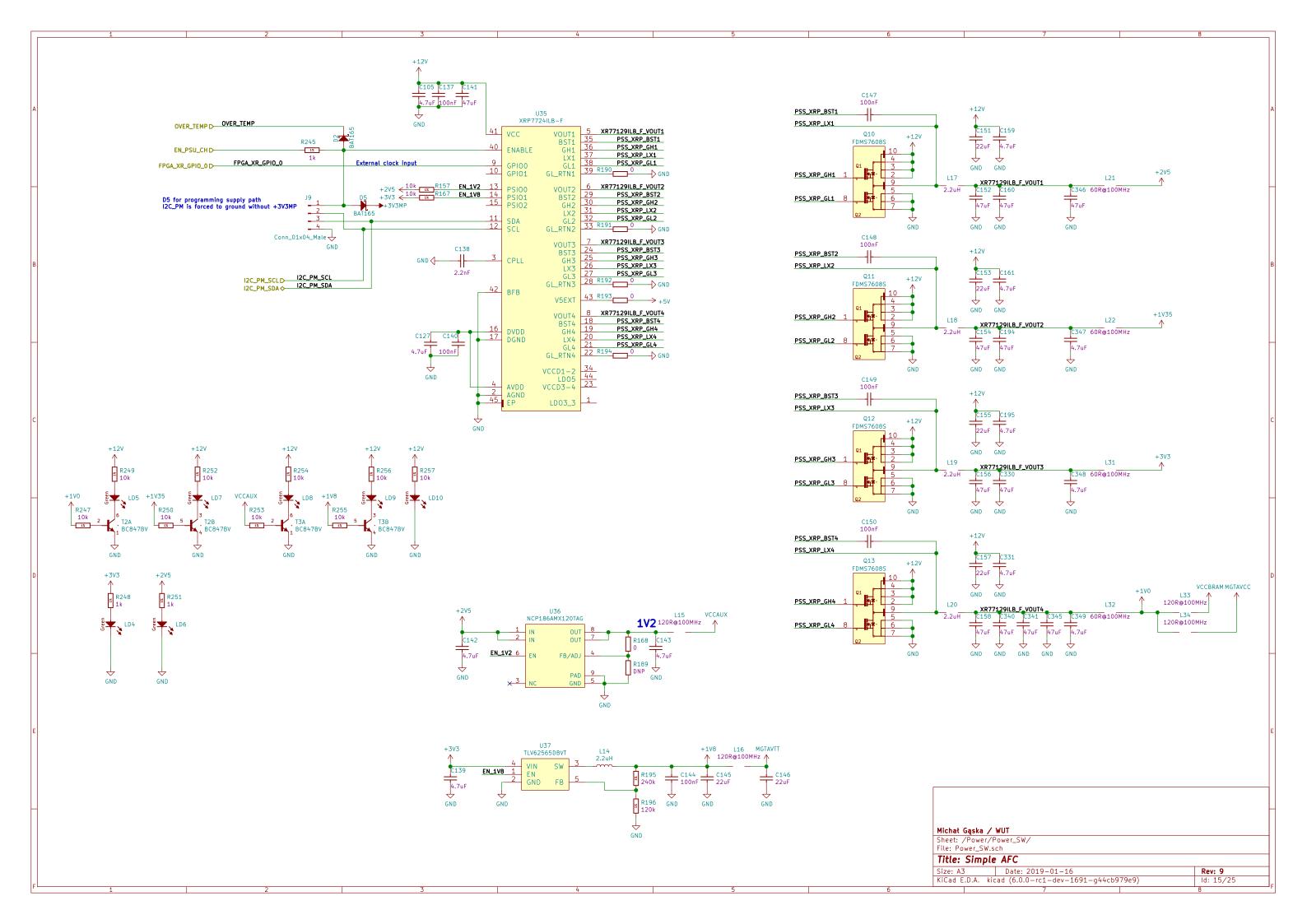


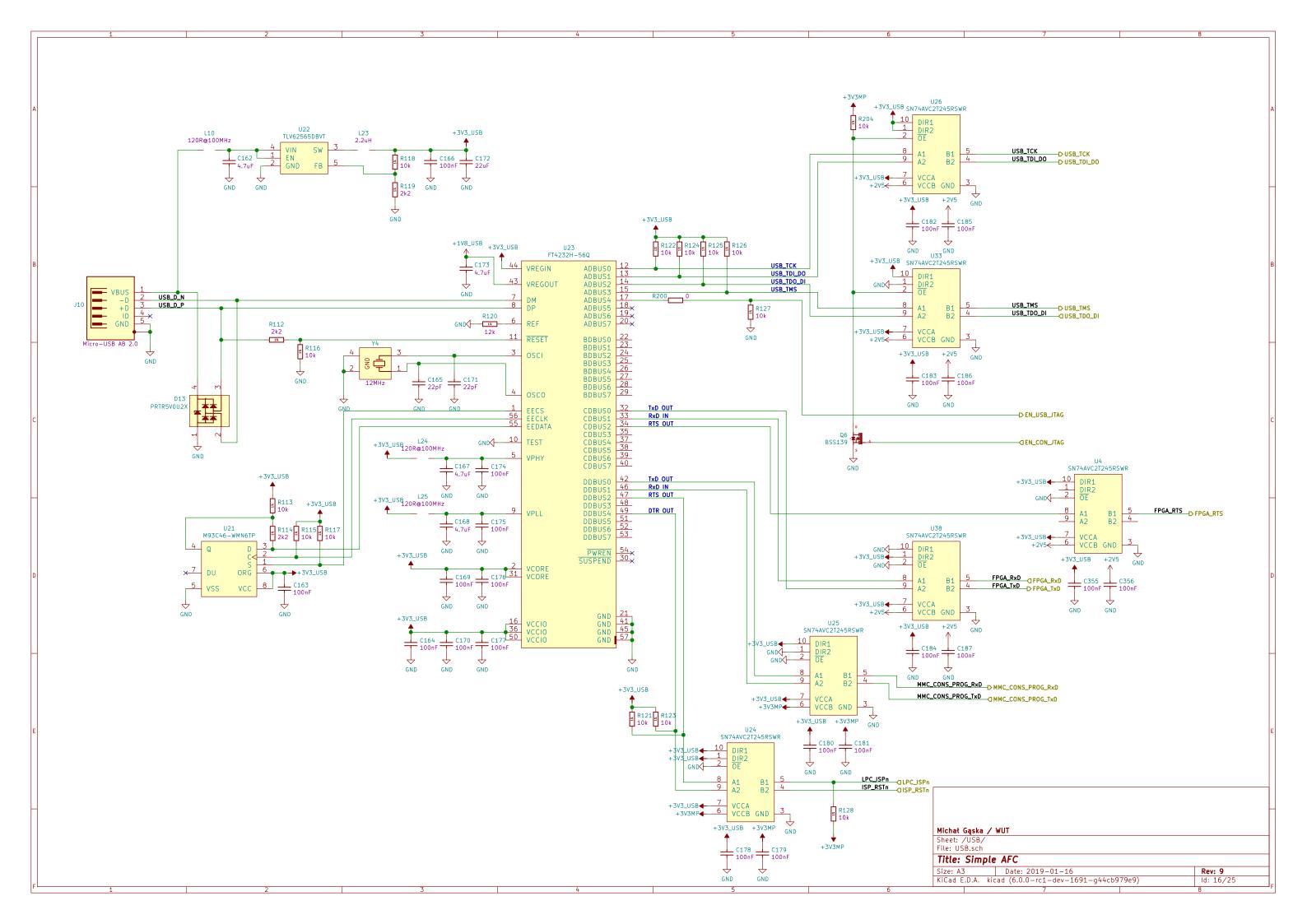


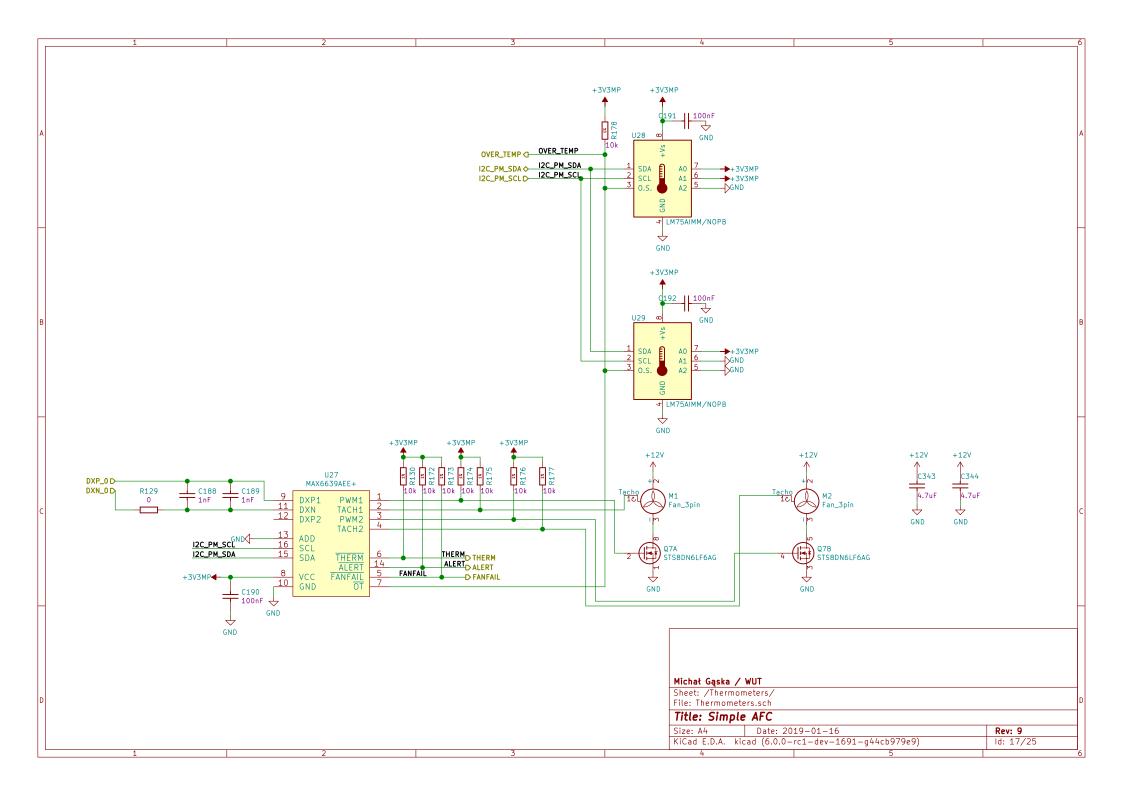


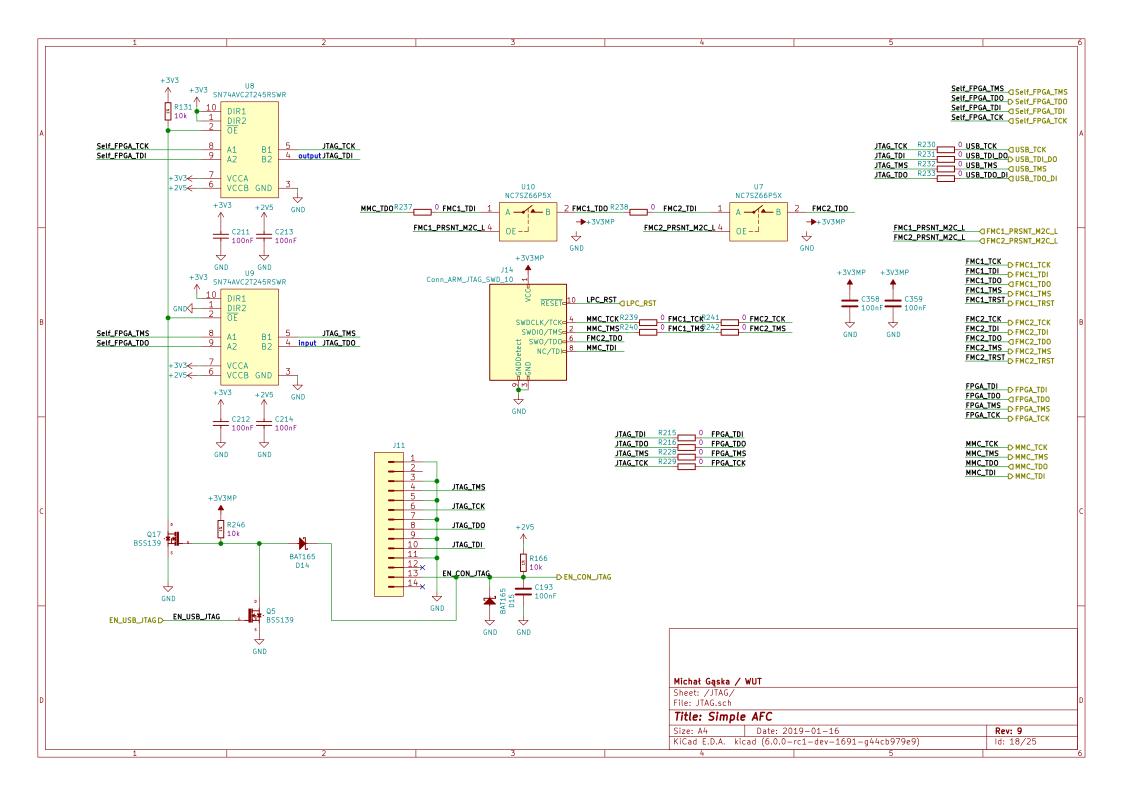


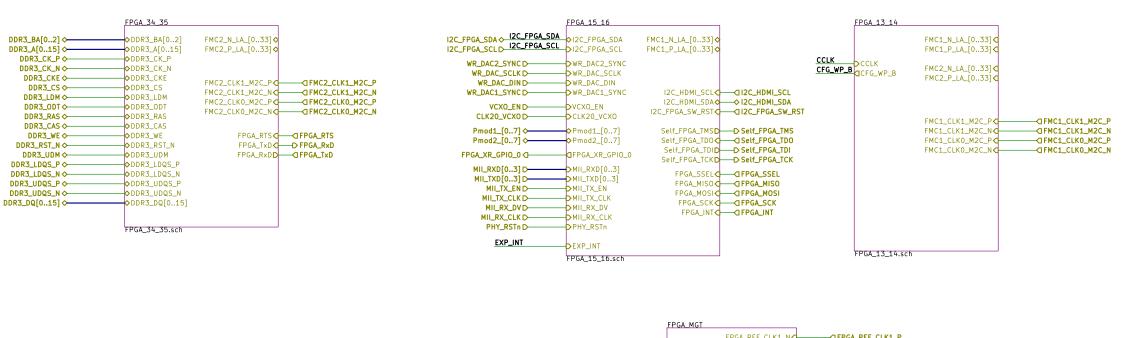


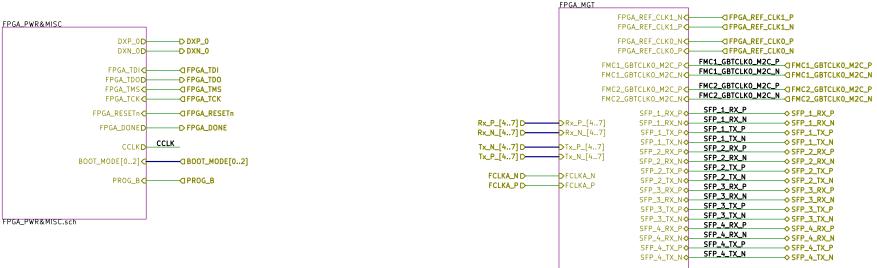


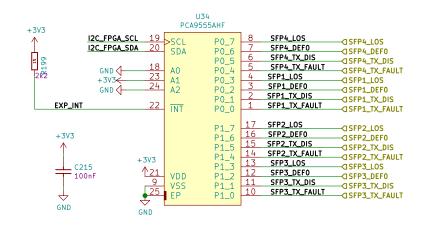








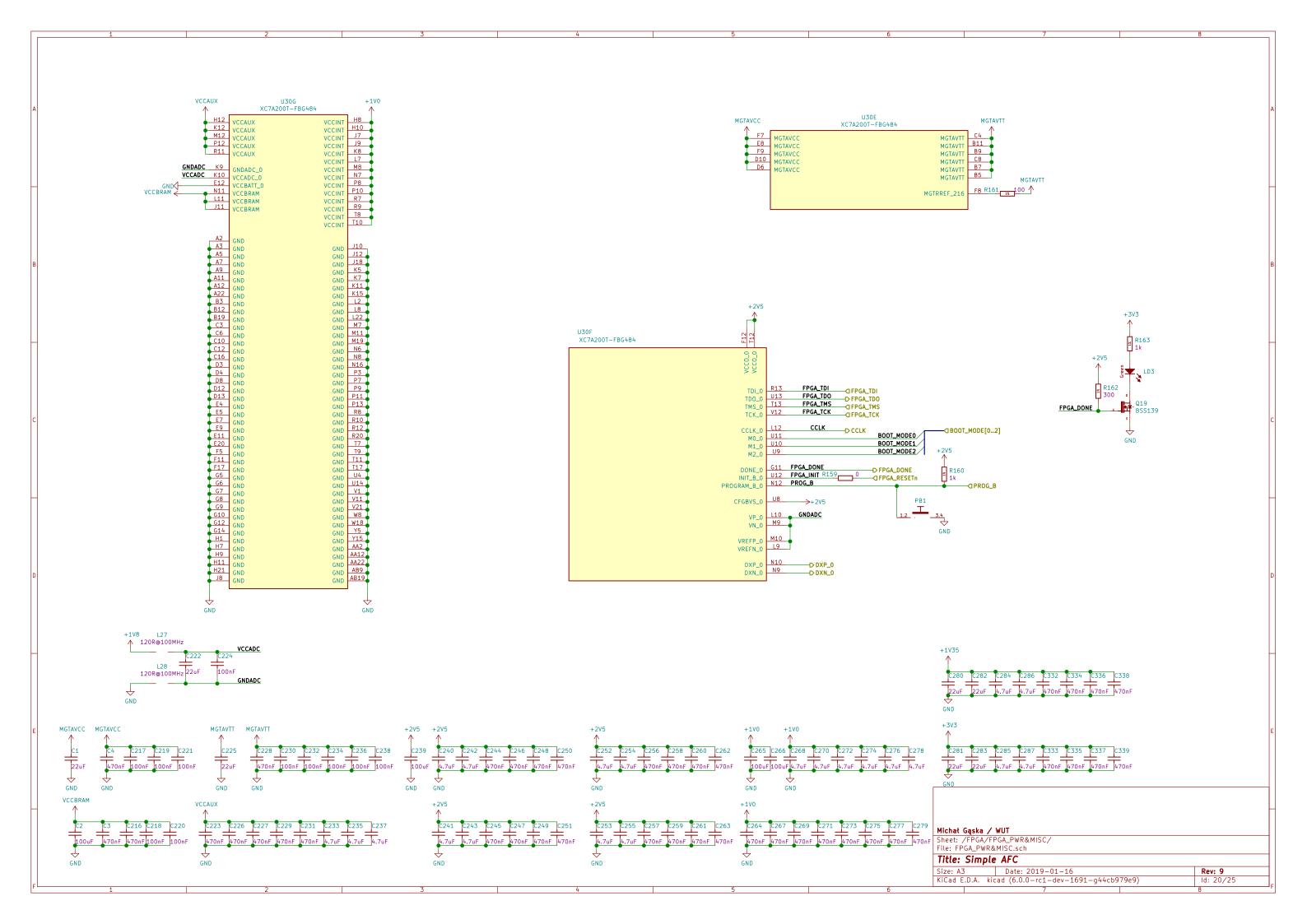


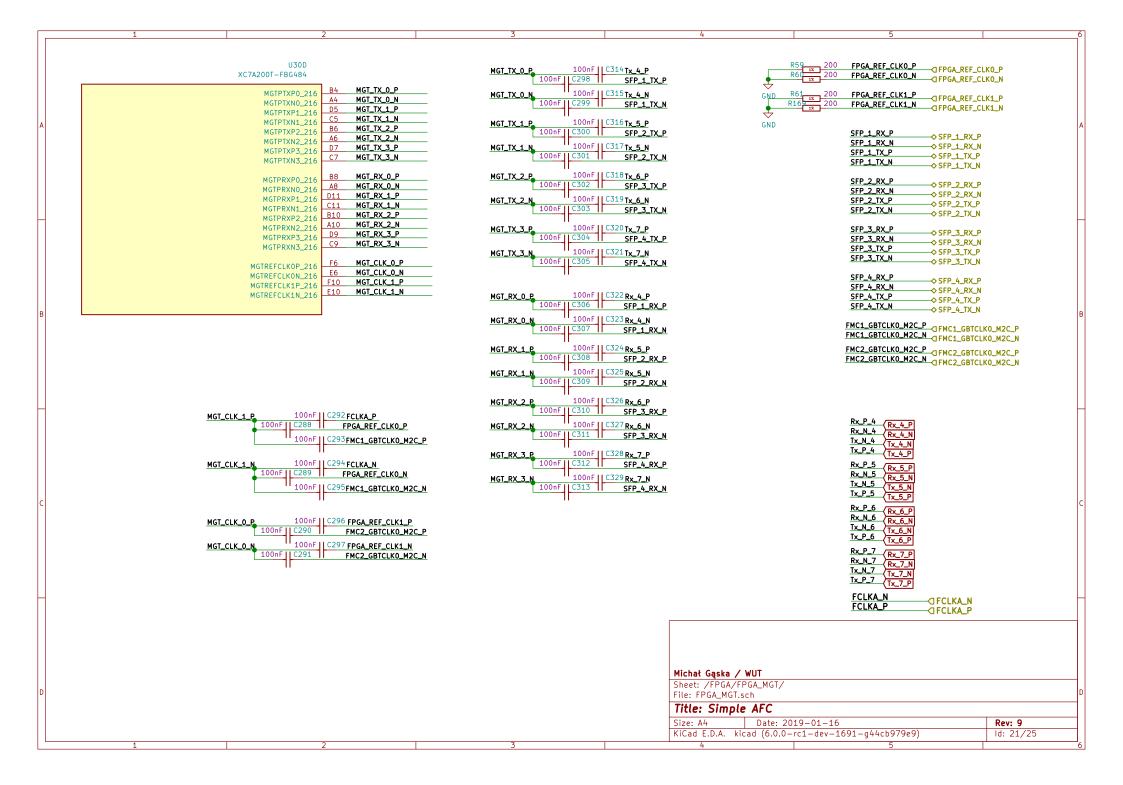


			U3 PCA95!				
2C_APP_SCL D- 2C_APP_SDA ◇-	I2C_APP_SCL I2C_APP_SDA GND +3V3 GND CHAPTER EXP_INT_2	19 20 18 23 24	>SCL SDA A0 A1 A2	P0_7 P0_6 P0_5 P0_4 P0_3 P0_2 P0_1 P0_0	8 7 6 5 4 3 2	CFG_WP_ THERM FANFAIL ALERT	B — THERM — FANFAIL — TALERT — CEN_CON_TA — EN_USB_JTA
	+3V3 C342 100nF	+3V3 121 9 25 GND	VDD VSS EP	P1_7 P1_6 P1_5 P1_4 P1_3 P1_2 P1_1 P1_0	17 16 15 14 13 12 11 10		— dsfp1_Rs — dsfp2_Rs — dsfp3_Rs — dsfp4_Rs

FPGA_MGT.sch

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Sheet: /FPGA,	/	
File: FPGA.sch		
Title: Simp	ole AFC	
Size: A3	Date: 2019-01-16	Rev: 9
KiCad E.D.A.	kicad (6.0.0-rc1-dev-1691-g44cb979e9)	ld: 19/25
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FMC2_P_LA_0 FMC2_N_LA_0 FMC2_P_LA_1 FMC2_N_LA_1 +1V35 FMC2_P_LA_2 +2V5 FMC2_N_LA_2 FMC2_P_LA_3 FMC2_N_LA_3 AA7 AB4 R5 T2 V6 W3 C1 F2 V3 W44 W44 DDR3_BA0 DDR3_BA1 → DDR3_BA[0..2] FMC2_P_LA_4 FMC2_N_LA_4 FMC2_P_LA_5 DDR3_A0 → DDR3_A[0..15] FMC2_N_LA_5 DDR3_A1 FMC2_P_LA_6 DDR3_A2 0 0 34 FMC2_N_LA_6 DDR3_A3 FMC2_LA_6_P 0_L1P_T0_34 FMC2_LA_6_N FMC2_LA_3_P DDR3_A4 DDR3_A5 FMC2_LA_6_N FMC2_LA_3_P 0 I1N T0 34 FMC2_P_LA_7 U2 FMC2_N_LA_7 B2 DDR3_DQ15 E1 DDR3_UDQS_P DDR3_A6 DDR3_A7 FMC2_LA_3_N IO_L2N_T0_AD12N_35 IO_L3P_T0_DQS_AD5P_35 _L2N_T0_34 FMC2_P_LA_8 R3 O_L3P_T0_DQS_34 FMC2_N_LA_8 DDR3_UDQS_N DDR3_A8 IO_L3N_TO_DQS_34 IO_L4P_T0_34 IO_L3N_TO_DQS_AD5N_35 IO_L4P_TO_35 FMC2 LA 8 P W2 E2 DDR3_UDM DDR3_A9 FMC2_P_LA_9 FMC2_LA_8_N
FMC2_LA_8_N DDR3_A10 O_L4N_T0_34 IO_L4N_T0_35 FMC2_N_LA_9 W1 G1 DDR3 DQ8 DDR3_A11 O_L5P_T0_34 IO_L5P_T0_AD13P_35 FMC2_LA_10_N FMC2_LA_10_N FMC2_P_LA_10 DDR3_A12 F1 DDR3_DQ10 IO_L5N_TO_AD13N_35 IO_L6P_TO_35 IO_L6N_TO_VREF_35 FMC2_LA_2_P FMC2_LA_2_N DDR3_A13 DDR3_A14 FMC2_N_LA_10 F3 DDR3_DQ11 0 L6P T0 34 E3 DDR3_DQ4 ٧3 FMC2_P_LA_11 FMC2_N_LA_11 0_L6N_T0_VREF_34 FMC2_LA_14_P DDR3_A15 0 L7P T1 34 FMC2_LA_14_N AB1 _L7N_T1_34 FMC2_P_LA_12 AB3 _L8P_T1_34 IO_L8P_T1_AD14P_35 FMC2_N_LA_12 FMC2 LA 16 N AB2 G2 DDR3 LDM L8N T1 34 IO L8N T1 AD14N 35 DDR3_CK_P DDR3_CK_N DDR3_CKE DDR3_CKE DDR3_CKE FMC2_LA_9_P FMC2_LA_9_N IO_L9P_T1_DQS_AD7P_35 K2 DDR3_LDQS_P FMC2_P_LA_13 AA3 J2 DDR3_LDQS_N _L9N_T1_DQS_34 IO_L9N_T1_DQS_AD7N_35 FMC2_LA_15_P J5 DDR3_DQ7 AA5 IO_L10P_T1_AD15P_35 H5 DDR3_DQ3 FMC2_P_LA_14 FMC2_LA_15_N FMC2_LA_15_N AB5 IO_L10N_T1_AD15N_35 IO_L11P_T1_SRCC_35 L10N T1 34 FMC2_LA_1_P FMC2_LA_1_P FMC2_LA_1_N FMC2_LA_1_N FMC2_N_LA_14 Y4 H3 DDR3_DQ6 _L11P_T1_SRCC_3 AA4 FMC2_LA_1_N FMC2_LA_1_N AA4

FMC2_CLK0_M2C_PD FMC2_CLK0_M2C_P V4

FMC2_CLK0_M2C_ND FMC2_CLK1_M2C_PD W4

FMC2_CLK1_M2C_ND FMC2_CLK1_M2C_PD PM O_L11N_T1_SRCC_34 O_L12P_T1_MRCC_34 IO_L11N_T1_SRCC_35 IO_L12P_T1_MRCC_35 FMC2_P_LA_15 H4 DDR3_DQ2 FMC2_N_LA_15 DDR3_CAS DDR3_CAS G4 DDR3_RST_N _L12N_T1_MRCC_34 IO_L12N_T1_MRCC_35 FMC2_P_LA_16 FMC2_N_LA_16 K4 DDR3_ODT IO L13P T2 MRCC 35 L13P T2 MRCC 34 J4 DDR3_RAS FMC2_LA_0_P
FMC2_LA_0_P
FMC2_LA_0_N
FMC2_LA_1_P
FMC2_LA_12_P
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FMC2_LA_12_P
FMC2_LA_12_P IO_L13N_T2_MRCC_35 DDR3_UDM
DDR3_UDM

DDR3_LDQS_P

DDR3_LDQS_P L3 DDR3_A12 K3 DDR3_A10 FMC2_P_LA_17 114P T2 SRCC 34 IO_L14P_T2_SRCC_35 IO_L14N_T2_SRCC_35 U5 _L14N_T2_SRCC_34 FMC2_N_LA_17 M1 DDR3_A4 O_L15P_T2_DQS_34 O_L15N_T2_DQS_34 IO_L15P_T2_DQS_35 IO_L15N_T2_DQS_35 FMC2_P_LA_18 L1 DDR3_CKE M3 DDR3_A5 W5 U6 FMC2_LA_4_P FMC2_N_LA_18 0_L16P_T2_34 IO_L16P_T2_35 FMC2_LA_4_N FMC2_LA_5_P FMC2_LA_4_N M2 DDR3_BA1 IO 116N T2 35 FMC2_P_LA_19 116N T2 34 FMC2_LA_5_P K6 DDR3_A3 R6 IO_L17P_T2_35 FMC2_N_LA_19 FMC2_LA_5_N FMC2_LA_11_P J6 DDR3_CAS L5 DDR3_BA0 DDR3_DQ15 DDR3_DQ14 L17N_T2_34 IO 117N T2 35 → DDR3_DQ[0..15] FMC2_P_LA_20 Y6 _L18P_T2_34 IO_L18P_T2_35 FMC2_N_LA_20 FMC2_LA_11_N L4 DDR3_A15 DDR3_DQ13)_L18N_T2_34)_L19P_T3_34 IO_L18N_T2_35 IO_L19P_T3_35 FMC2 LA 13 P DDR3 DQ12 ٧7 N4 DDR3 BA2 FMC2_P_LA_21 FMC2_N_LA_21 FMC2_LA_13_N FMC2_LA_13_N DDR3_DQ11 _L19N_T3_VREF_34 DDR3_DQ10 DDR3_DQ9 AB7 _L20P_T3_34 FMC2_P_LA_22 P1 DDR3_A8 AB6 _L20N_T3_34 IO_L20N_T3_35 FMC2_LA_7_P FMC2_LA_7_N FMC2_LA_7_N P5 DDR3_CK_P
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> Michał Gąska / WUT Sheet: /FPGA/FPGA_34_35/ File: FPGA_34_35.sch Title: Simple AFC Date: 2019-01-16 Rev: 9 KiCad E.D.A. kicad (6.0.0-rc1-dev-1691-g44cb979e9)

