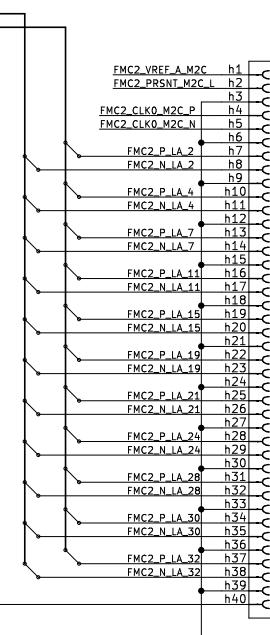
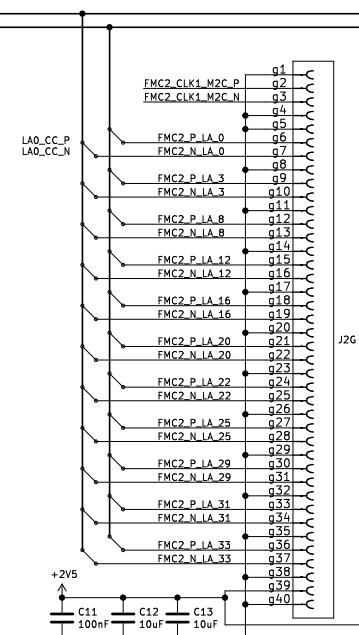
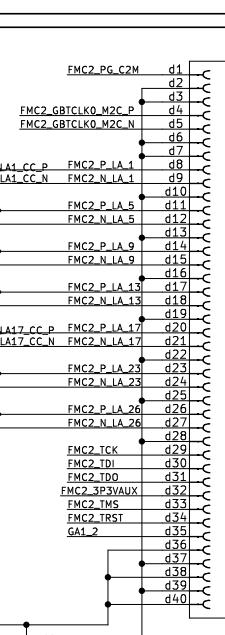
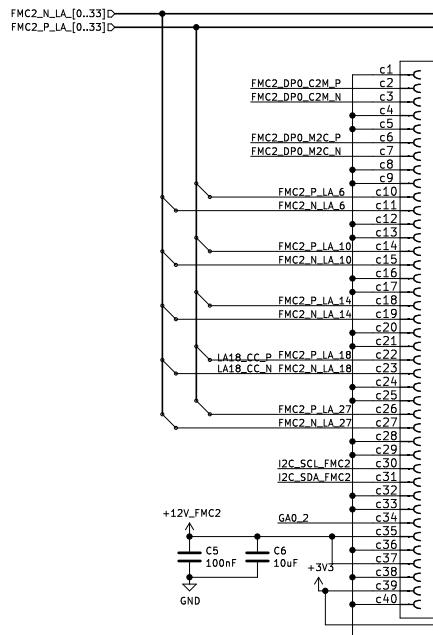


1 2 3 4 5 6 7 8

A



FMC2_PG_C2M → JFMC2_PG_C2M
FMC2_PRSNT_M2C_L → JFMC2_PRSNT_M2C_L

J2C_SCL_FMC2 → JI2C_SCL_FMC2
J2C_SDA_FMC2 → JI2C_SDA_FMC2

FMC2_TCK → JFMC2_TCK
FMC2_TDI → JFMC2_TDI
FMC2_TDO → JFMC2_TDO
FMC2_TMS → JFMC2_TMS
FMC2_TRST → JFMC2_TRST

FMC2_GBTCLK0_M2C_P → JFMC2_GBTCLK0_M2C_P
FMC2_GBTCLK0_M2C_N → JFMC2_GBTCLK0_M2C_N
FMC2_CLK1_M2C_P → JFMC2_CLK1_M2C_P
FMC2_CLK1_M2C_N → JFMC2_CLK1_M2C_N
FMC2_CLK0_M2C_P → JFMC2_CLK0_M2C_P
FMC2_CLK0_M2C_N → JFMC2_CLK0_M2C_N

GA0_2 → JGA0_2
GA1_2 → JGA1_2



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Size: A3

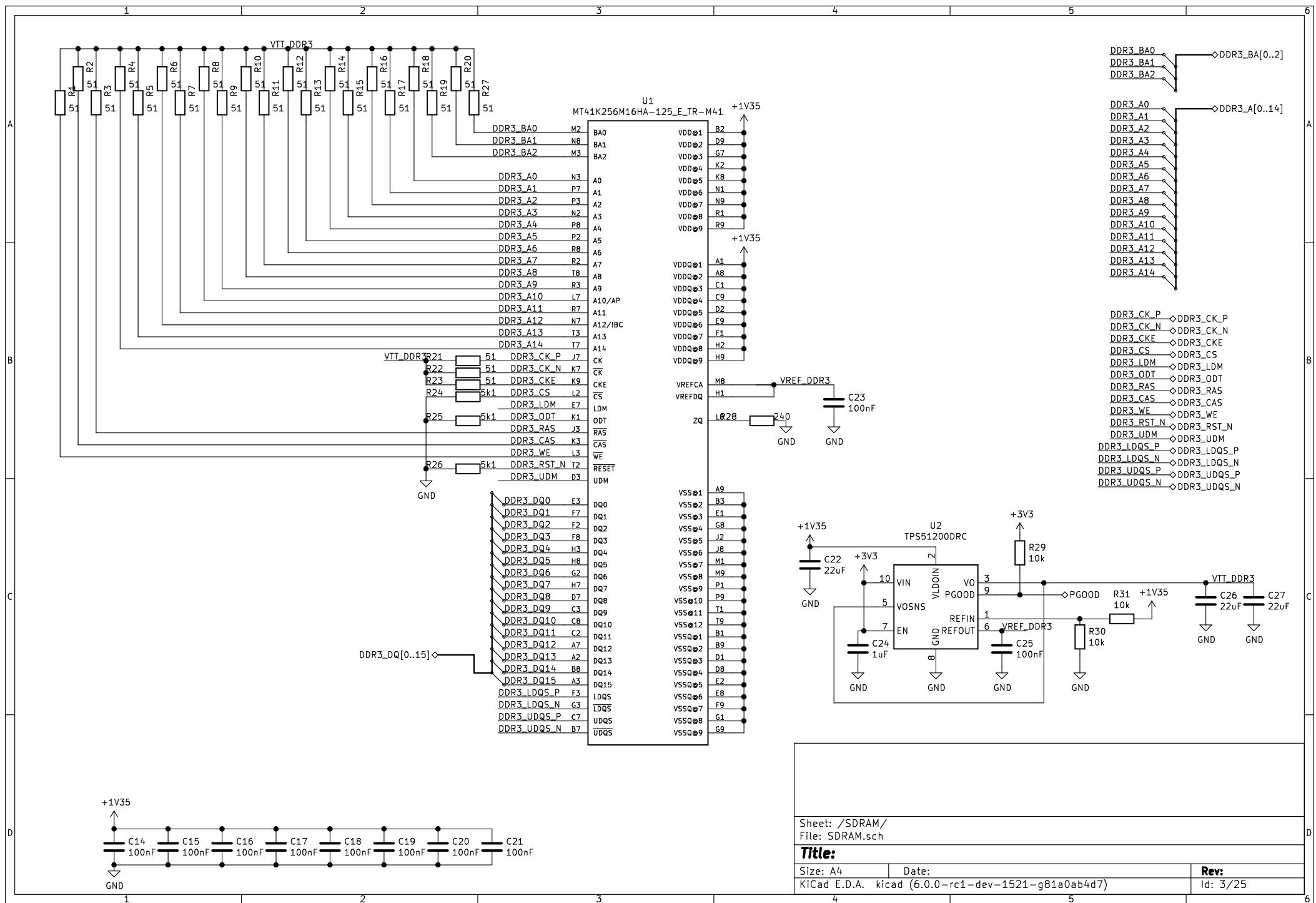
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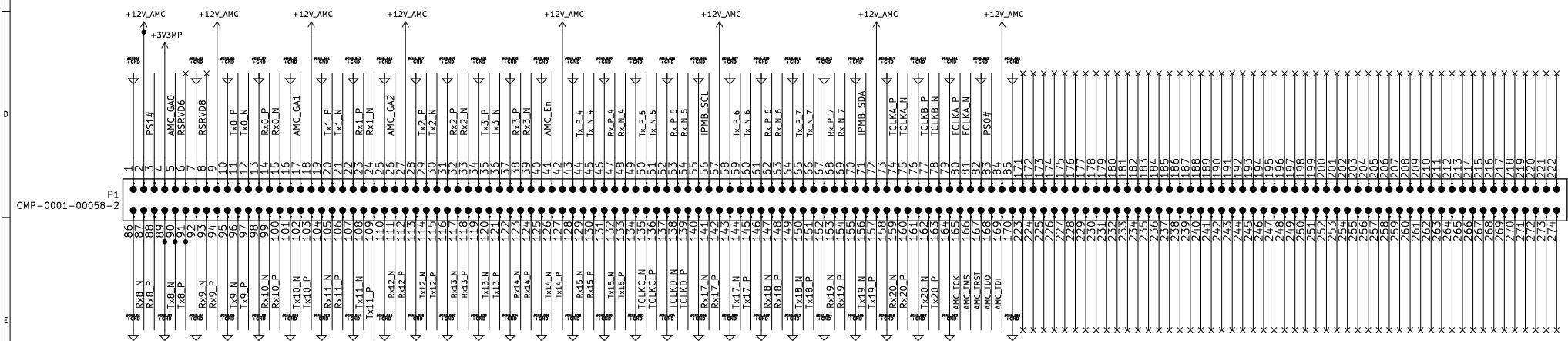
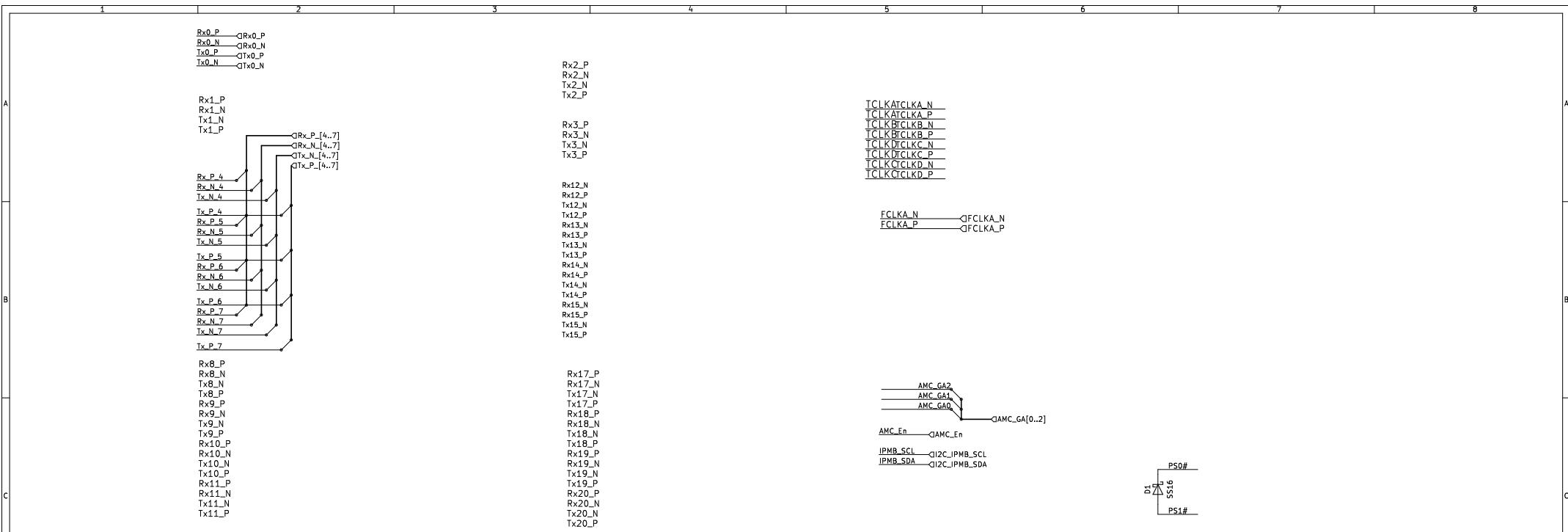
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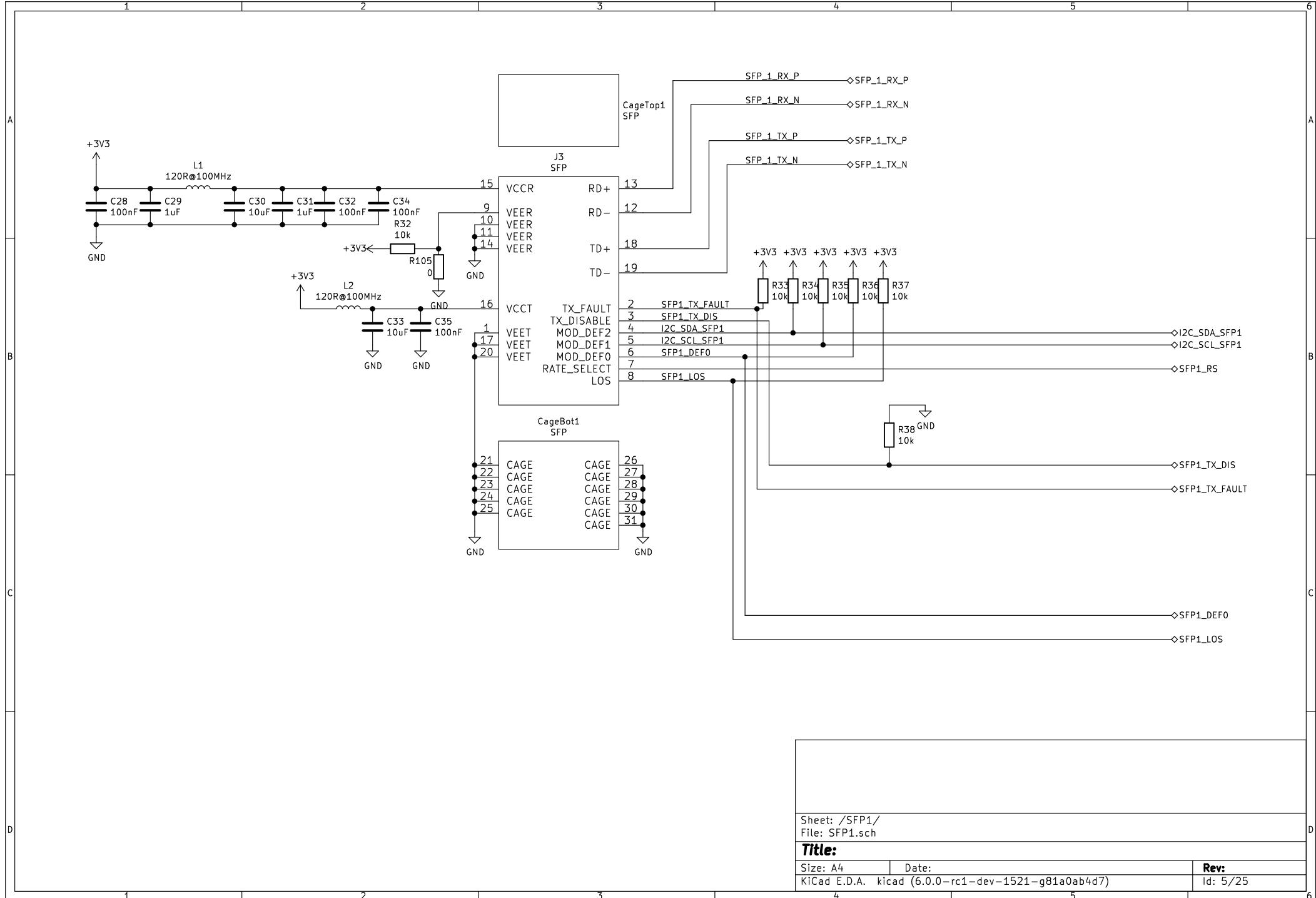




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ize: A3 Date:
iCad E.D.A. kicad (6.0.0)



A

A

B

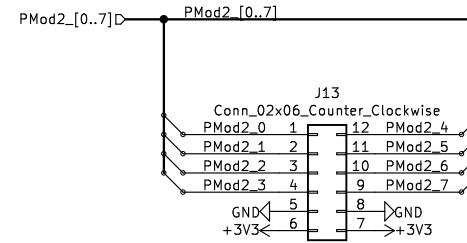
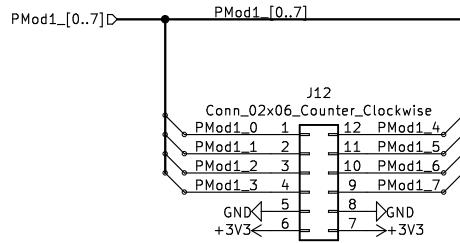
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D



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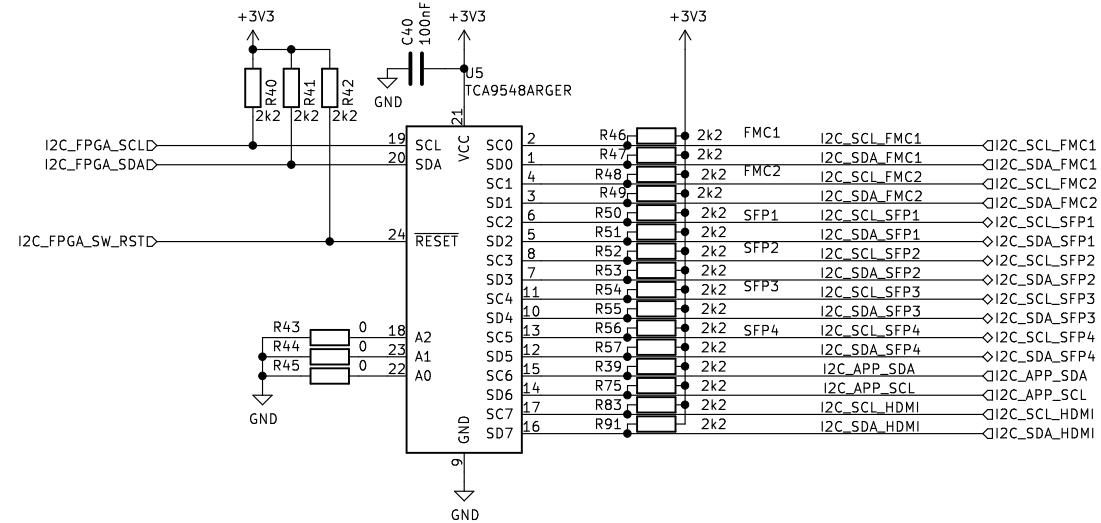
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Size: A4 Date:

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Rev:
Id: 6/25

A



B

C

D

A

B

C

D

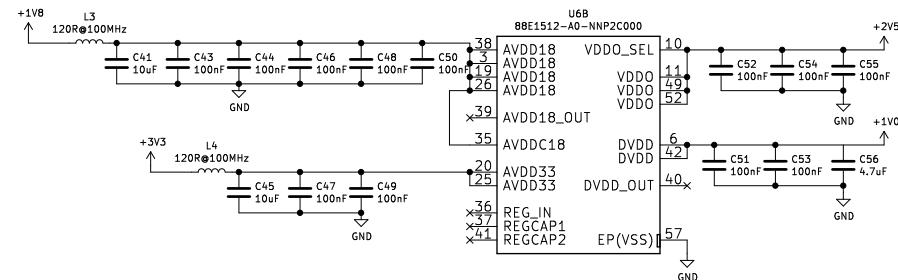
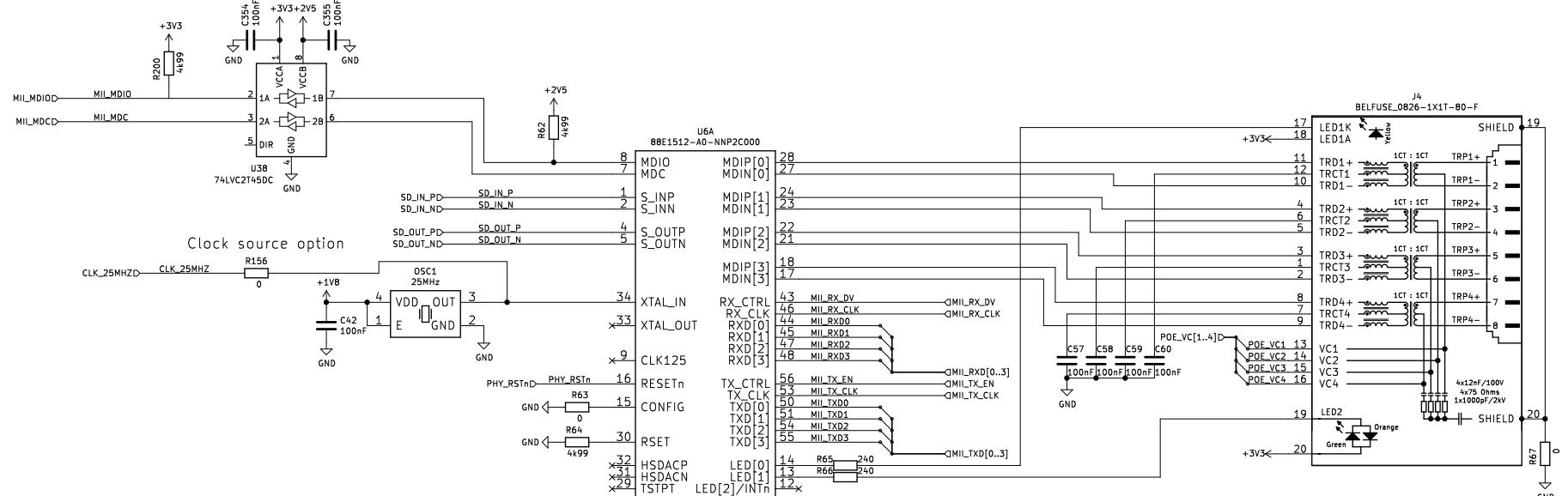
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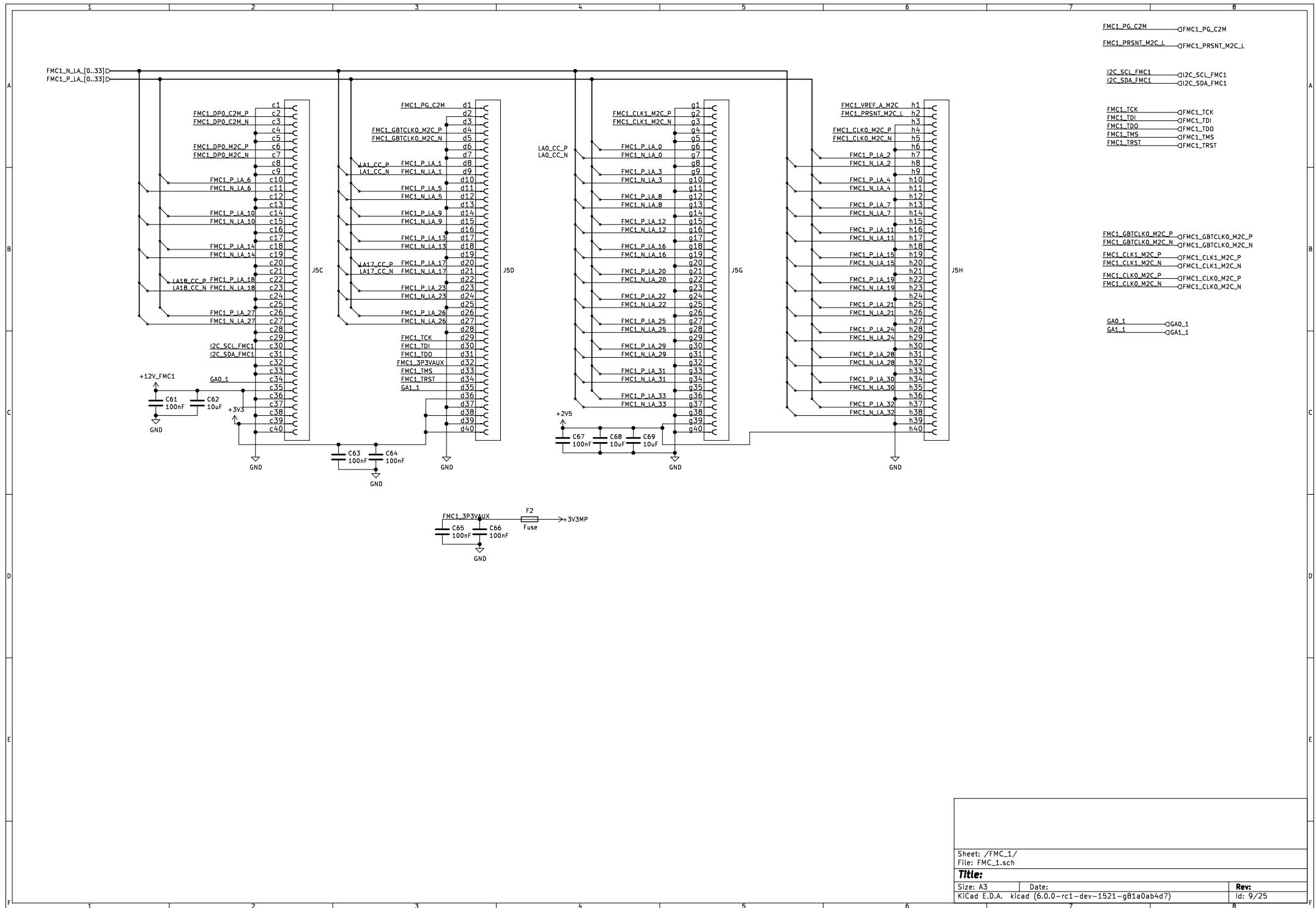
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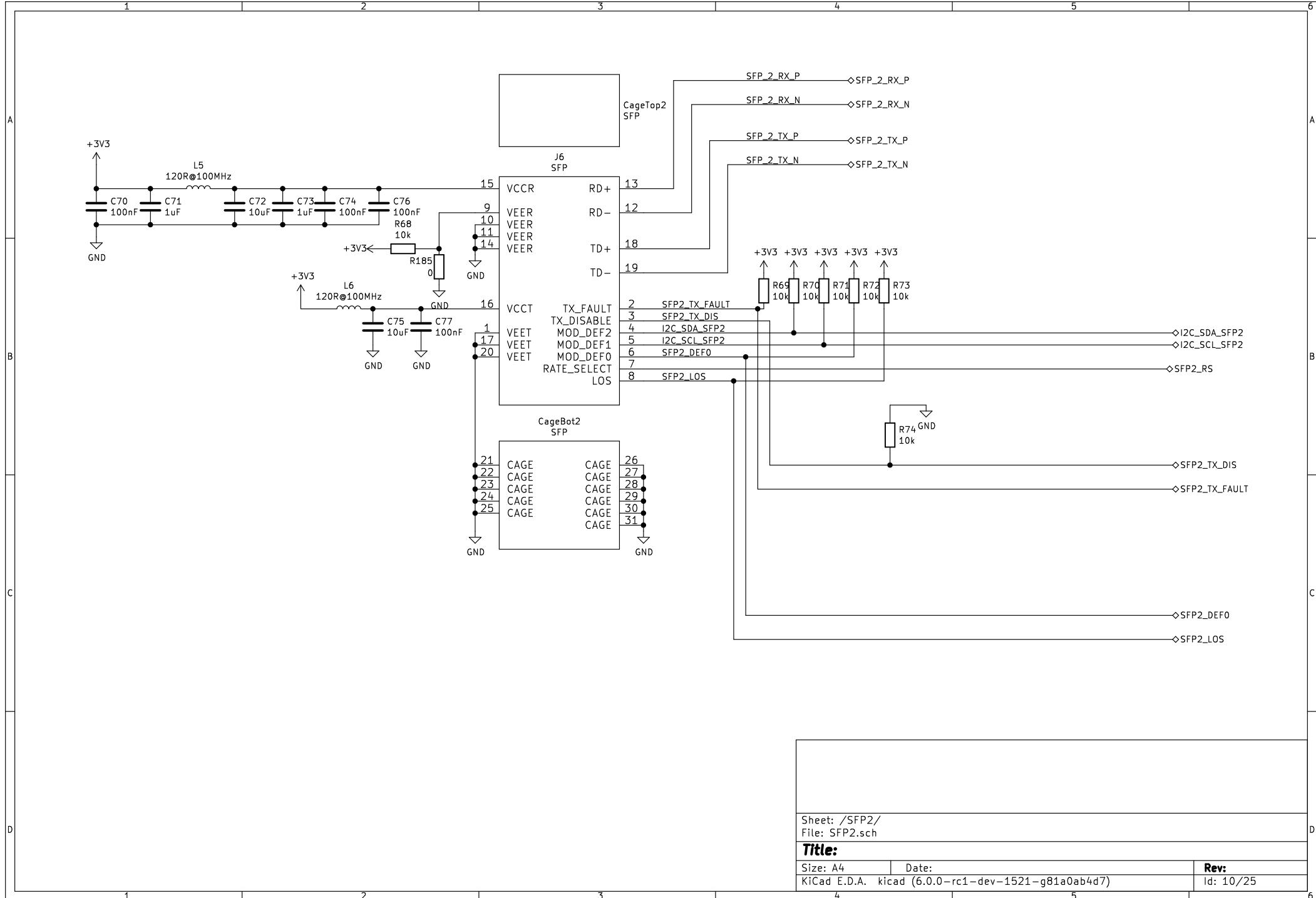
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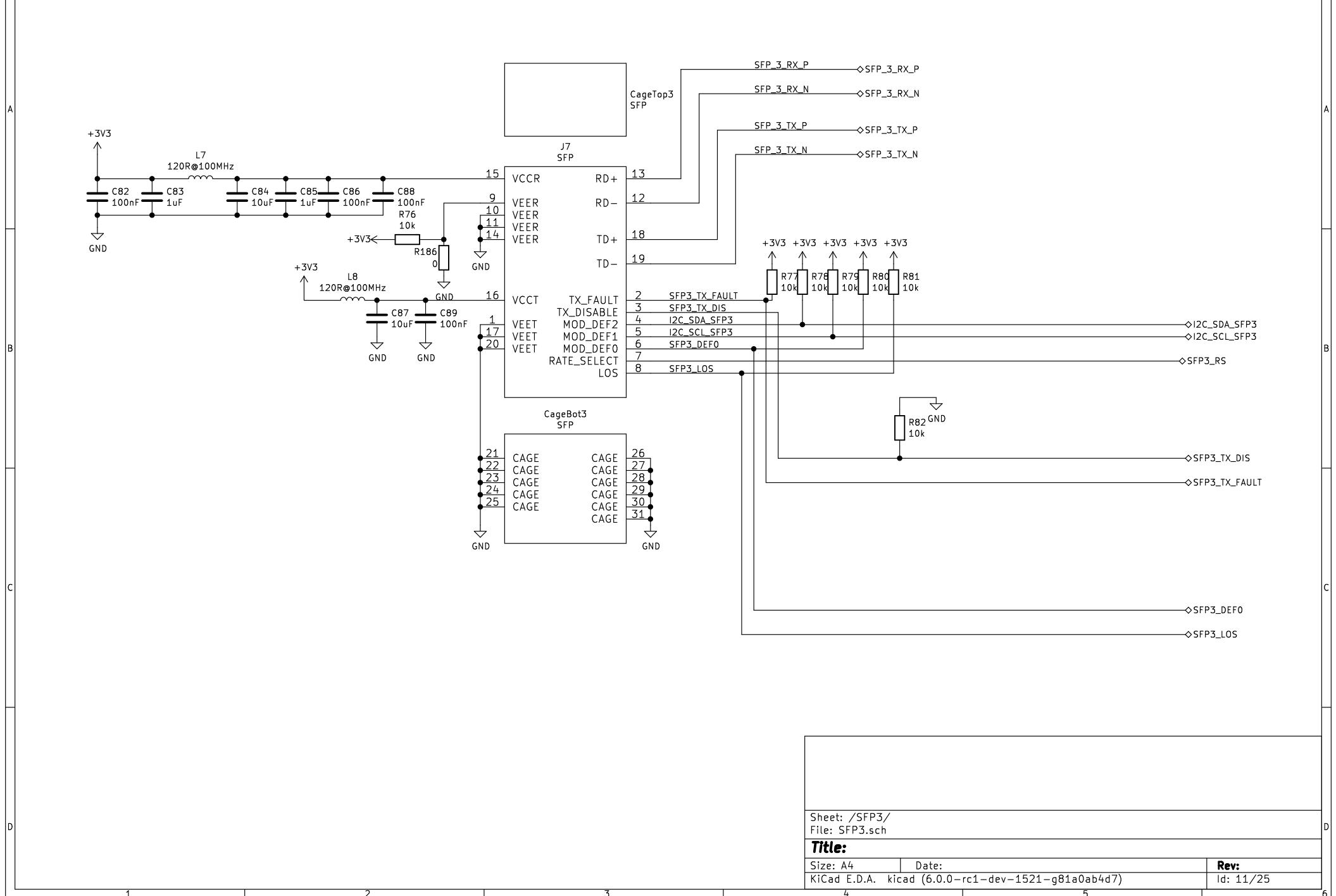
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Id: 7/25

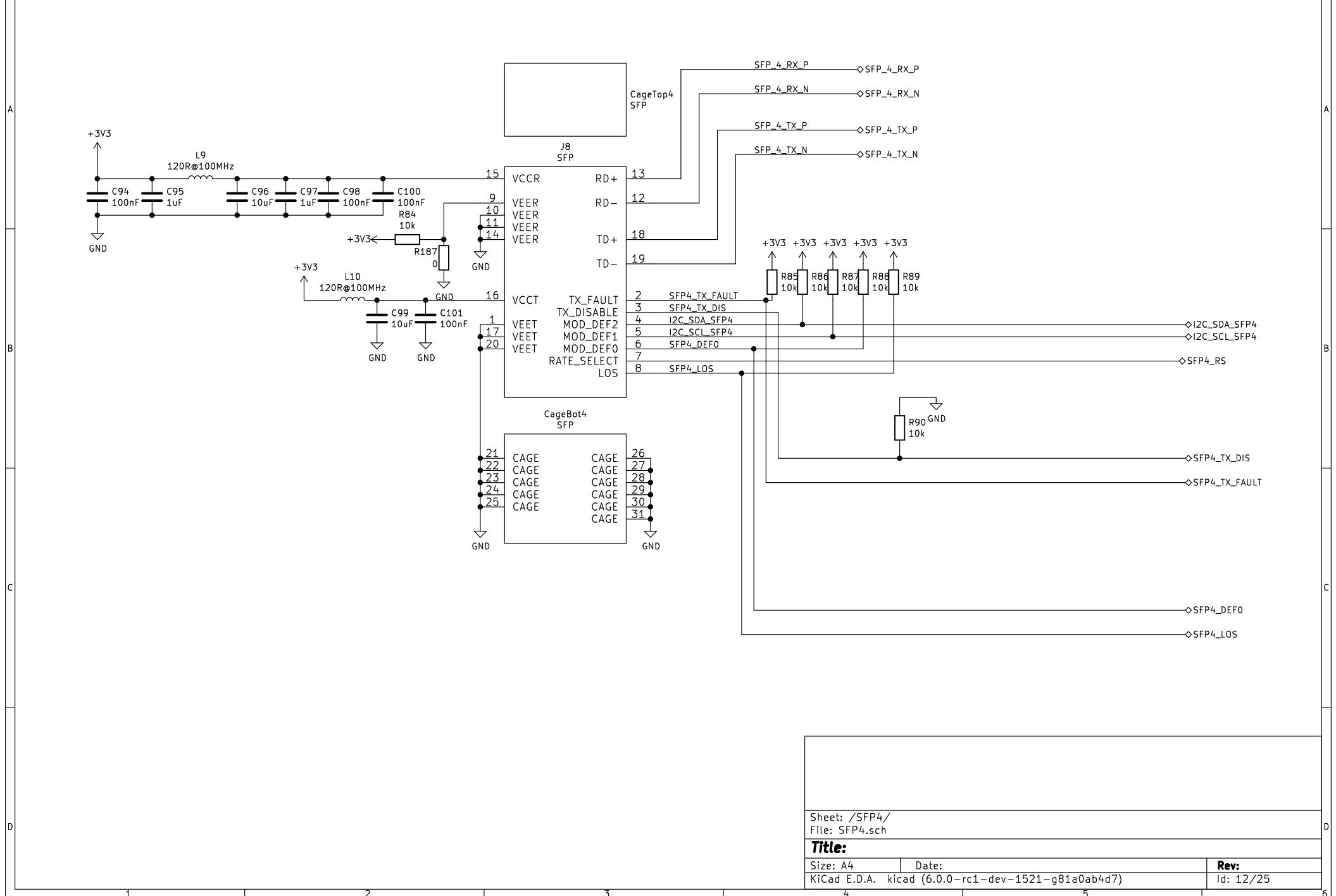


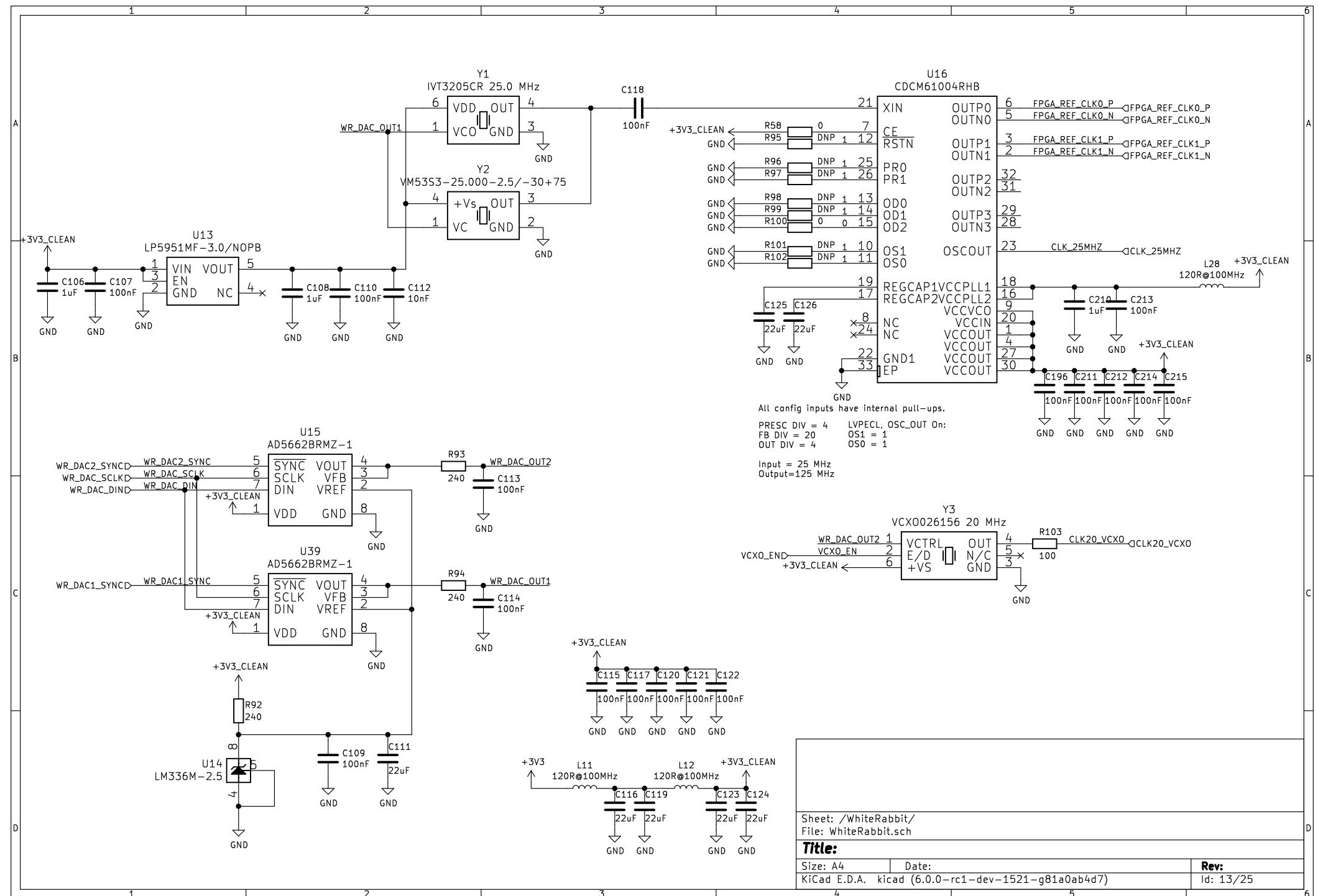
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Id: 8/25

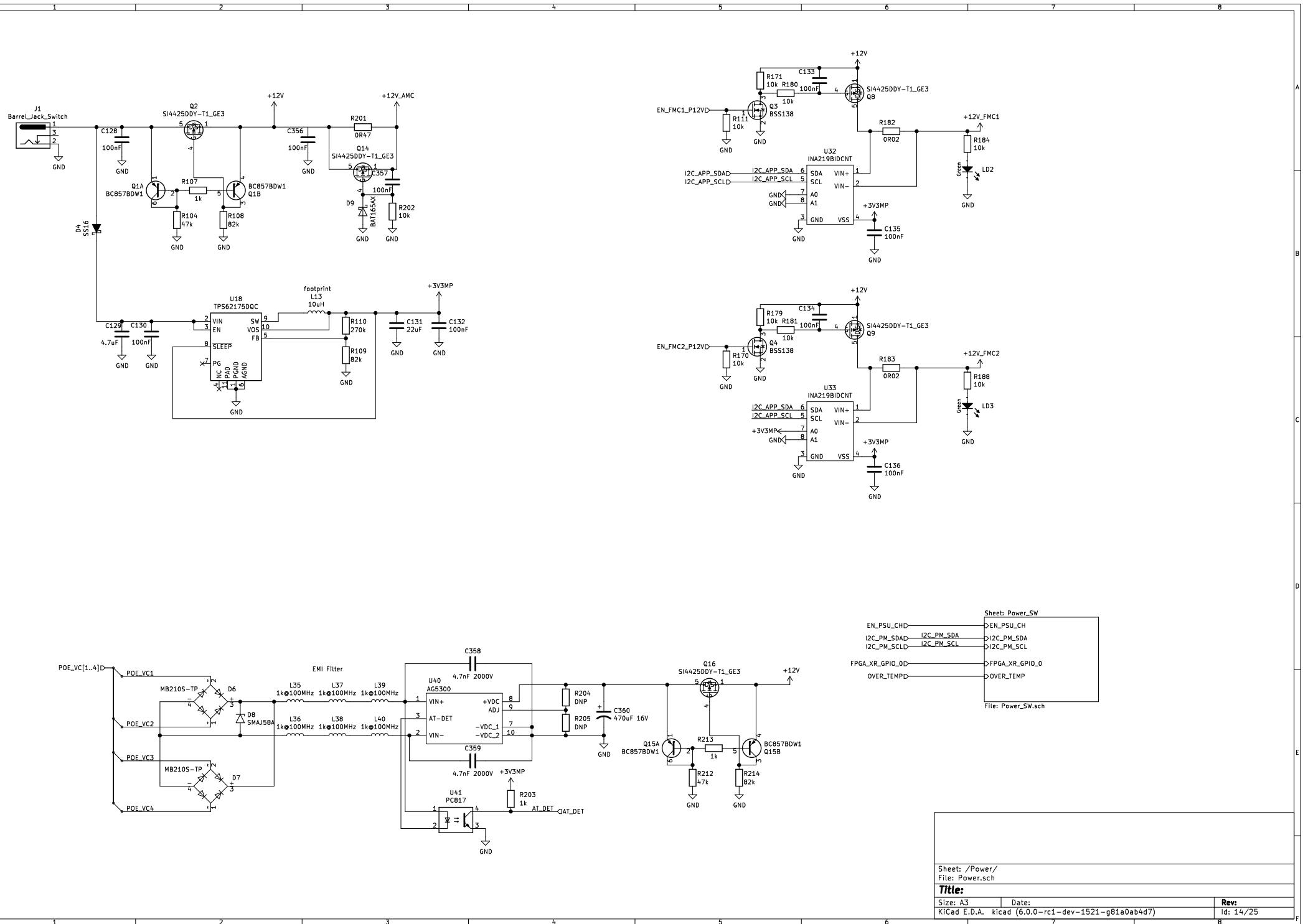


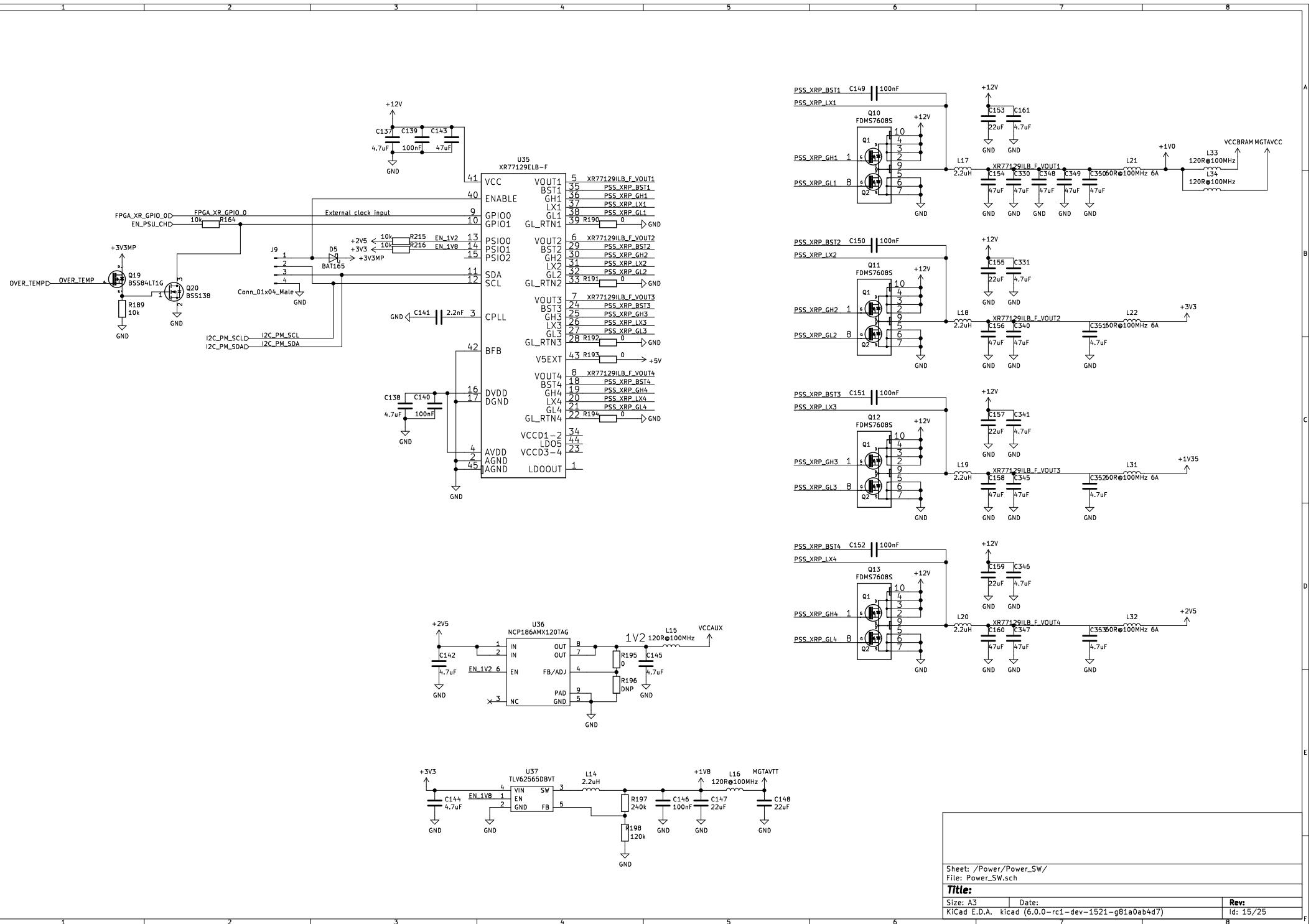


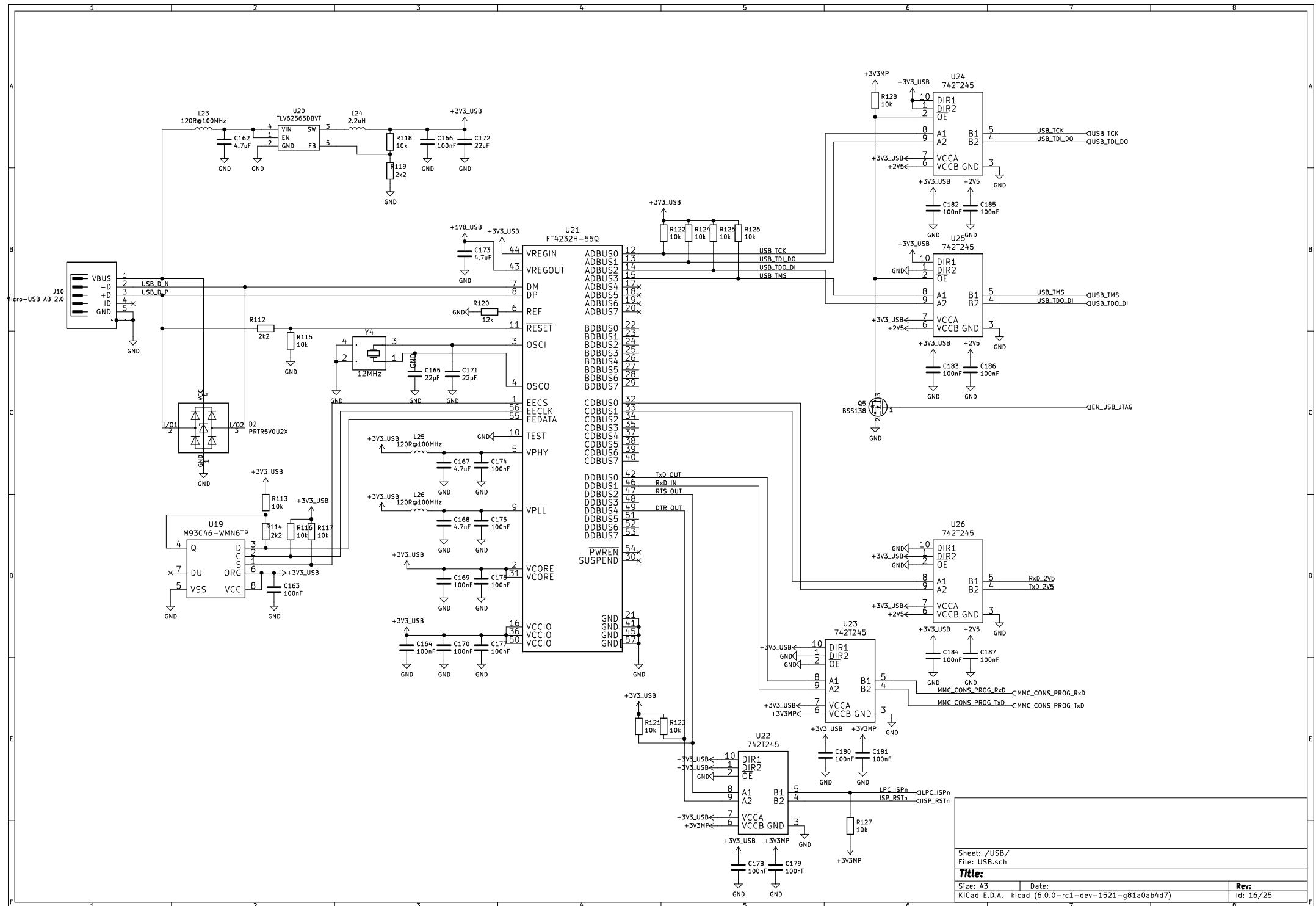




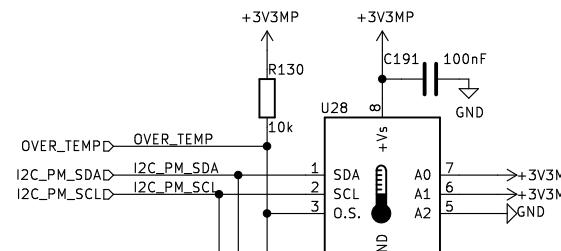




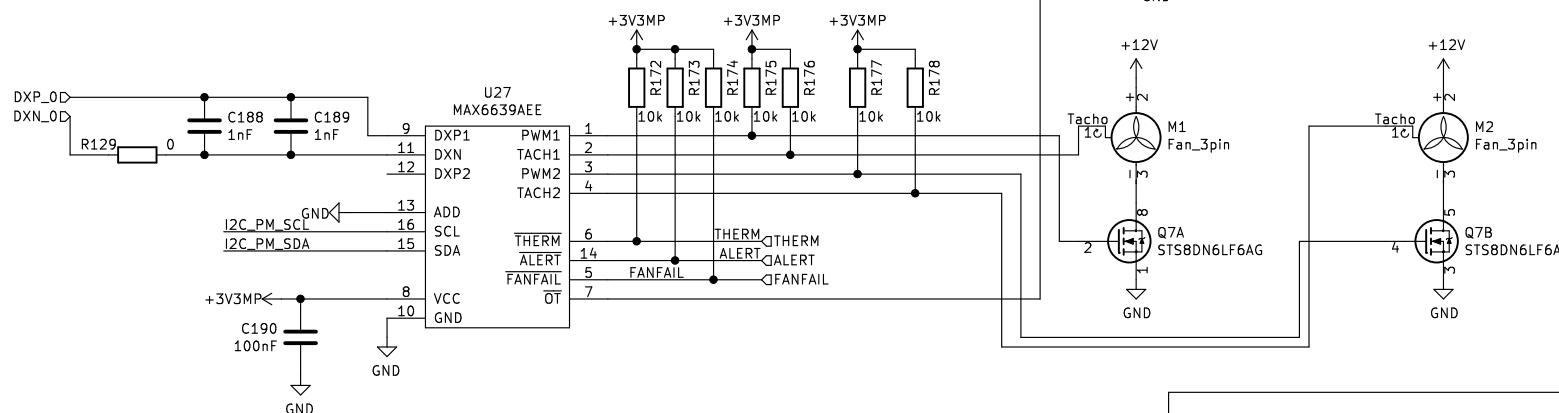
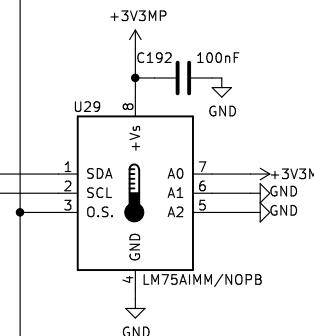




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D

Sheet: /Thermometers/
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Title:

Size: A4 Date:

KiCad E.D.A. kicad (6.0.0-rc1-dev-1521-g81a0ab4d7)

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A

USB_TCK —> USB_TCK
USB_TDI_D0 —> USB_TDI_D0
USB_TMS —> USB_TMS
USB_TDO_D1 —> USB_TDO_D1

B

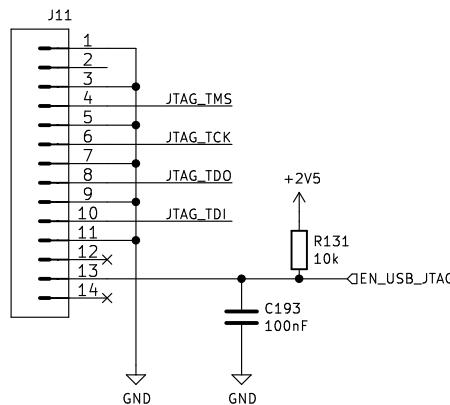
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FMC1_TDI —> FMC1_TDI
FMC1_TDO —> FMC1_TDO
FMC1_TMS —> FMC1_TMS
FMC1_TRST —> FMC1_TRST

FMC2_TCK —> FMC2_TCK
FMC2_TDI —> FMC2_TDI
FMC2_TDO —> FMC2_TDO
FMC2_TMS —> FMC2_TMS
FMC2_TRST —> FMC2_TRST

FPGA_TDI —> FPGA_TDI
FPGA_TDO —> FPGA_TDO
FPGA_TMS —> FPGA_TMS
FPGA_TCK —> FPGA_TCK

C

D



Sheet: /JTAG/
File: JTAG.sch

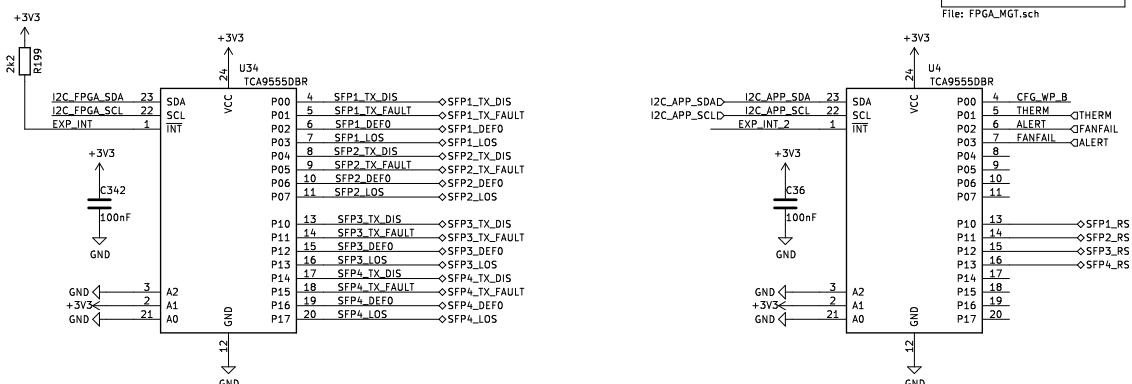
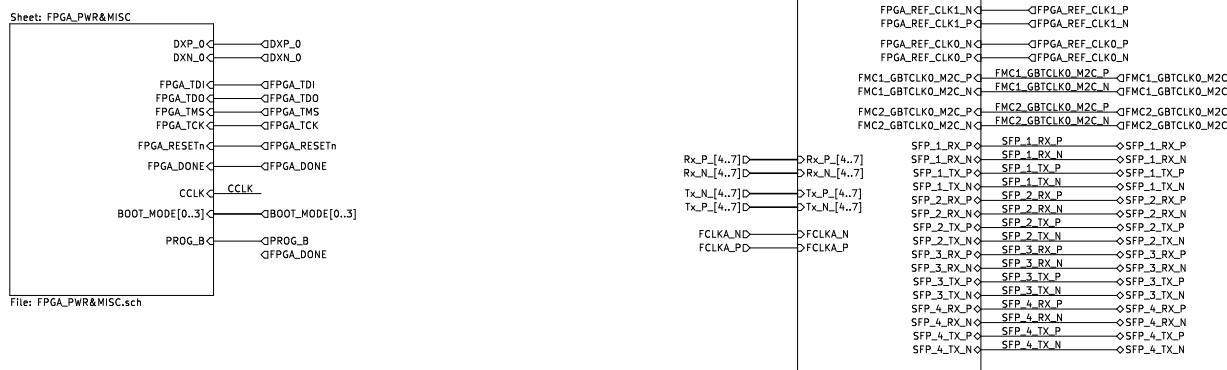
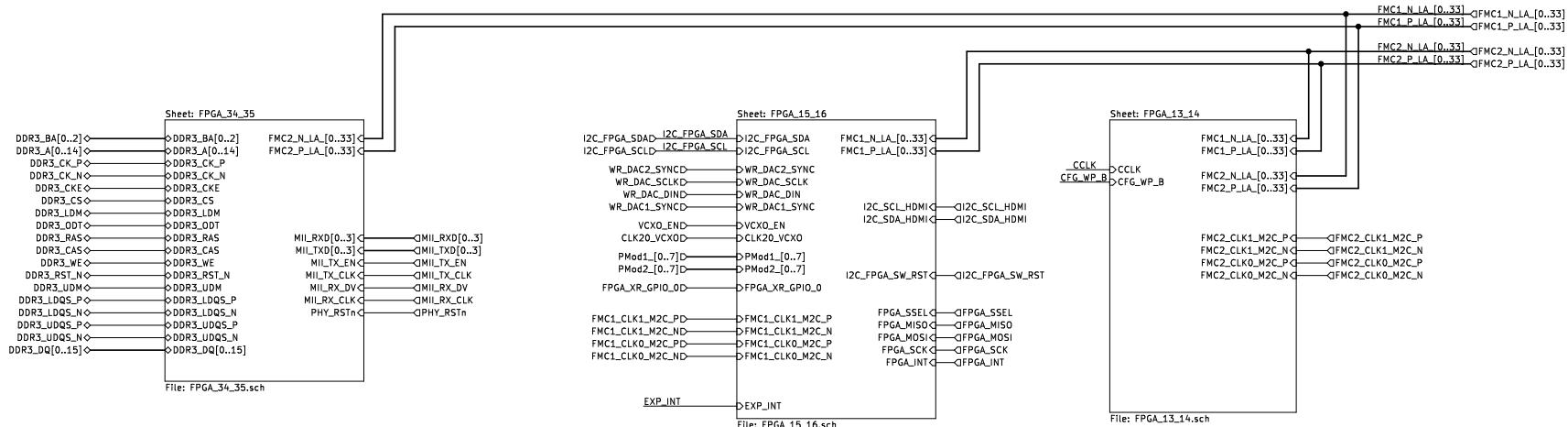
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Rev:

Id: 18/25

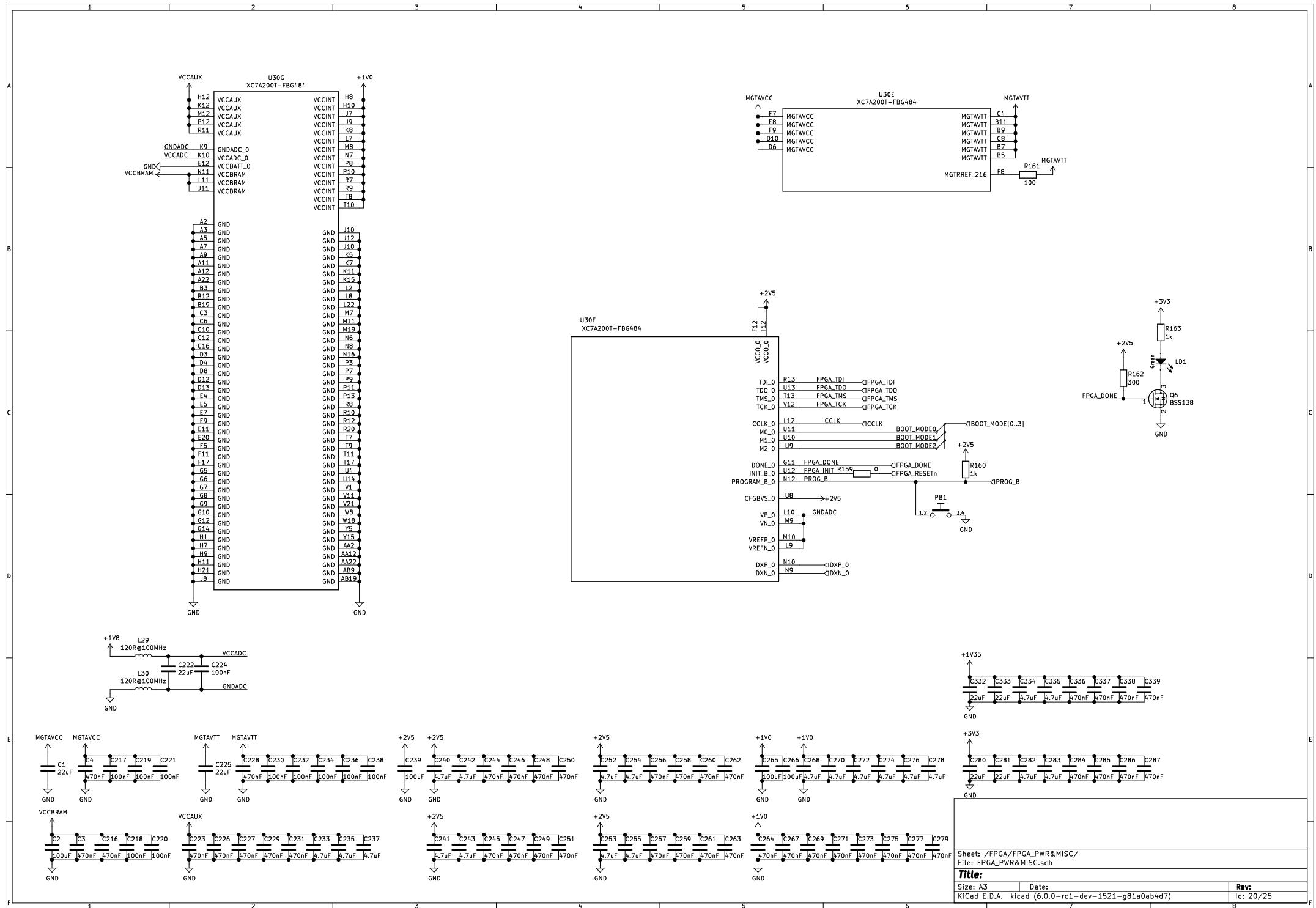


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Size: A3

KiCad E.D.A. ki



1 2 3 4 5 6

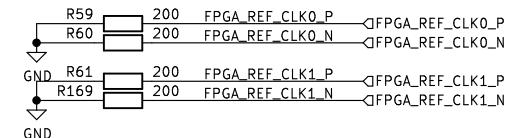
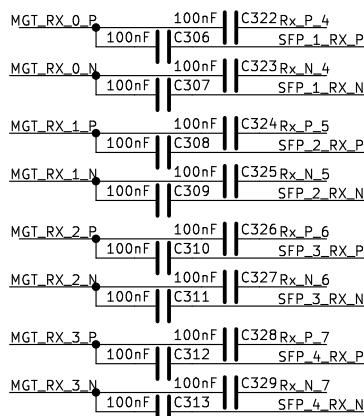
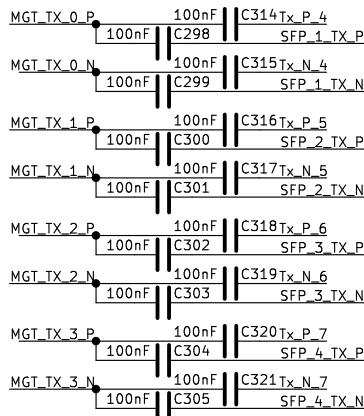
U30D
XC7A200T-FBG484

A

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D5	MGT_TX_1_P
MGTPTXP1_216	
MGPTXN1_216	
B6	MGT_TX_2_P
MGPTXP2_216	
MGPTXN2_216	
A6	MGT_TX_2_N
D7	MGT_TX_3_P
C7	MGT_TX_3_N
MGTPRXP0_216	
MGPRXN0_216	
D11	MGT_RX_1_P
C11	MGT_RX_1_N
MGPRXP1_216	
MGPRXN1_216	
B10	MGT_RX_2_P
MGPRXP2_216	
MGPRXN2_216	
A10	MGT_RX_2_N
D9	MGT_RX_3_P
C9	MGT_RX_3_N
MGTRCLK0P_216	
MGTRCLK0N_216	
F6	MGT_CLK_0_P
E6	MGT_CLK_0_N
F10	MGT_CLK_1_P
E10	MGT_CLK_1_N

B

MGT_CLK_0_P	100nF C292FCLKA_P
	100nF C288 FPGA_REF_CLK0_P
MGT_CLK_0_N	100nF C293FMC1_GBTCLK0_M2C_P
	100nF C289 FPGA_REF_CLK0_N
MGT_CLK_1_P	100nF C296 FPGA_REF_CLK1_P
	100nF C290 FMC2_GBTCLK0_M2C_P
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	100nF C291 FMC2_GBTCLK0_M2C_N



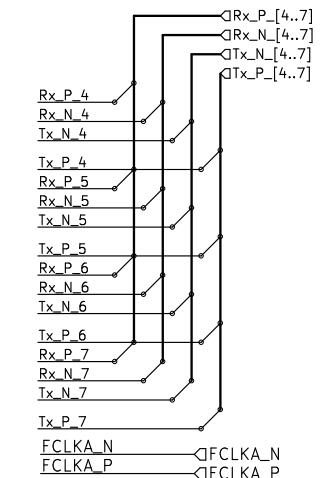
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SFP_1_RX_N → SFP_1_RX_N
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SFP_1_TX_N → SFP_1_TX_N

SFP_2_RX_P → SFP_2_RX_P
SFP_2_RX_N → SFP_2_RX_N
SFP_2_TX_P → SFP_2_TX_P
SFP_2_TX_N → SFP_2_TX_N

SFP_3_RX_P → SFP_3_RX_P
SFP_3_RX_N → SFP_3_RX_N
SFP_3_TX_P → SFP_3_TX_P
SFP_3_TX_N → SFP_3_TX_N

SFP_4_RX_P → SFP_4_RX_P
SFP_4_RX_N → SFP_4_RX_N
SFP_4_TX_P → SFP_4_TX_P
SFP_4_TX_N → SFP_4_TX_N

FMC1_GBTCLK0_M2C_P → FMC1_GBTCLK0_M2C_P
FMC1_GBTCLK0_M2C_N → FMC1_GBTCLK0_M2C_N
FMC2_GBTCLK0_M2C_P → FMC2_GBTCLK0_M2C_P
FMC2_GBTCLK0_M2C_N → FMC2_GBTCLK0_M2C_N



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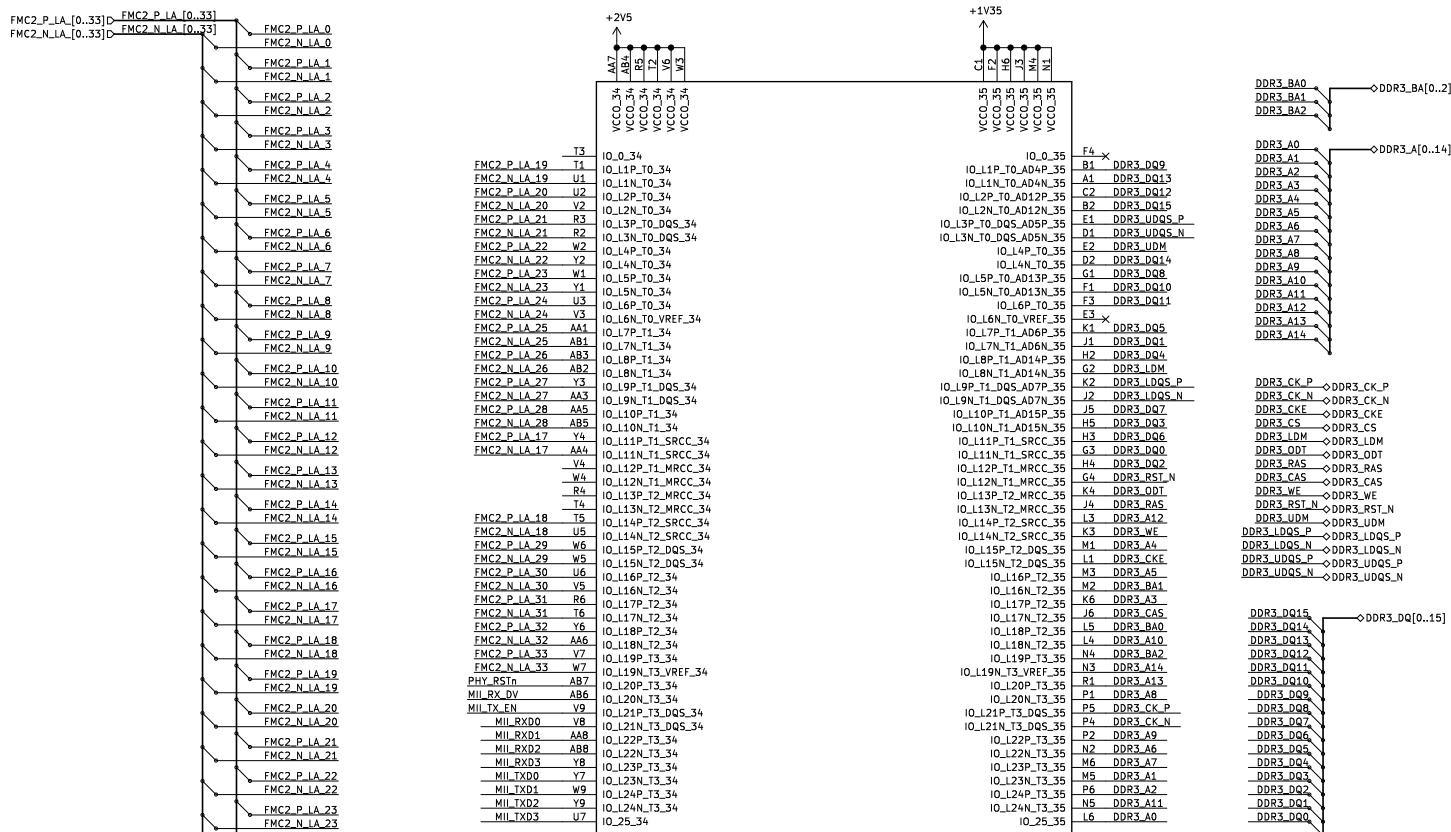
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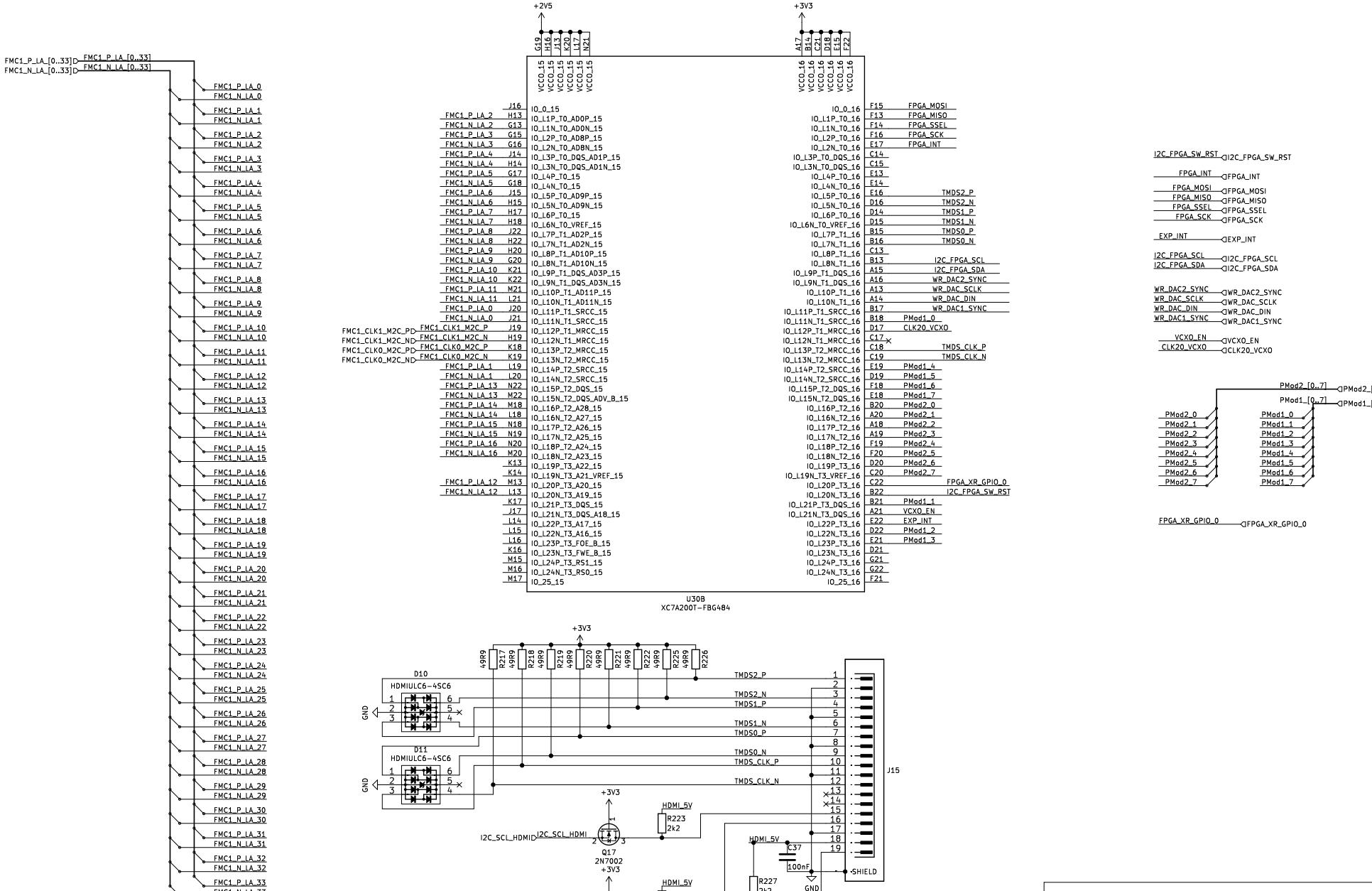
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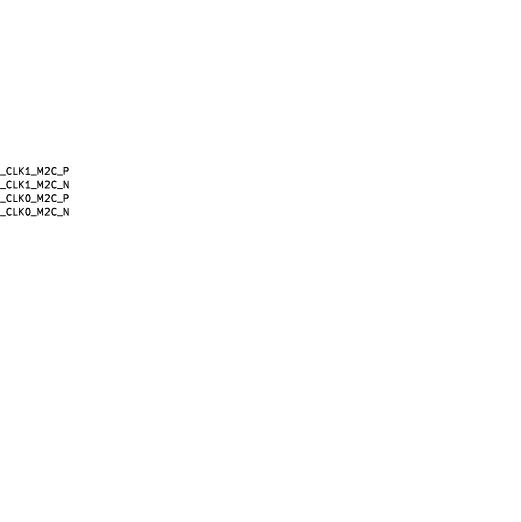
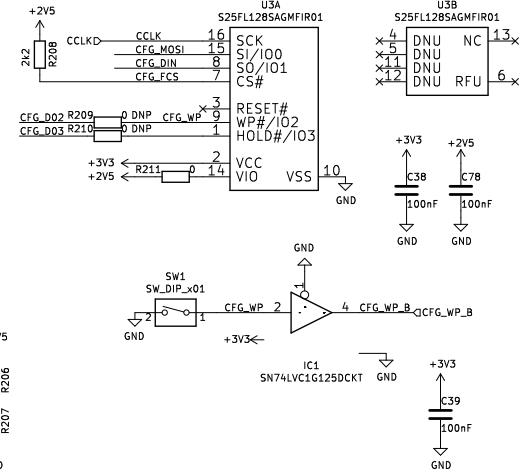
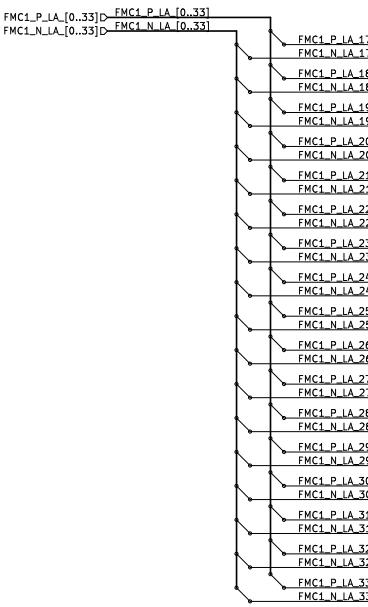
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Size: A3	Date:	Rev:
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