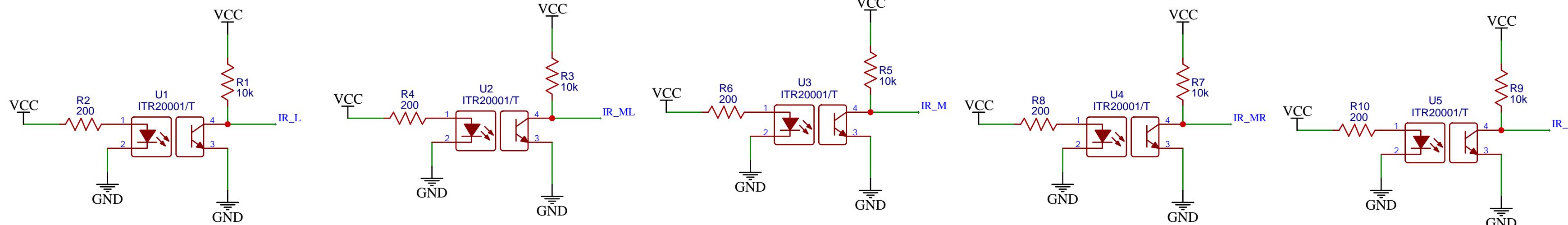


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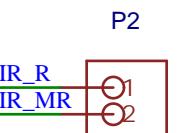
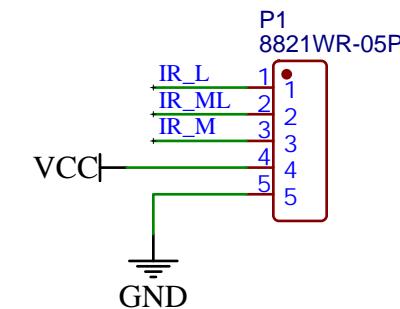
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PORT1 PORT2



TITLE:

LTR-PCB-v2.0

REV: 1.0

EasyEDA

Company: Your Company

Sheet: 1/1

Date: 2025-11-01 Drawn By: b38543793