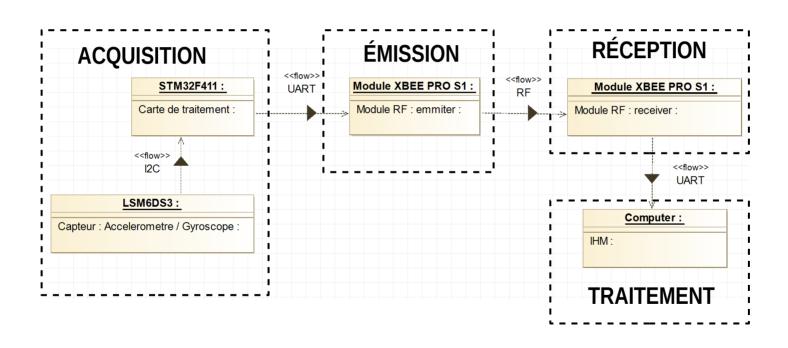
# Projet EMB

Mise en service du LSM6DS3

## ARCHITECTURE DU SYSTÈME



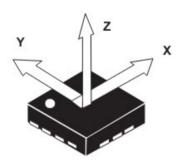
## **ACQUISITION**

Le LSM6DS3: (STMicroelectronics)

Accéléromètre 3D

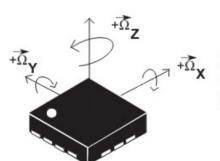
Type: MEMS

Range :±2/±4/±8/±16 g



(TOP VIEW)
DIRECTION OF THE
DETECTABLE
ACCELERATIONS

**Gyroscope 3D** ±125/±245/±500/±1000/±2000 dps.



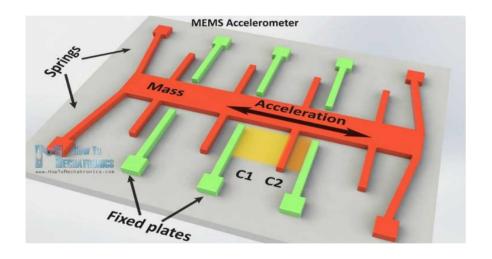
(TOP VIEW)
DIRECTIONS OF THE
DETECTABLE
ANGULAR RATES



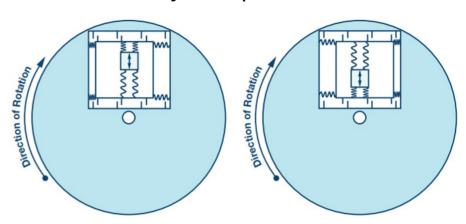
Module Grove - 3-Axis Digital Accelerometer v1.0

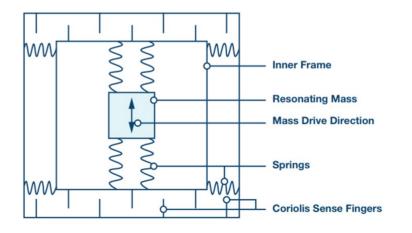
### Principe Physique

#### Accéléromètre 3D



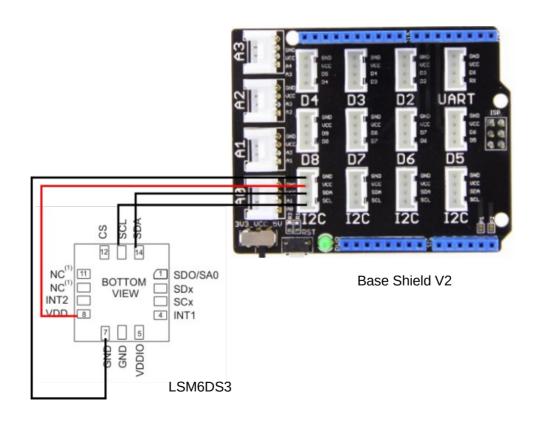
#### Gyroscope 3D





## **COMMUNICATION 12C**

#### Hardware



#### Déterminer le SAD

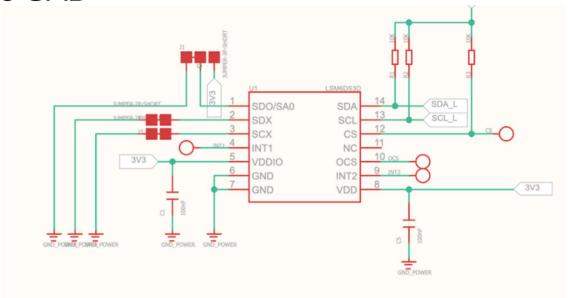
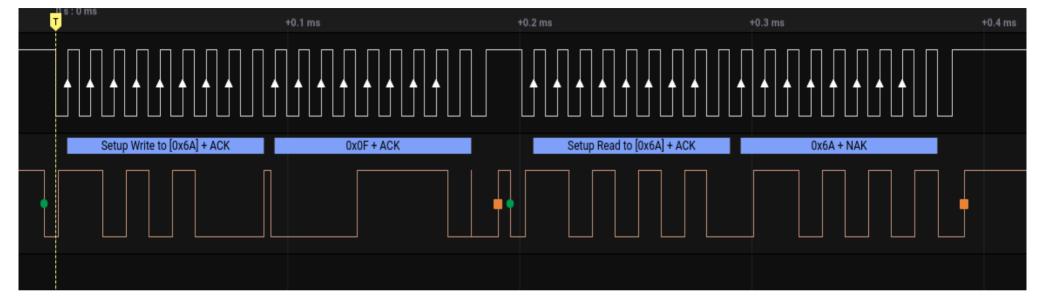


Table 11. SAD+Read/Write patterns

Command	SAD[6:1]	SAD[0] = SA0	R/W	SAD+R/W
Read	110101	0	1	11010101 (D5h)
Write	110101	0	0	11010100 (D4h)
Read	110101	1	1	11010111 (D7h)
Write	110101	1	0	11010110 (D6h)

#### Lecture d'un registre

```
int main(void)
    HAL Init(); // passage par stm32f4xx hal msp.c : configuration des broches
    SystemClock Config();
    uart2_Init(); // CABLE
    i2c1_Init();  // Modifier stm32f4xx_hal_msp.c pour configurer les broches
    HAL Delay(500);
    uart6 Init();
      TEST 12C
    // LECTURE WHOAMI
    uint16 t slaveAdd = 0xD5;
    uint16_t rdata[1];
    uint16_t wdata[1];
    rdata[0] = 0x00;
    wdata[0] = 0x00;
    i2c1 ReadRegBuffer(slaveAdd, 0x0F, rdata, 1);
    term printf6("data reg = %d \n\r",rdata[0]);
```



Capture d'une trame I2C (Read) avec un Analyseur logique et Logic

Master	ST	SAD + W		SUB		SR	SAD + R			NMAK	SP
Slave			SAK		SAK			SAK	DATA		

Communication I2C : LSMDS3 datasheet

#### Software

```
uint16 t GC data[1];
GC data[0] = 0x4C;
i2c1 WriteRegBuffer(0xD4, LSM6DS3 CTRL2 G , GC data, 1);
uint16 t AC data[1];
AC data[0] = 0x4A;
i2c1 WriteRegBuffer(0xD4, LSM6DS3 CTRL1_XL , AC_data, 1);
uint16 t GP data[1];
GP data[0] = 0 \times 00;
i2c1 WriteRegBuffer(0xD4, LSM6DS3 CTRL7 G , GP data, 1);
uint16 t ODR data[1];
ODR data[0] = 0 \times 09;
i2c1 WriteRegBuffer(0xD4, LSM6DS3 CTRL8 XL , ODR data , 1);
uint16 t redata[1];
redata[0]= 0x00;
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 CTRL8 XL, redata, 1);
    term printf6("data reg = %d",redata[0]);
```

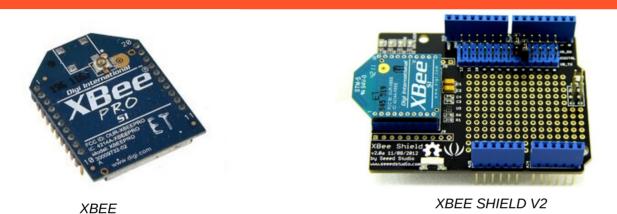
```
uint8_t buffer[1];
int8_t buffer00[1];
uint8_t buffer1[1];
int8_t buffer01[1];
uint8_t buffer2[1];
int8_t buffer02[1];
float gx, gy, gz, ax, ay, az;
```

Code C++ (3)

```
while(1)
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 OUTX L G, buffer, 1);
i2c1_ReadRegBuffer(slaveAdd, LSM6DS3_OUTX_H_G, buffer00, 1);
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 OUTY L G, buffer1, 1);
i2c1_ReadRegBuffer(slaveAdd, LSM6DS3_OUTY_H_G, buffer01, 1);
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 OUTZ L G, buffer2, 1);
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 OUTZ H G, buffer02, 1);
gx = (((int16_t)buffer00[0] << 8)| buffer [0]) * 2000.0 / 32768.0;
gy = (((int16_t)buffer01[0] << 8)| buffer1 [0]) * 2000.0 / 32768.0;
gz = (((int16 \ t)buffer02[0] << 8)| buffer2 [0]) * 2000.0 / 32768.0;
```

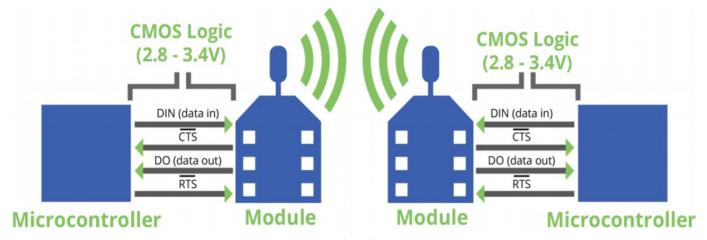
```
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 OUTX L XL, buffer, 1);
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 OUTX H XL, buffer00, 1);
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 OUTY L XL, buffer1, 1);
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 OUTY H XL, buffer01, 1);
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 OUTZ L XL, buffer2, 1);
i2c1 ReadRegBuffer(slaveAdd, LSM6DS3 OUTZ H XL, buffer02, 1);
ax = (float)(((int16_t)buffer00[0] << 8)| buffer [0]) * (4.0 / 32768.0);
ay = (float)(((int16\ t)buffer01[0] << 8)| buffer1 [0]) * (4.0 / 32768.0);
az = (float)(((int16_t)buffer02[0] << 8)| buffer2 [0]) * (4.0 / 32768.0);
term printf6("gx=%f, gy=%f, gz=%f, ax=%f, ay=%f, az=%f \n\r",gx , gy, gz, ax, ay, az);
    HAL Delay(1000); // 1000 ms
return 0;
```

## LIAISON ZIGBEE



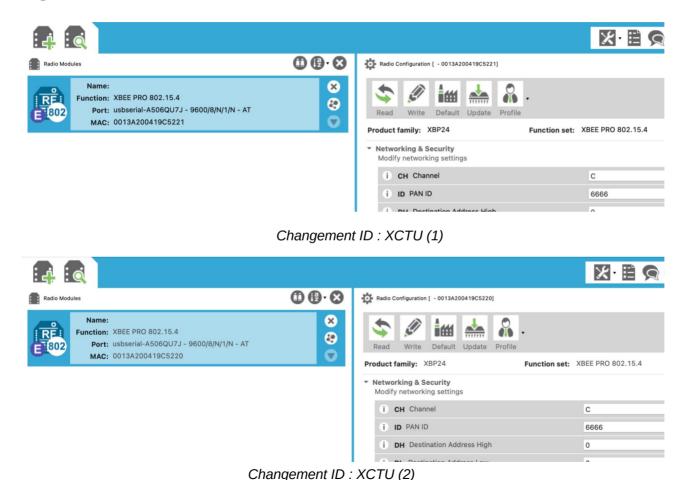


XBEE SHIELD USB

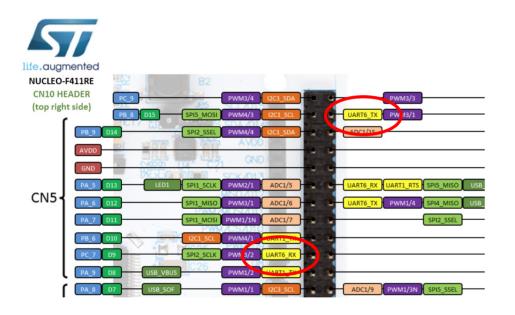


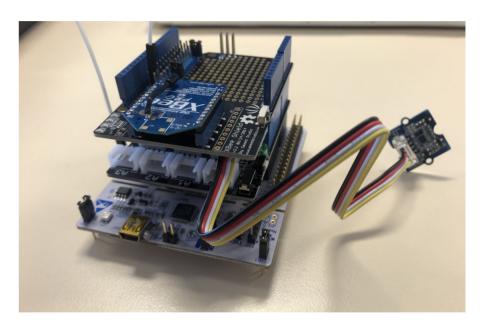
ZIGBEE datasheet

#### Paramétrage des identifiants



#### Hardware





#### Initialisation de UART6

```
void uart2_Init(void);
void uart6_Init(void);
void term_printf2(const char* fmt, ...);
void term_printf_stlink2(const char* fmt, ...);
void term_printf6(const char* fmt, ...);
void term_printf_stlink6(const char* fmt, ...);
```

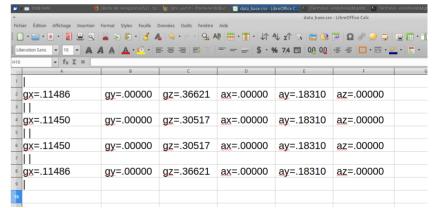
drv uart.h

### IHM



ІНМ.ру

data base.csv



data\_base.csv

```
#initialisation du port série

def serial_init():
    ser = serial.Serial(
    port='/dev/ttyUSB0',\
    baudrate=9600,\
    parity=serial.PARITY_NONE,\
    stopbits=serial.STOPBITS_ONE,\
    bytesize=serial.EIGHTBITS,\
        timeout=0.05)
    print("port initialisé")
    return ser
```

```
def clear database(self):
    self.database = [("")]*100
    with open('data base.csv', mode='w') as data base:
        data base writer = csv.writer(data base)
        data base writer.writerow("")
    self.ctn = 0
def read data(self):
       if (self.ser.in waiting > 0):
           string = self.ser.readline()
            self.data = (string.decode('Ascii'))
           print(self.data)
           text = f'data: {self.data}'
            self.label.setText(text)
           self.database[self ctnl = self data
            self.send c (parameter) self: Window
           self.ctn = self.ctn +1
```

data base writer = csv.writer(data base,delimiter=' ',quotechar='|', quoting=csv.QUOTE MINIMAL)

def send csv(self.data):

with open('data base.csv', mode='w') as data base:

data base writer.writerow(data)