

LABORATÓRIO DE ORGANIZAÇÃO E ARQUITETURA DE COMPUTADORES

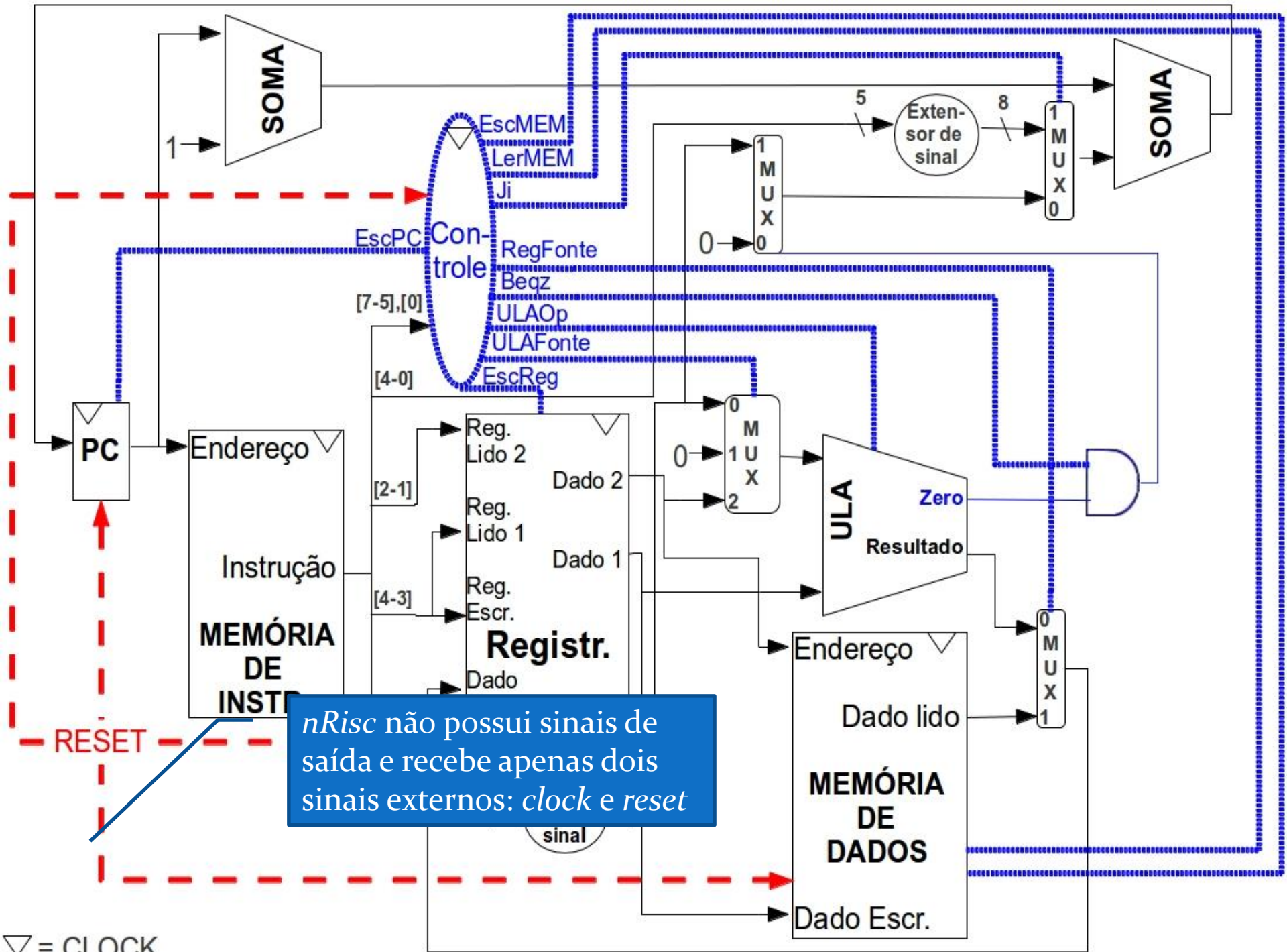
Datapath: Exemplo

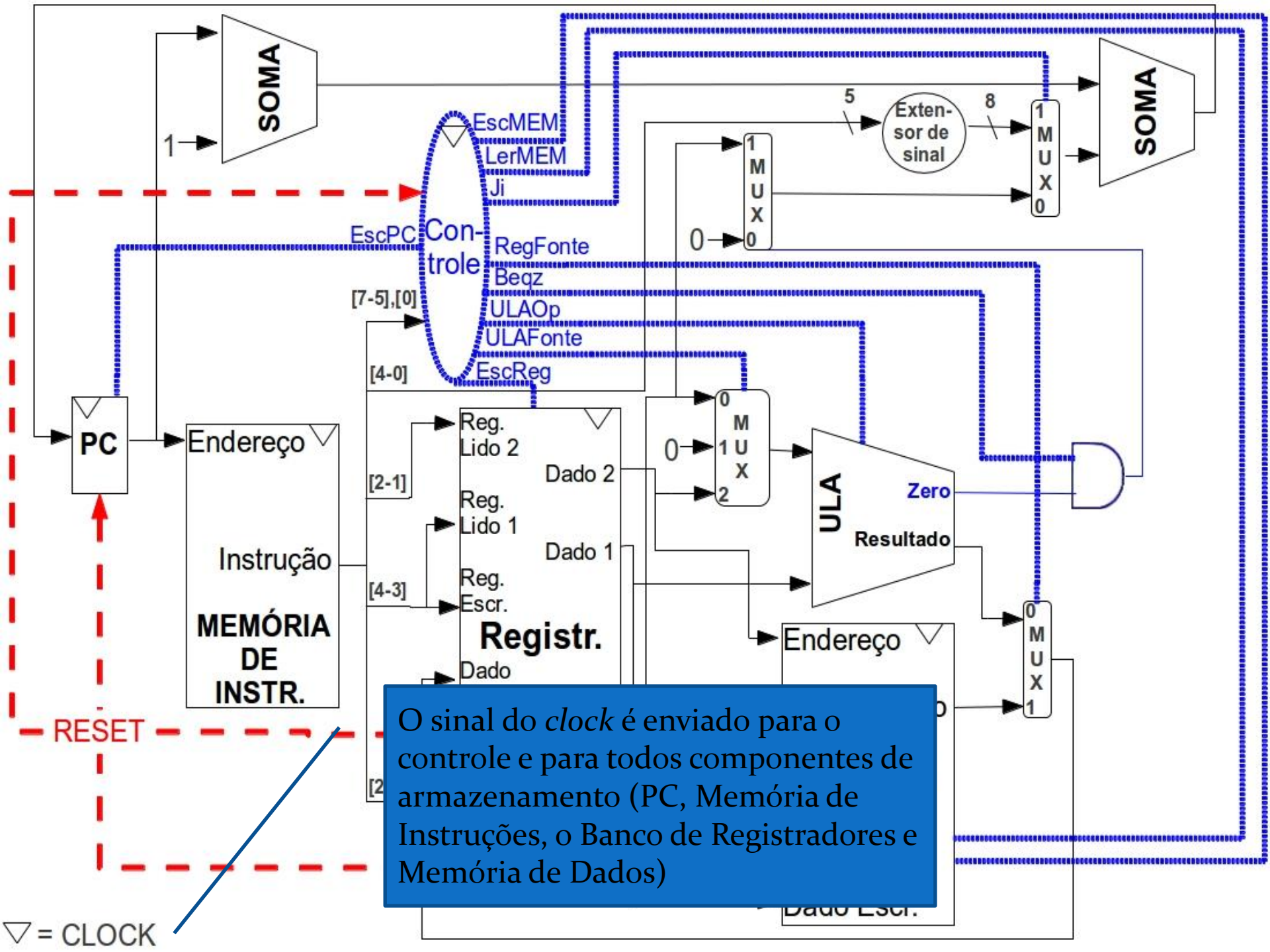
PROFA. JULIANA SANTIAGO TEIXEIRA

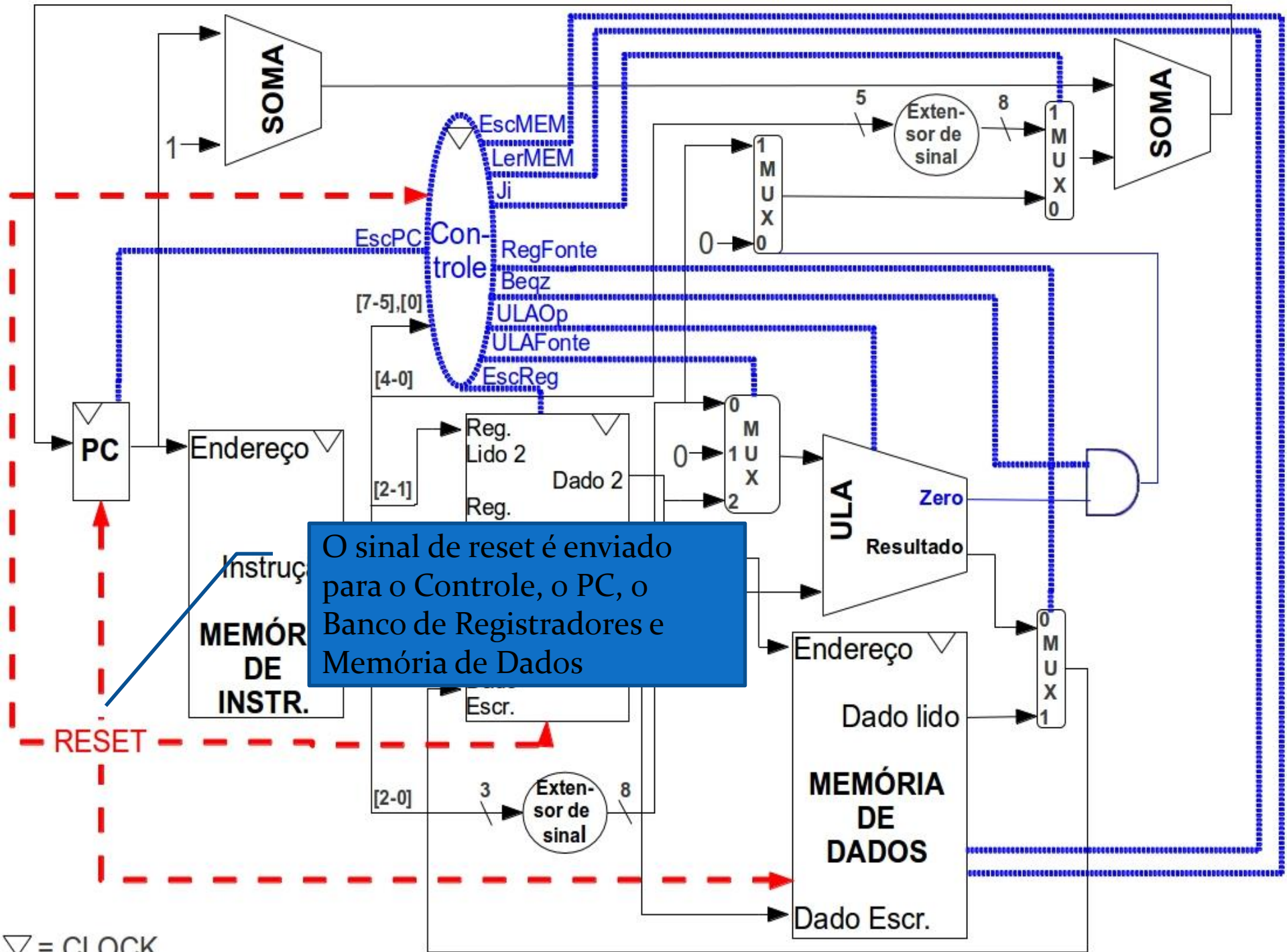
julianasteixeira@hotmail.com

Conjunto de Instruções

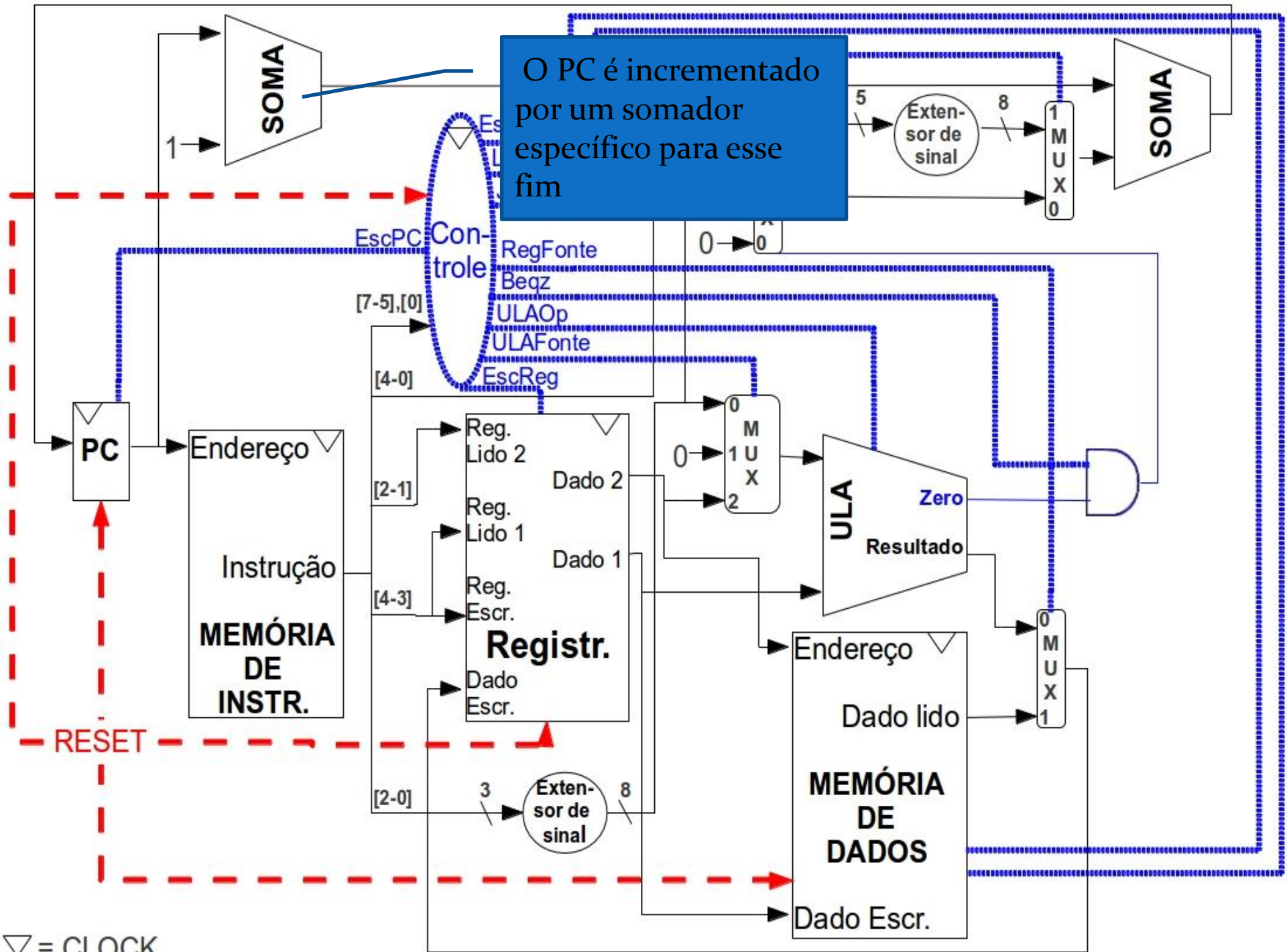
Instrução	Opcode (3b)	Operandos (4b)		Distinção (1b)
lw	000	reg. dest (2b)	reg. end (2b)	inútil
sw	001	reg. orig. (2b)	reg. end (2b)	0
halt	001	inútil		1
add	010	reg. 1/dest. (2b)	reg. 2 (2b)	0
not	010	reg. dest. (2b)	reg. 2 (2b)	1
sub	011	reg. 1/dest. (2b)	reg. 2 (2b)	0
slt	011	reg. 1/dest. (2b)	reg. 2 (2b)	1
and	100	reg. 1/dest. (2b)	reg. 2 (2b)	0
or	100	reg. 1/dest. (2b)	reg. 2 (2b)	1
addi	101	reg. 1/dest. (2b)	imediato (3b)	
beqz	110	reg. orig (2b)	imediato (3b)	
ji	111	imediato (5b)		

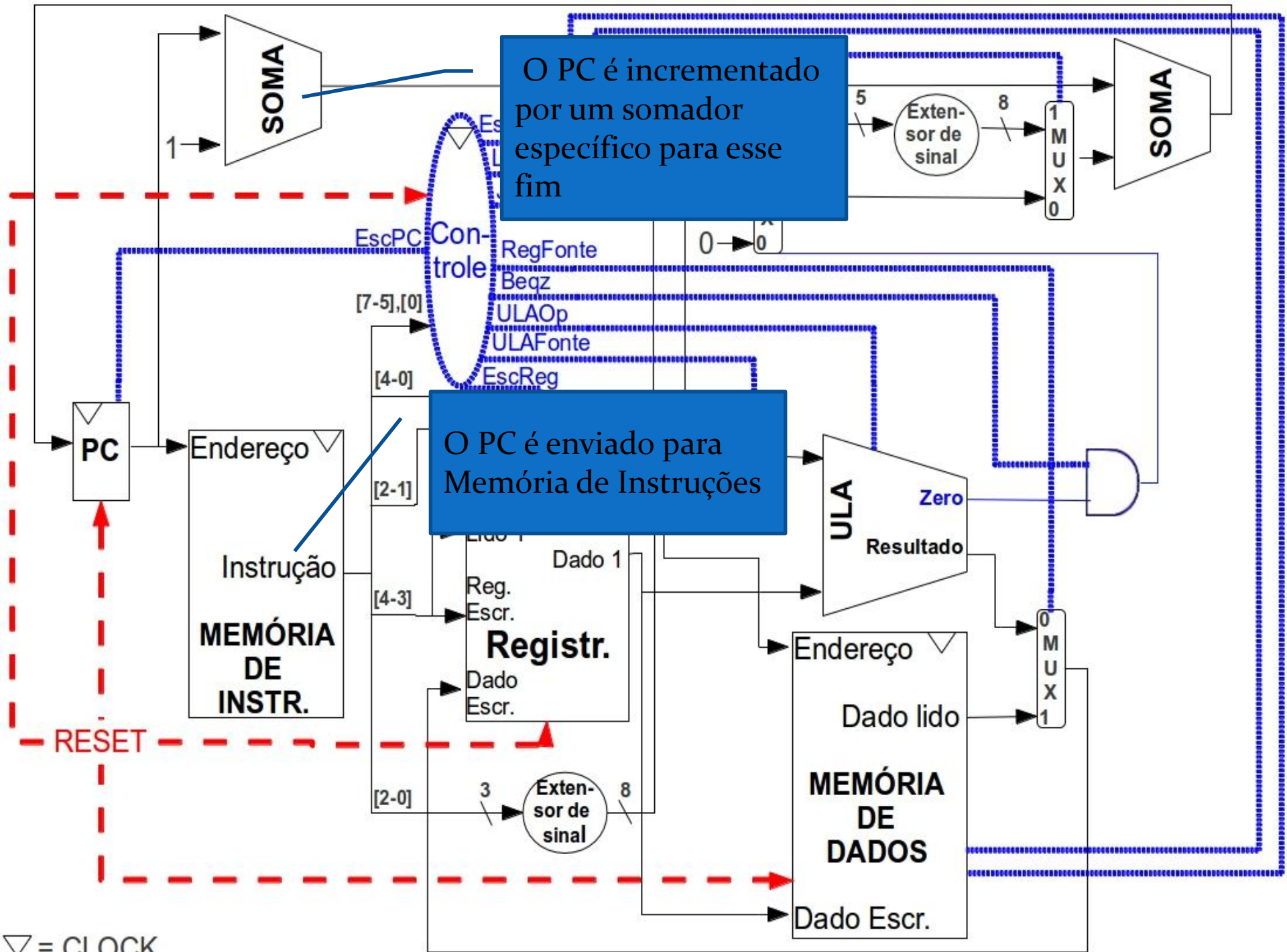


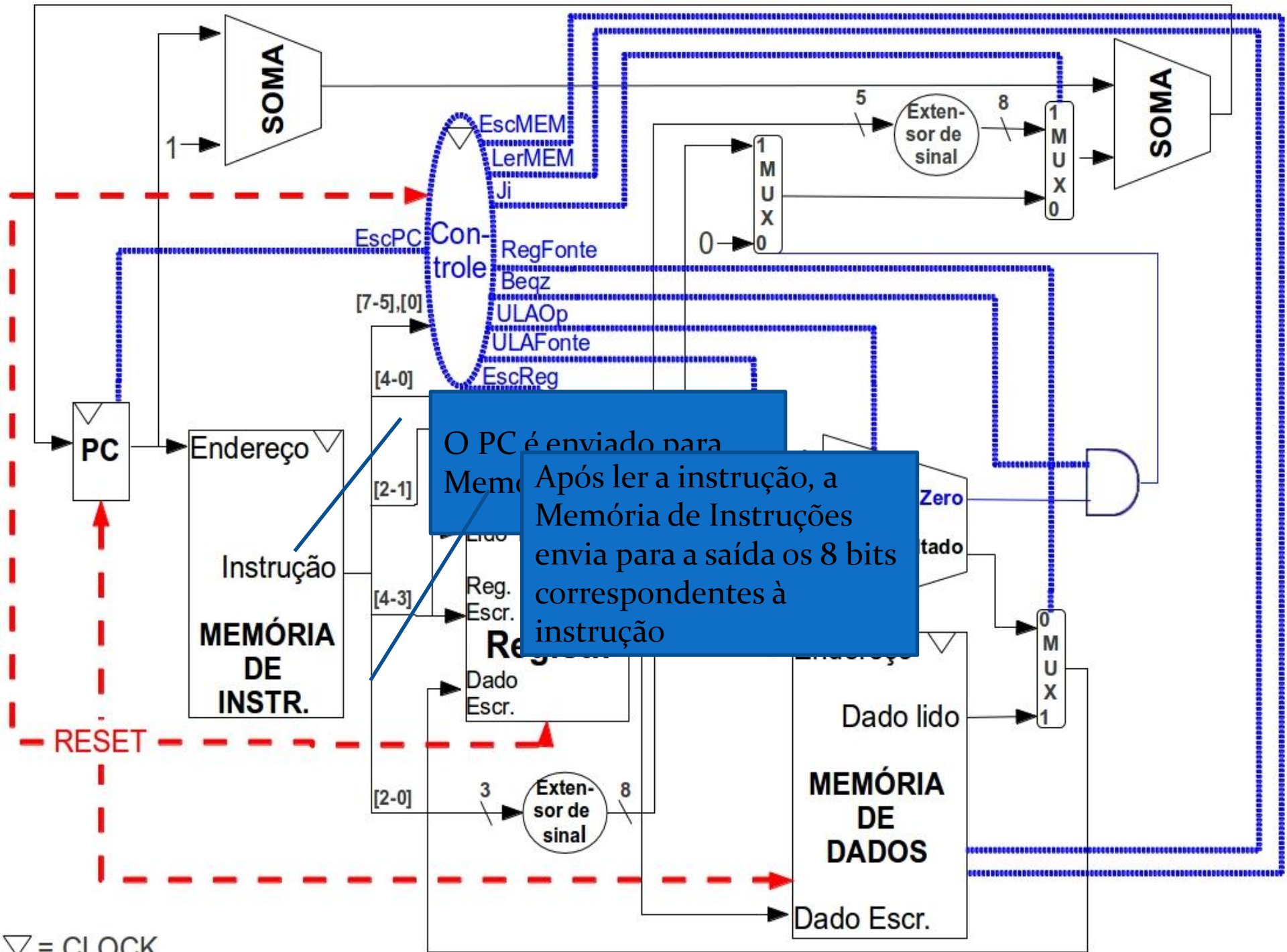




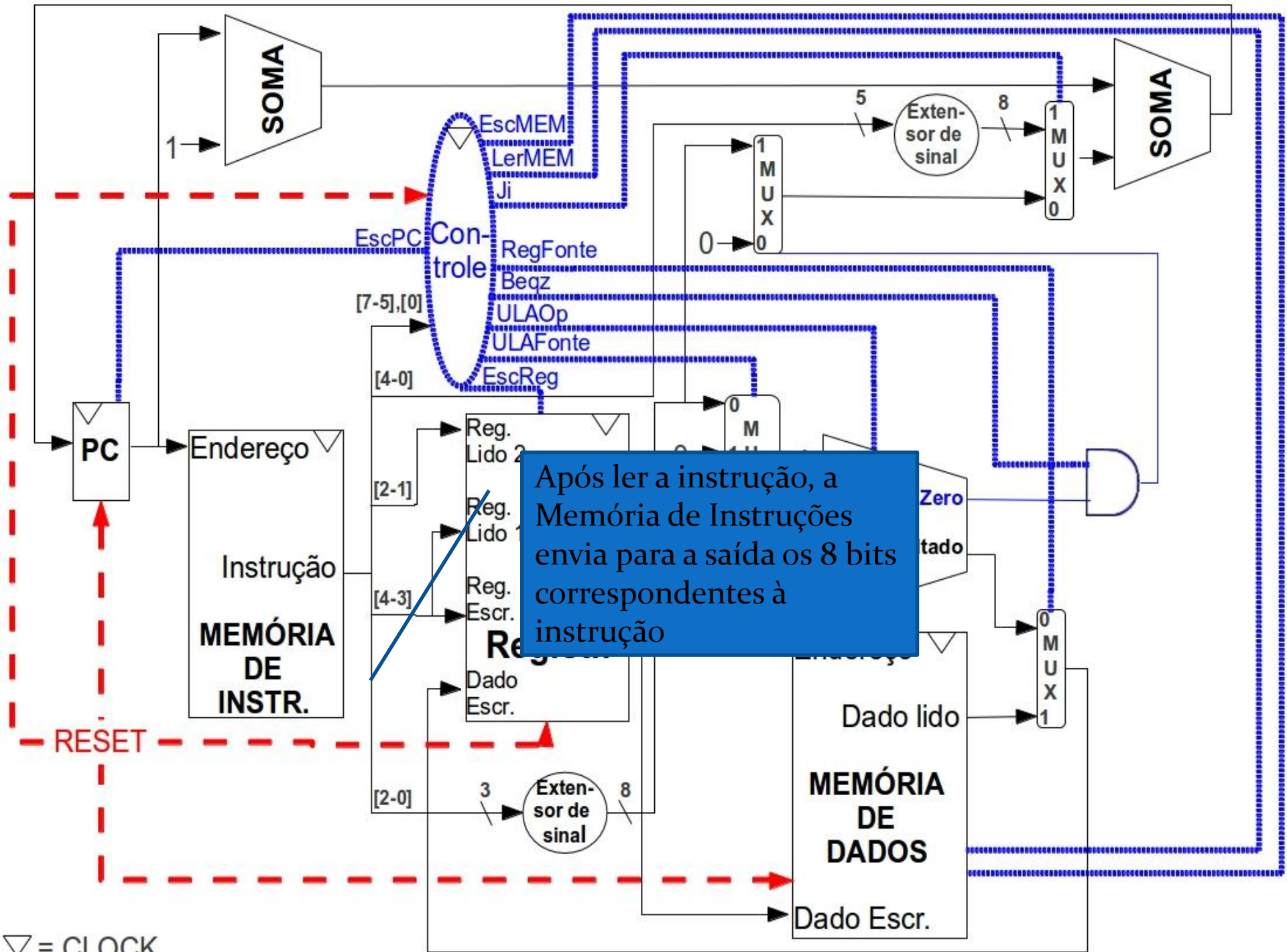
▽ = CLOCK







▽ = CLOCK



Após ler a instrução, a Memória de Instruções envia para a saída os 8 bits correspondentes à instrução