## **Algorithm 1** Single-Ended Messung

- 1:  $DDRF \leftarrow DDRF$  AND NOT (1 shifted left by DDF0)
- 2:  $ADMUX \leftarrow ADMUX$  OR (1 shifted left by ADLAR) OR (1 shifted left by REFS0);
- 3: // Uncomment for auto trigger mode:
- 4: //ADCSRA = ADCSRA OR (1 shifted left by ADATE)
- 5: // ADCSRB = ADCSRB OR (1 shifted left by ADTS1) OR (1 shifted left by ADTS0)
- 6: // Enable Interrupt
- 7: // Enable ADC
- 8: // Prescaler  $\leftarrow 64 : ADC_f = 8M/64$
- 9: ADCSRA = ADCSRA OR (1 shifted left by ADEN) OR (1 shifted left by ADSC) OR (1 shifted left by ADPS2) OR (1 shifted left by ADPS1)
- 10: DIDR0 = DIDR0 OR (1 shifted left by ADC0D)
- 11: function ADCREAD
- 12:  $adc\_value \leftarrow 0 // uint16 _t$
- 13: end function

## **Letzte Hüfe**

xB Fucking HELS

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## **Contents**

## **Todo list**