



# Software Safety Requirements and Architecture Lane Assistance

Document Version: 1.0.0 Released on 2017-12-19



## Document history

Date	Version	Editor	Description
19.12.2017	1.0.0	Bernhard Rode	Initial version of the safety concept

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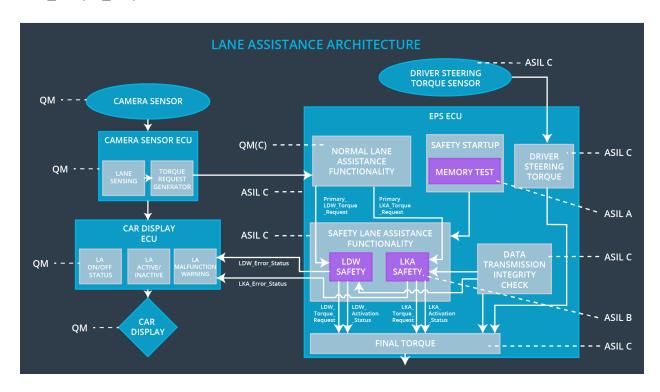
#### Purpose

The technical safety concept involves:

- Turning functional safety requirements into technical safety requirements
- Allocating technical safety requirements to the system architecture

# Inputs to the Software Requirements and Architecture Document

The lane keeping item shall ensure that the lane departure oscillating torque amplitude is below Max\_Torque\_Amplitude.

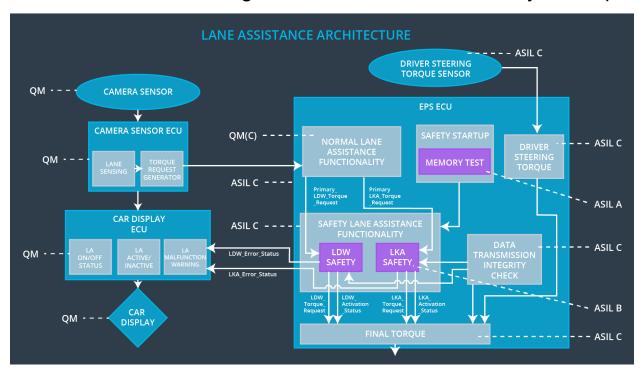


### Technical safety requirements

Technical Safety Requirements related to Functional Safety Requirement 01-01 are:

ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Architecture Allocation	Safe State
Technical Safety Requirement 01	The LDW safety component shall ensure that the amplitude of the 'LDW_Torque_Request' sent to the 'Final electronic power steering Torque' component is below 'Max_Torque_amplitude'	С	50 ms	LDW Safety	LDW torque output is set to zero
Technical Safety Requirement 02	The validity and integrity of the data transmission for 'LDW_Torque_Request' signal shall be ensured	С	50 ms	Data Transmission Integrity Check	LDW torque output is set to zero
Technical Safety Requirement 03	As soon as a failure is detected by the LDW function, it shall deactivate the LDW feature and the 'LDW_Torque_Request' shall be set to zero	С	50 ms	LDW Safety	LDW torque output is set to zero
Technical Safety Requirement 04	As soon as the LDW function deactivates the LDW feature, the 'LDW Safety' software block shall send a signal to the car display ECU to turn on a warning light	С	50 ms	LDW Safety	LDW torque output is set to zero
Technical Safety Requirement 05	Memory test shall be conducted at start up of the EPS ECU to check for any faults in memory	Α	Length of vehicle ignition cycle	Safety startup - Memory test	LDW torque output is set to zero

#### Refined Architecture Diagram from the Technical Safety Concept



## Software Requirements

#### Lane Departure Warning (LDW) Amplitude Malfunction Software Requirements:

ID	Technical Safety Requirement	ASIL	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 01	The LDW safety component shall ensure that the amplitude of the LDW_Torque_Request sent to the Final Electronic Power Steering Torque component is below Max_Torque_Amplitude	С	50 ms	LDW Safety	LDW torque output is set to zero

ID	Software Safety	ASI	Allocation Software Elements	Safe State
	Requirement	L		
Software Safety Requirem ent 01-01	The input signal "Primary_LDW_Torq_Req " shall be read and pre- processed to determine the torque request coming from the "Basic/Main LAFunctionality" SW Component. Signal" processed_LDW_Torq_Re q" shall be generated at the end of the processing.	С	LDW_SAFETY_INPUT_PROCES SING	N/A
Software Safety Requirem ent 01-02	In case the "processed_LDW_Torq_R eq" signal has a value greater than "Max_Torque_Ampltide_ LDW" (maximum allowed safe torque), the torque signal "limited_LDW_Torq_Req " shall be set to 0, else "limited_LDW_Torq_Req " shall take the value of "processed_LDW_Torq_R eq".	С	TORQUE_LIMITER	"limited_LDW_Torq_ Req" = 0(Nm=Newton- meter)
Software Safety	The "limited_LDW_Torq_Req	С	LDW_SAFETY_OUTPUT_GENE RATOR	LDW_Torq_Req= 0 (Nm)

Requirem	"shall be transformed	
ent 01-03	into a signal	
	"LDW_Torq_Req" which	
	is suitable to be	
	transmitted outside of	
	the LDW	
	Safetycomponent ("LDW	
	Safety") to the "Final EPS	
	Torque"component	

ID	Technical Safety Requirement	ASIL	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 02	The validity and integrity of the data transmission for 'LDW_Torque_Request' signal shall be ensured	С	50 ms	Data Transmission Integrity Check	LDW torque output is set to zero

ID	Software Safety Requirement	ASIL	Allocation Software Elements	Safe State
Software Safety Requirement 02-01	Any data to be transmitted outside of the LDW Safety component ("LDW Safety") including "LDW_Torque_Req" and "activation_status" (seeSofSafReq03-02) shall be protected by an End2End(E2E) protection mechanism	С	E2ECalc	LDW_Torq_Req= 0 (Nm)
Software Safety Requirement 02-02	The E2E protection protocol shall contain and attach the control data: alive counter (SQC) and CRC to the data to be transmitted.	С	E2ECalc	LDW_Torq_Req= 0 (Nm)

ID	Technical Safety Requirement	ASIL	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 03	As soon as a failure is detected by the LDW function, it shall deactivate the LDW feature and the LDW_Torque_Request shall be set to zero	С	50 ms	LDW Safety	torque output is set to zero

ID	Software Safety Requirement	ASI L	Allocation Software Elements	Safe State
Software Safety Requirement0 3-01	Each of the SW elements shall output a signal to indicate any error which is detected by the element. Error signal = error_status_input (LDW_SAFETY_INPUT_PROCESSIN G), error_status_torque_limiter (TORQUE_LIMITER), error_status_output_gen (LDW_SAFETY_OUTPUT_GENERA TOR)	С	All	N/A
Software Safety Requirement0 3-02	A software element shall evaluate the error status of all the other software elements and in case any 1 of them indicates an error, it shall deactivate the LDW feature ("activation_status" = 0)	С	LDW_SAFETY_ACTIVATI ON	Activation_stat us = 0 (LDW function deactivated)
Software Safety Requirement0 3-03	In case of no errors from the software elements, the status of the LDW feature shall be set to activated ("activation_status" = 1)	С	LDW_SAFETY_ACTIVATI ON	N/A
Software Safety Requirement0 3-04	In case an error is detected by any of the software elements, it shall set the value of its corresponding torque to 0 so that "LDW_Torq_Req" is set to 0	С	All	LDW_Torq_Re q = 0
Software Safety Requirement0 3-05	Once the LDW functionality has been deactivated, it shall stay deactivated till the time the ignition is switched from off to on again.	С	LDW_SAFETY_ACTIVATI ON	Activation_stat us = 0 (LDW function deactivated)

ID	Technical Safety Requirement	ASIL	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 04	As soon as the LDW function deactivates the LDW feature, the LDW Safety software block shall send a signal to the car display ECU to turn on a warning light	С	50 ms	LDW Safety	torque output is set to zero

ID	Software Safety Requirement	A S I L	Allocation Software Elements	Safe State
Software Safety Requirement 04-01	When the LDW function is deactivated (activation_status set to 0), the activation_status shall be sent to the car display ECU.	С	LDW_SAFETY_A CTIVATION, Car Display ECU	N/A

ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 05	Memory test shall be conducted at start up of the EPS ECU to check for any faults in memory	Α	Length of vehicle ignition cycle	Safety startup - Memory test	LDW torque output is set to zero

ID	Software Safety Requirement	A S I L	Allocation Software Elements	Safe State
Software Safety Requirement 05-01	A CRC verification check over the software code in the Flash memory shall be done every time the ignition is switched from off to on to check for any corruption of content.	Α	MEMORYTEST	Activation_status = 0
Software Safety Requirement 05-02	Standard RAM tests to check the data bus, address bus and device integrity shall be done every time the ignition is switched from off to on (E.g. walking 1s test, RAM pattern test. Refer RAM and processor vendor recommendations)	Α	MEMORYTEST	Activation_status = 0
Software Safety Requirement 05-03	The test result of the RAM or Flash memory shall be indicated to the LDW_Safety component via the "test_status" signal	Α	MEMORYTEST	Activation_status = 0
Software Safety Requirement 05-04	In case any fault is indicated via the "test_status" signal the INPUT_LDW_PROCESSING shall set an error on error_status_input (=1) so that the LDW functionality is deactivated and the LDWTorque is set to 0	А	LDW_SAFETY_IN PUT_PROCESSIN G	Activation_status = 0

### Refined Architecture Diagram

