Entry: 001 **Date:** 1/27/2011 **Version:** 1.00 **Title:** Defining requirements

After the overwhelming success of the Class-D audio amplifier, it was decided to pursue an impoved, less experimental version. Some ideas for the revised circuit include using entirely surface mount parts (reduced size and potentially increased performance), a higher supply voltage (increased power output), and an output inductor with a lower series resistance.

Other ideas brought forward for consideration are the use of a different class, such as Class G, Class H, Class I, or Class T. These ideas were rejected due to potential copyright/patent issues and complexity.

Entry: 002 **Date:** 1/29/2011 **Version:** 1.00 **Title:** Initial BOM

This post is duplicated from the NBitWonder Forums.

After some research, I've arrived at a partial bill of materials.

- MOSFETs - DMN4009

- Gate driver - IRS2004

- Comparator - LM311

- Integrating error amp - TL082

The amplifier will, like the prototype, be a self-oscillating type. My hope is that with proper construction techniques, the operating frequency will be quite high, ideally upwards of 500kHz. To that end, the output filter has been designed as a second-order Butterworth filter with a cutoff of 50kHz when operated with an 8-ohm speaker, a common value for home use. An inductor has been identified with a very low series resistance, which should help efficiency improve over the prototype.

The MOSFETs selected for use this time are cheaper than the IRL520Ns used last time and have a much lower on-state resistance, which should lead to increased efficiency. The MOSFET gate driver is cheaper than the one used for the experiment, but supports a lower drive current and a lower maximum voltage – but at the voltages slated for use in this project, this becomes a non-issue.

The input stages (TL082 and LM311) are slated to be unchanged from the previous iteration, since the parts are already low-cost and well-suited for the task at hand.

Expected power output, based on a +/-18V supply and an 8-ohm speaker, is $\frac{18^2}{8 \times 2} = 20W$ RMS under ideal conditions. Peak power (ie, turning on one MOSFET or the other and allowing the output filter to reach steady state) is arrived at with $\frac{V^2}{R}$; the extra factor of 2 converts peak power to RMS. To support this power output, the power supply should be capable of delivering $\sqrt{\frac{20W}{8\Omega}} = 1.58$ amps per channel.

Entry: 003 **Date:** 1/30/2011 **Version:** 1.00 **Title:** PCB Layout

Today saw significant work on the PCB layout.

Entry: 003 **Date:** 1/31/2011 **Version:** 1.00 **Title:** More PCB Layout

The PCB layout was finished today. A secondary version was created that features RCA input jacks instead of the more compact and lower cost 3.5mm stereo minijack.