

1. Description

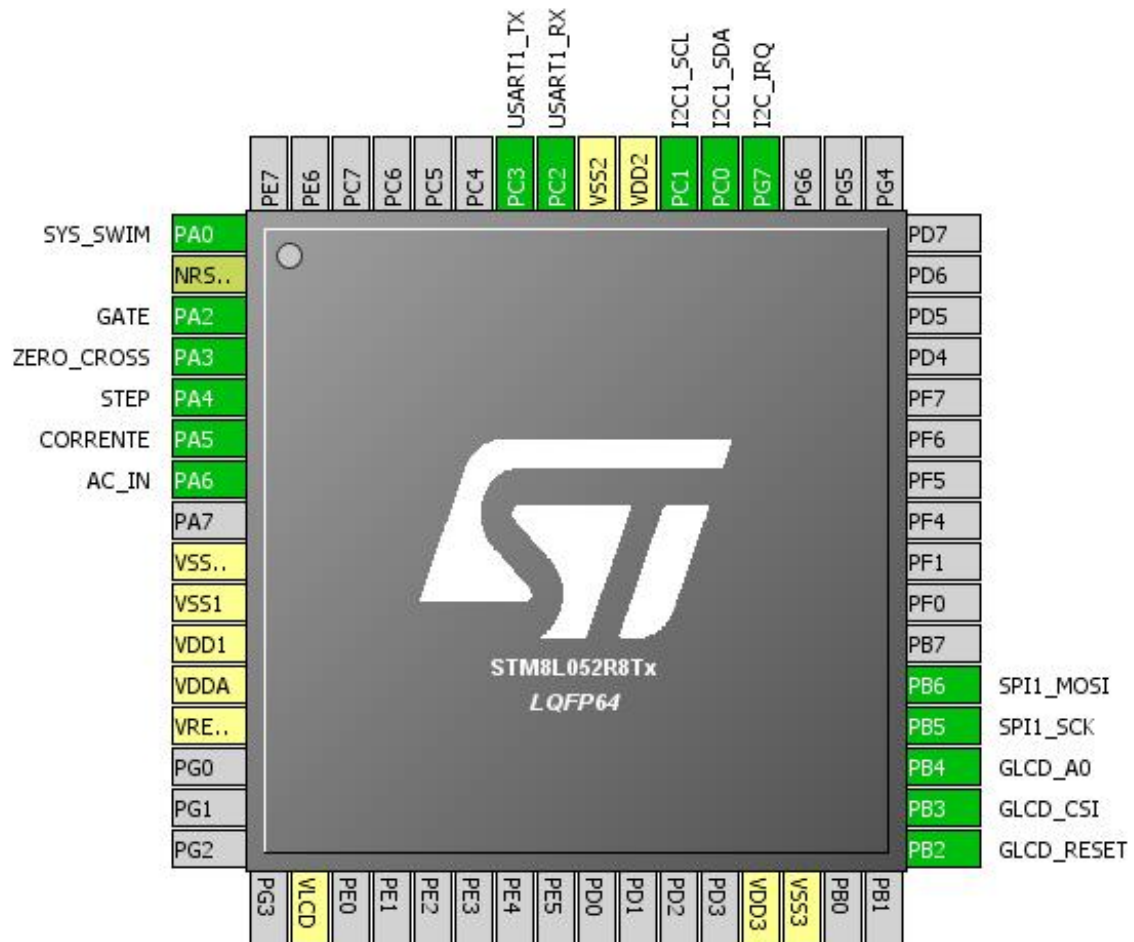
1.1. Project

| | |
|-----------------|------------------|
| Project Name | Teste |
| Board Name | No information |
| Generated with: | STM8CubeMX 1.2.0 |
| Date | 12/20/2017 |

1.2. MCU

| | |
|----------------|-------------------------|
| MCU Series | STM8L |
| MCU Line | STM8L051/052 Value Line |
| MCU name | STM8L052R8Tx |
| MCU Package | LQFP64 |
| MCU Pin number | 64 |

2. Pinout Configuration

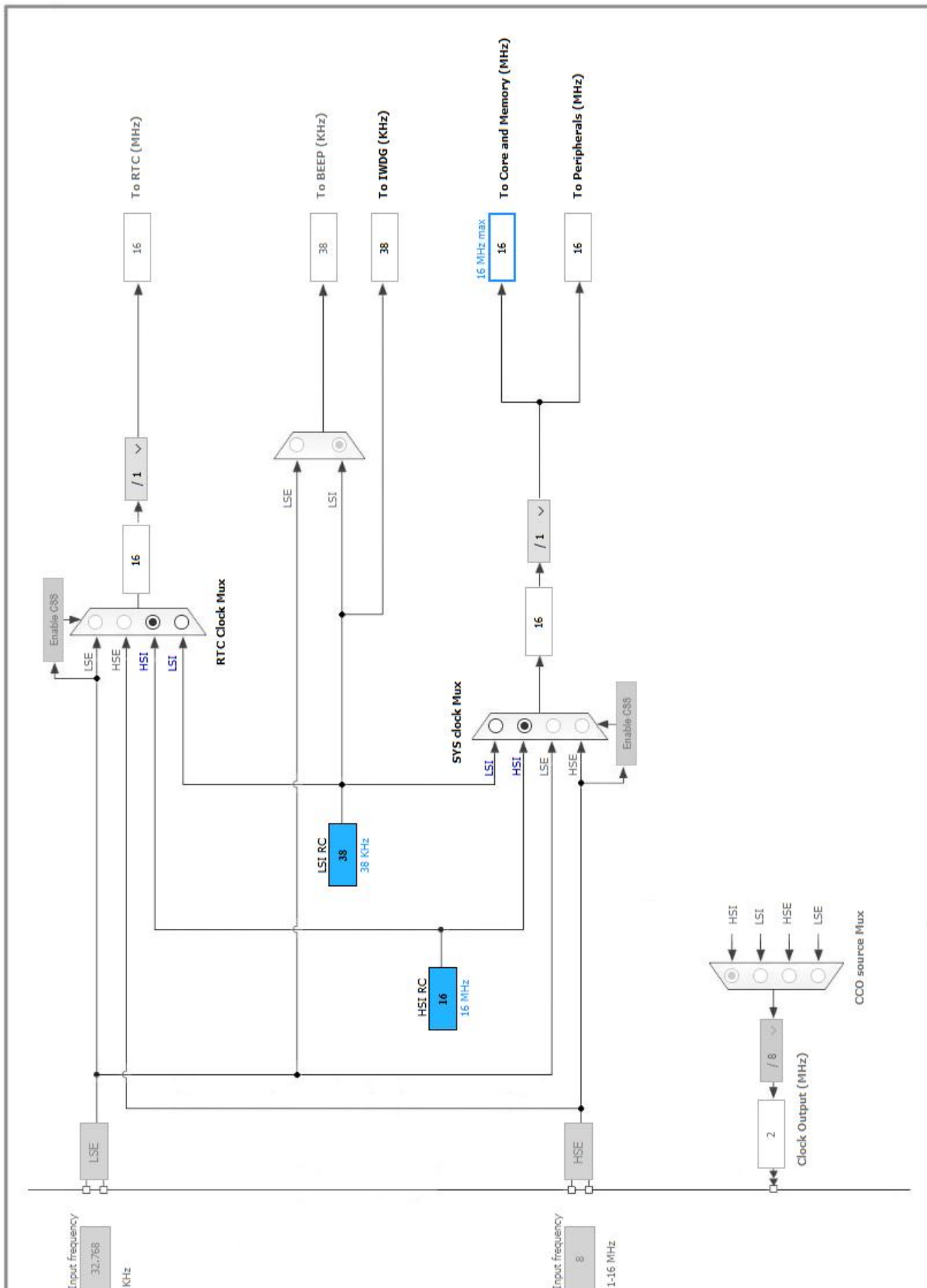


3. Pins Configuration

| Pin Number LQFP64 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|----------------------|---------------------------------------|----------|--------------------------|------------|
| 1 | PA0 | I/O | SYS_SWIM | |
| 2 | NRST/PA1 | Reset | | |
| 3 | PA2 * | I/O | GPIO_Input | GATE |
| 4 | PA3 * | I/O | GPIO_Input | ZERO_CROSS |
| 5 | PA4 | I/O | ADC1_IN2 | STEP |
| 6 | PA5 | I/O | ADC1_IN1 | CORRENTE |
| 7 | PA6 | I/O | ADC1_IN0 | AC_IN |
| 9 | VSSA/VREF- | Power | | |
| 10 | VSS1 | Power | | |
| 11 | VDD1 | Power | | |
| 12 | VDDA | Power | | |
| 13 | VREF+ | Power | | |
| 18 | VLCD | Power | | |
| 29 | VDD3 | Power | | |
| 30 | VSS3 | Power | | |
| 33 | PB2 * | I/O | GPIO_Output | GLCD_RESET |
| 34 | PB3 * | I/O | GPIO_Output | GLCD_CSI |
| 35 | PB4 | I/O | SPI1_NSS | GLCD_A0 |
| 36 | PB5 | I/O | SPI1_SCK | |
| 37 | PB6 | I/O | SPI1_MOSI | |
| 52 | PG7 * | I/O | GPIO_Output | I2C_IRQ |
| 53 | PC0 | I/O | I2C1_SDA | |
| 54 | PC1 | I/O | I2C1_SCL | |
| 55 | VDD2 | Power | | |
| 56 | VSS2 | Power | | |
| 57 | PC2 | I/O | USART1_RX | |
| 58 | PC3 | I/O | USART1_TX | |

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Power Consumption Calculator report

5.1. Microcontroller Selection

| | |
|-----------|-------------------------|
| Series | STM8L |
| Line | STM8L051/052 Value Line |
| MCU | STM8L052R8Tx |
| Datasheet | 23337_Rev2 |

5.2. Parameter Selection

| | |
|-------------|-----|
| Temperature | 25 |
| Vdd | 3.0 |