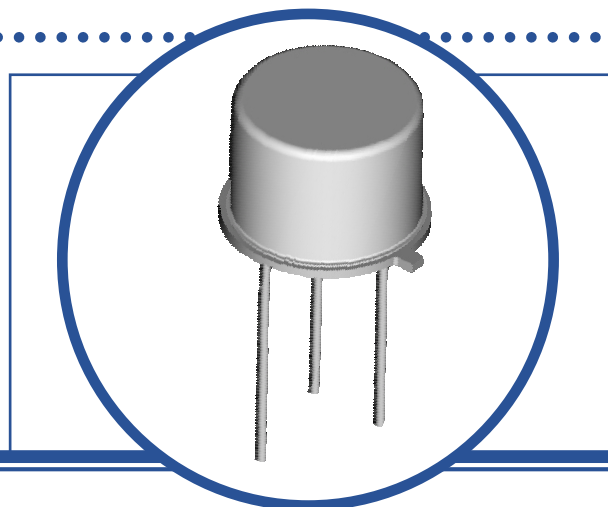


SILICON NPN TRANSISTOR

BFY51

- $V_{(BR)CEO} = 30V$ (Min).
- Hermetic TO-39 Metal Package.
- Ideally Suited General Purpose Amplifier Applications
- Screening Options Available



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ unless otherwise stated)

V_{CBO}	Collector – Base Voltage	60V
V_{CEO}	Collector – Emitter Voltage	30V
V_{EBO}	Emitter – Base Voltage	6V
I_C	Continuous Collector Current	1.0A
P_D	Total Power Dissipation at $T_A = 25^\circ C$	0.8W
	Derate Above $25^\circ C$	4.57mW/ $^\circ C$
P_D	Total Power Dissipation at $T_C = 25^\circ C$	5W
	Derate Above $25^\circ C$	28.6mW/ $^\circ C$
T_J	Junction Temperature Range	-65 to +200 $^\circ C$
T_{stg}	Storage Temperature Range	-65 to +200 $^\circ C$

THERMAL PROPERTIES

Symbols	Parameters	Max.	Units
$R_{\theta JA}$	Thermal Resistance, Junction To Ambient	218.75	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance, Junction To Case	35	$^\circ C/W$

Semelab Limited reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

Semelab Limited

Coventry Road, Lutterworth, Leicestershire, LE17 4JB
Telephone +44 (0) 1455 556565 Fax +44 (0) 1455 552612 Email: sales@semelab-tt.com

Website: <http://www.semelab-tt.com>



A subsidiary of
TT electronics plc.

Document Number 9728

Issue 1

Page 1 of 3

SILICON NPN TRANSISTOR BFY51

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise stated)

Symbols	Parameters	Test Conditions	Min.	Typ	Max.	Units
$V_{(BR)CEO}^{(1)}$	Collector-Emitter Breakdown Voltage	$I_C = 10\text{mA}$ $I_B = 0$	30			V
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage	$I_C = 10\mu\text{A}$ $I_E = 0$	60			
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E = 10\mu\text{A}$ $I_C = 0$	6			
I_{CBO}	Collector Cut-Off Current	$V_{CB} = 40\text{V}$ $I_E = 0$			50	nA
		$T_A = 100^\circ\text{C}$			2.5	μA
I_{EBO}	Emitter Cut-Off Current	$V_{EB} = 5\text{V}$ $I_C = 0$			50	nA
		$T_A = 100^\circ\text{C}$			2.8	μA
$h_{FE}^{(1)}$	Forward-current transfer ratio	$I_C = 10\text{mA}$ $V_{CE} = 6\text{V}$	30			
		$I_C = 150\text{mA}$ $V_{CE} = 6\text{V}$	40			
		$I_C = 1.0\text{A}$ $V_{CE} = 6\text{V}$	15			
$V_{CE(sat)}^{(1)}$	Collector-Emitter Saturation Voltage	$I_C = 150\text{mA}$ $I_B = 15\text{mA}$			0.35	V
		$I_C = 1.0\text{A}$ $I_B = 100\text{mA}$			1.6	
$V_{BE(sat)}^{(1)}$	Base-Emitter Saturation Voltage	$I_C = 1.0\text{A}$ $I_B = 100\text{mA}$			2	

DYNAMIC CHARACTERISTICS

h_{fe}	Small-Signal Current Gain	$I_C = 1.0\text{mA}$ $V_{CE} = 6\text{V}$ $f = 1.0\text{KHz}$	30			
f_T	Transition Frequency	$I_C = 50\text{mA}$ $V_{CE} = 6\text{V}$ $f = 20\text{MHz}$	50			MHz
C_{obo}	Output Capacitance	$V_{CB} = 12\text{V}$ $I_E = 0$ $f = 1.0\text{MHz}$			12	pF

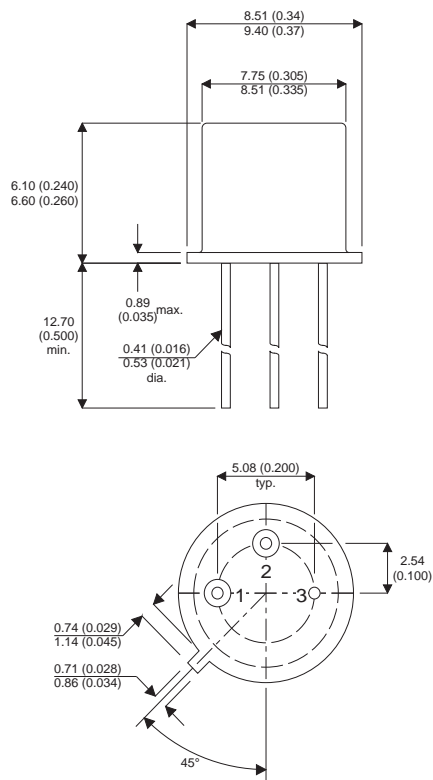
Notes

(1) Pulse Width $\leq 380\mu\text{s}$, $\delta \leq 2\%$

SILICON NPN TRANSISTOR BFY51

MECHANICAL DATA

Dimensions in mm (inches)



TO-39 (TO-205AD) METAL PACKAGE Underside View

Pin 1 - Emitter

Pin 2 - Base

Pin 3 - Collector