

CEM 3350

Dual Voltage Controlled State Variable Filter

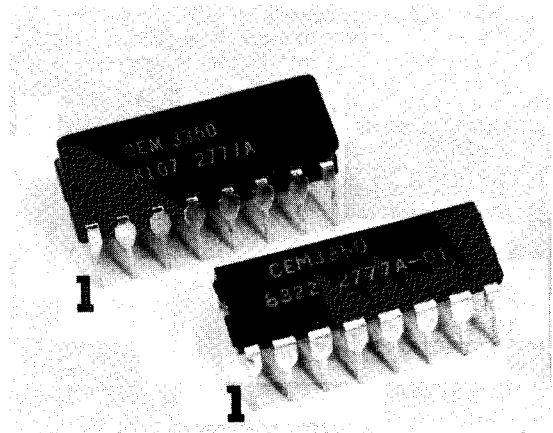
The CEM 3350 is a dual voltage controlled state-variable filter intended for electronic musical instruments and other signal processing applications. Each filter provides both voltage control of center/cut-off frequency over more than 12 octaves and voltage control of Q from 1/2 to greater than 40. All control scales are exponential, allowing for easier control of the parameters over their wide range. Although the two filters are completely independent, they may be easily interconnected to form a wide variety of filter responses.

Each filter also provides two signal inputs: For signals applied to the fixed gain input, the output will remain constant as the Q is

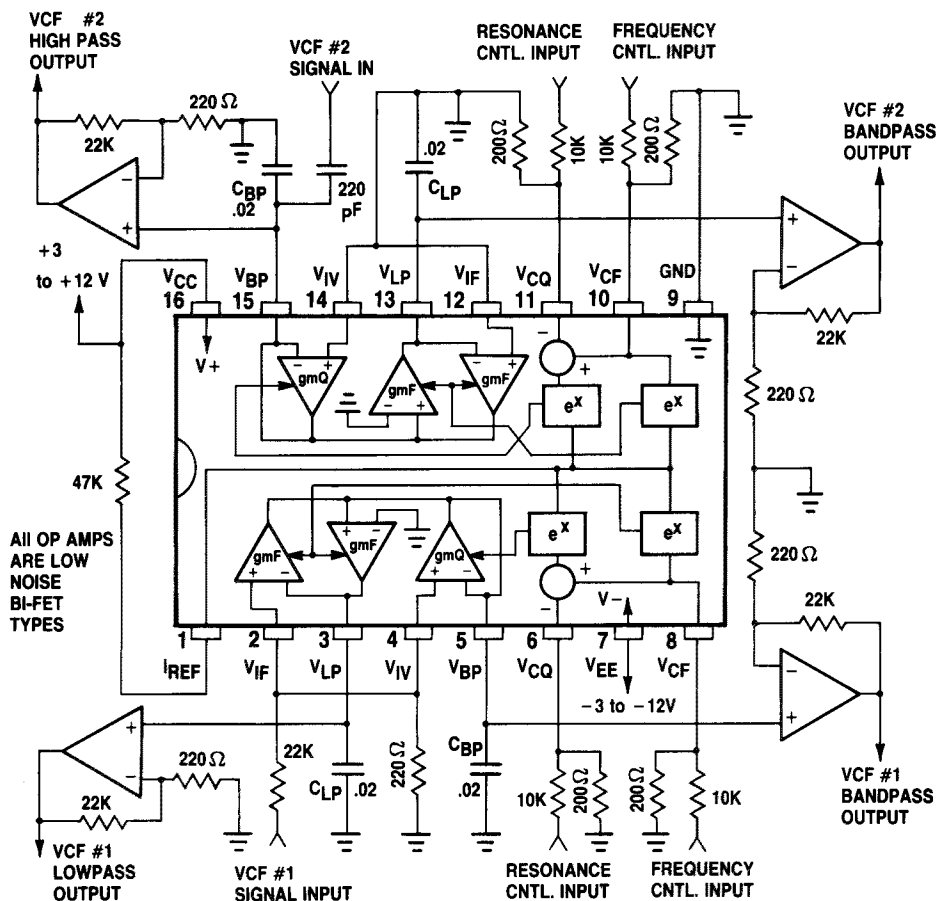
varied, while for signals applied to the variable gain input, the output decreases as Q is increased. The input signal may be proportioned between these two inputs to provide any desired characteristic.

Finally, each filter provides two simultaneous outputs, making directly available low-pass and band-pass, or band-pass and high-pass responses depending upon where the input signal is applied.

Able to operate over a wide supply range, the versatile CEM 3350 allows new and unique filter responses to be created with a high degree of voltage control over the defining parameters.



Block and Connections Diagram



Features:

- Low Cost
- Two Independent State Variable Filters in a Single 16 Pin DIP
- Separate Frequency and Q Control Inputs for Each
- Wide Frequency Sweep and Q Control Range
- Exponential Control Scales for Both Frequency and Q
- Two Simultaneous Outputs on Each: Low-Pass and Band-Pass or Band-Pass and High-Pass Possible
- Two Simultaneous Inputs for Each: Fixed Gain and Variable Gain
- Chip Configurable Into Many Unique V.C. Filters
- Wide Supply Range: $\pm 3V$ to $\pm 16V$

Notes

Note 1. Nominal for 1000:1 Sweep

Note 2. With Q Enhancement

Note 3. $+60\text{mV} < V_{CF} < +260\text{mV}$. Most of this error occurs at higher transconductance portion of scale.

Note 4. $+60\text{mV} < V_{CF} < +260\text{mV}$. As appears at V_{CP} or V_{BP} pin.

Note 5. $-150\text{mV} < V_{CQ} < +40\text{mV}$. As appears at V_{CP} or V_{BP} pin.

Note 6. V_{IF} or $V_{IV} = 40 \text{ mV.P.P.}$

Note 7. $V_{CF} = 0$. As appears at V_{CP} or V_{BP} pin. 20KHz bandwidth.

Note 8. Varies proportionally with transconductance.

Note 9. Varies inversely with transconductance.

Note 10. Total supply voltage across the chip should not exceed 26V.

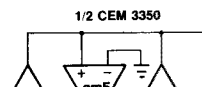


FIGURE 5: HI Q OVERLOAD LIMITER USING BANDPASS OUTPUT

Voltage at Pin 1 Referred to V_{EE}	1.2
Reference Current Range	10
Positive Supply Current, $V_{CF,Q} = 0$	—
Negative Supply Current	—
Supply Voltage Range ¹⁰	± 3



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