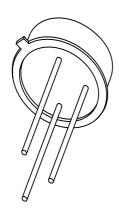
DISCRETE SEMICONDUCTORS

DATA SHEET



2N2906; 2N2906A PNP switching transistors

Product specification Supersedes data of September 1994 File under Discrete Semiconductors, SC04 1997 Jun 02





PNP switching transistors

2N2906; 2N2906A

FEATURES

- High current (max. 600 mA)
- Low voltage (max. 60 V).

APPLICATIONS

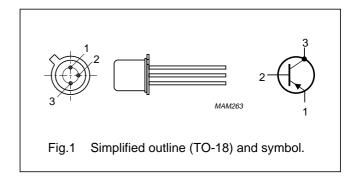
- High-speed switching
- Driver applications for industrial service.

DESCRIPTION

PNP switching transistor in a TO-18 metal package. NPN complements: 2N2222 and 2N2222A.

PINNING

PIN	DESCRIPTION		
1	emitter		
2	base		
3	collector, connected to case		



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-60	V
V _{CEO}	collector-emitter voltage	open base			
	2N2906		_	-40	V
	2N2906A		_	-60	V
I _C	collector current (DC)		_	-600	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	400	mW
h _{FE}	DC current gain	$I_C = -150 \text{ mA}; V_{CE} = -10 \text{ V}$	40	120	
f _T	transition frequency	$I_C = -50 \text{ mA}; V_{CE} = -20 \text{ V}; f = 100 \text{ MHz}$	200	_	MHz
t _{off}	turn-off time	$I_{Con} = -150 \text{ mA}$; $I_{Bon} = -15 \text{ mA}$; $I_{Boff} = 15 \text{ mA}$	_	300	ns

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-60	V
V _{CEO}	collector-emitter voltage	open base			
	2N2906		_	-40	V
	2N2906A		_	-60	V
V _{EBO}	emitter-base voltage	open collector	_	- 5	V
I _C	collector current (DC)		_	-600	mA
I _{CM}	peak collector current		_	-800	mA
I _{BM}	peak base current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	400	mW
		T _{case} ≤ 25 °C	_	1.2	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	200	°C
T _{amb}	operating ambient temperature		-65	+150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	in free air	438	K/W
R _{th j-c}	thermal resistance from junction to case		146	K/W

PNP switching transistors

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CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified

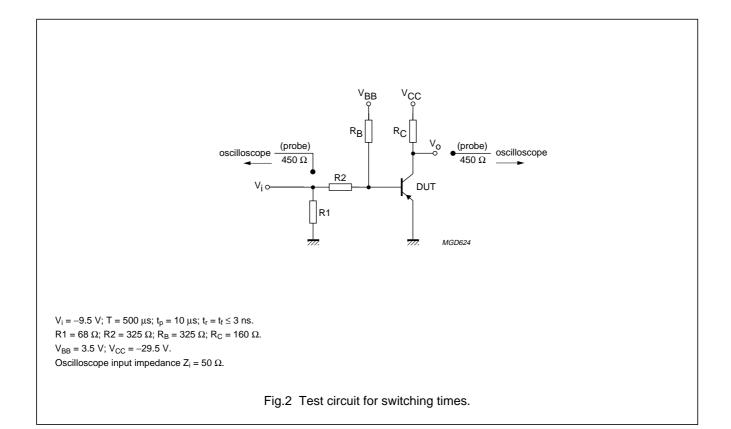
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current				
	2N2906	$I_E = 0; V_{CB} = -50 \text{ V}$	_	-20	nA
		I _E = 0; V _{CB} = -50 V; T _{amb} = 150 °C	_	-20	μΑ
I _{CBO}	collector cut-off current				
	2N2906A	$I_E = 0; V_{CB} = -50 \text{ V}$	_	-10	nA
		$I_E = 0$; $V_{CB} = -50 \text{ V}$; $T_{amb} = 150 \text{ °C}$	_	-10	μΑ
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = -5 \text{ V}$	_	-50	nA
h _{FE}	DC current gain	V _{CE} = -10 V			
	2N2906	$I_{\rm C} = -0.1 \text{mA}$	20	-	
		$I_C = -1 \text{ mA}$	25	-	
		$I_C = -10 \text{ mA}$	35	_	
		$I_C = -150 \text{ mA}$; note 1	40	120	
		$I_C = -500 \text{ mA}$; note 1	20		
h _{FE}	DC current gain	V _{CE} = -10 V		_	
	2N2906A	$I_{\rm C} = -0.1 \text{mA}$	40	-	
		$I_C = -1 \text{ mA}$	40	-	
		$I_C = -10 \text{ mA}$	40	_	
		$I_C = -150 \text{ mA}$; note 1	40	120	
		I _C = -500 mA; note 1	40	_	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -150 \text{ mA}$; $I_B = -15 \text{ mA}$; note 1		-400	mV
		$I_C = -500 \text{ mA}$; $I_B = -50 \text{ mA}$; note 1		-1.6	V
V _{BEsat}	base-emitter saturation voltage	$I_C = -150 \text{ mA}$; $I_B = -15 \text{ mA}$; note 1		-1.3	V
		$I_C = -500 \text{ mA}$; $I_B = -50 \text{ mA}$; note 1		-2.6	V
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = -10 \text{ V}$; $f = 1 \text{ MHz}$	_	8	pF
C _e	emitter capacitance	$I_C = i_c = 0$; $V_{EB} = -2$ V; $f = 1$ MHz	_	30	pF
f _T	transition frequency	$I_C = -50 \text{ mA}$; $V_{CE} = -20 \text{ V}$; $f = 100 \text{ MHz}$; note 1	200	_	MHz
Switching	times (between 10% and 90% leve	··ls); see Fig.2			
t _{on}	turn-on time	$I_{Con} = -150 \text{ mA}; I_{Bon} = -15 \text{ mA}; I_{Boff} = 15 \text{ mA}$	_	45	ns
t _d	delay time		_	15	ns
t _r	rise time		_	35	ns
t _{off}	turn-off time		_	300	ns
t _s	storage time		_	250	ns
t _f	fall time		_	50	ns

Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

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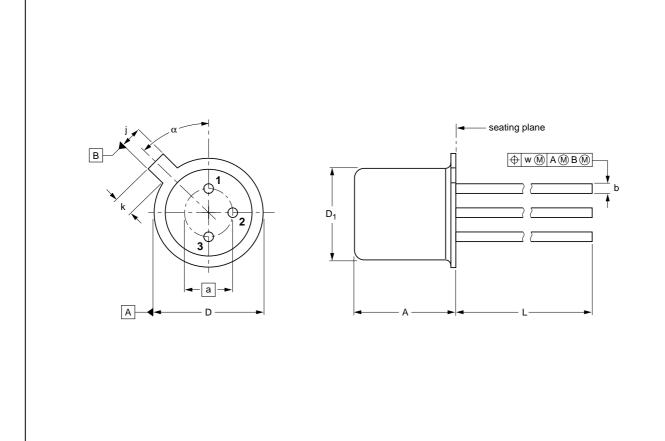
PNP switching transistors

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PACKAGE OUTLINE

Metal-can cylindrical single-ended package; 3 leads

SOT18/13



DIMENSIONS (millimetre dimensions are derived from the original inch dimensions)

UNIT	A	а	b	D	D ₁	j	k	L	w	α
mm	5.31 4.74	2.54	0.47 0.41	5.45 5.30	4.70 4.55	1.03 0.94	1.1 0.9	15.0 12.7	0.40	45°

OUTLINE		REFER	EUROPEAN ISSUE DATE			
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT18/13	B11/C7 type 3	TO-18				97-04-18

PNP switching transistors

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DEFINITIONS

Data sheet status			
Objective specification	This data sheet contains target or goal specifications for product development.		
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.		
Product specification	This data sheet contains final product specifications.		
Limiting values			
Limiting values given are in accordance with the Absolute Maximum Pating System (IEC 134). Stress above one or			

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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