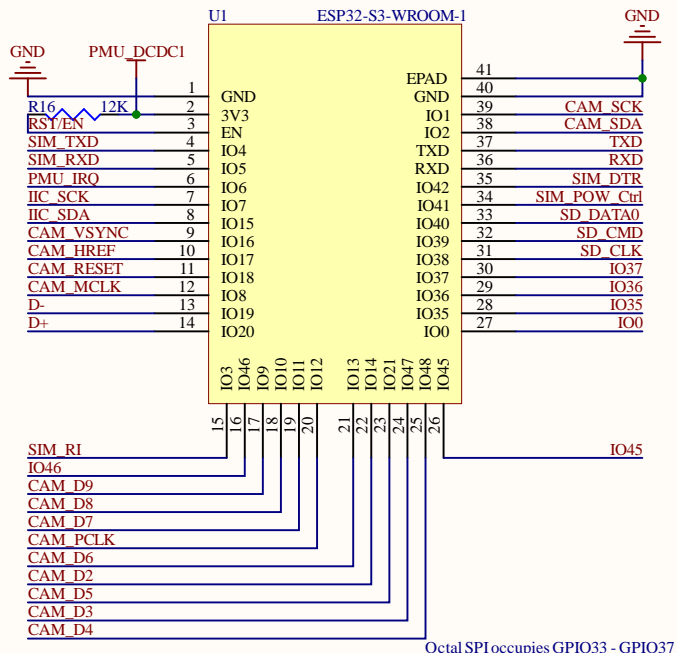
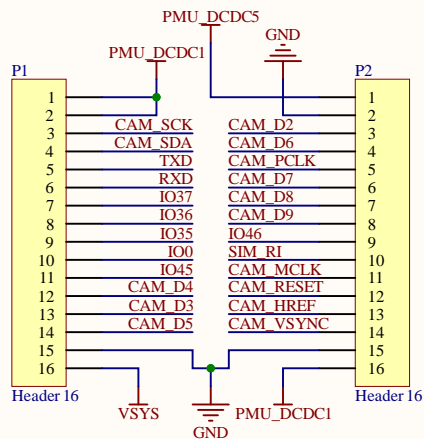
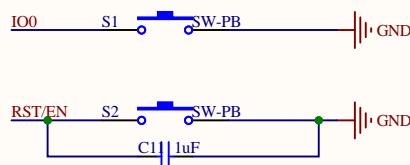
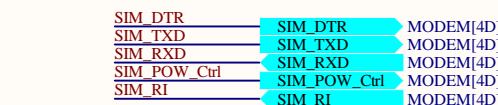
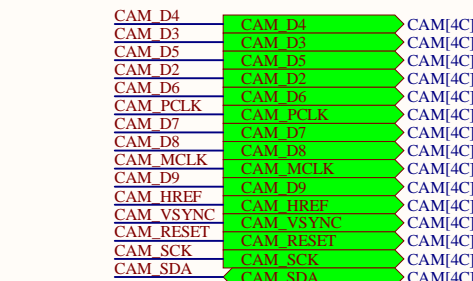
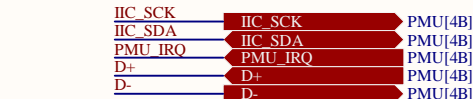
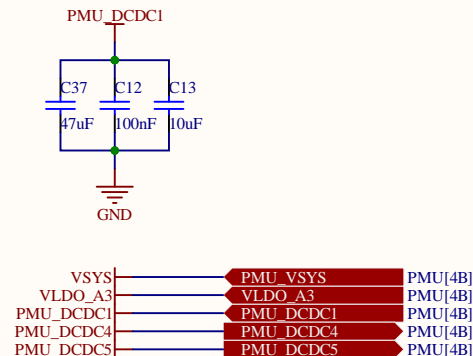
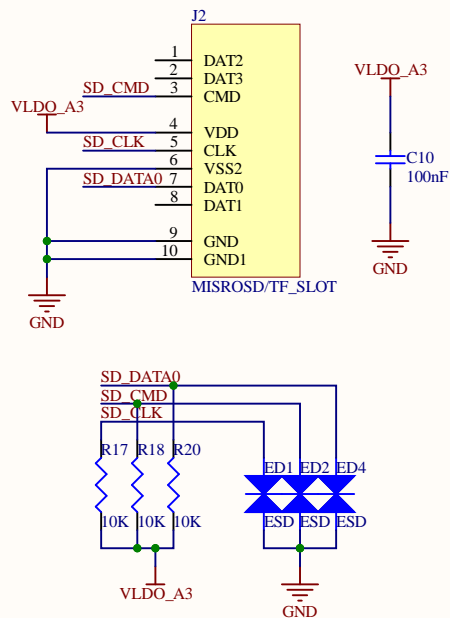


SOC

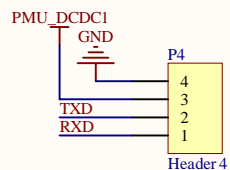


Octal SPI occupies GPIO33 - GPIO37

TF Card holder

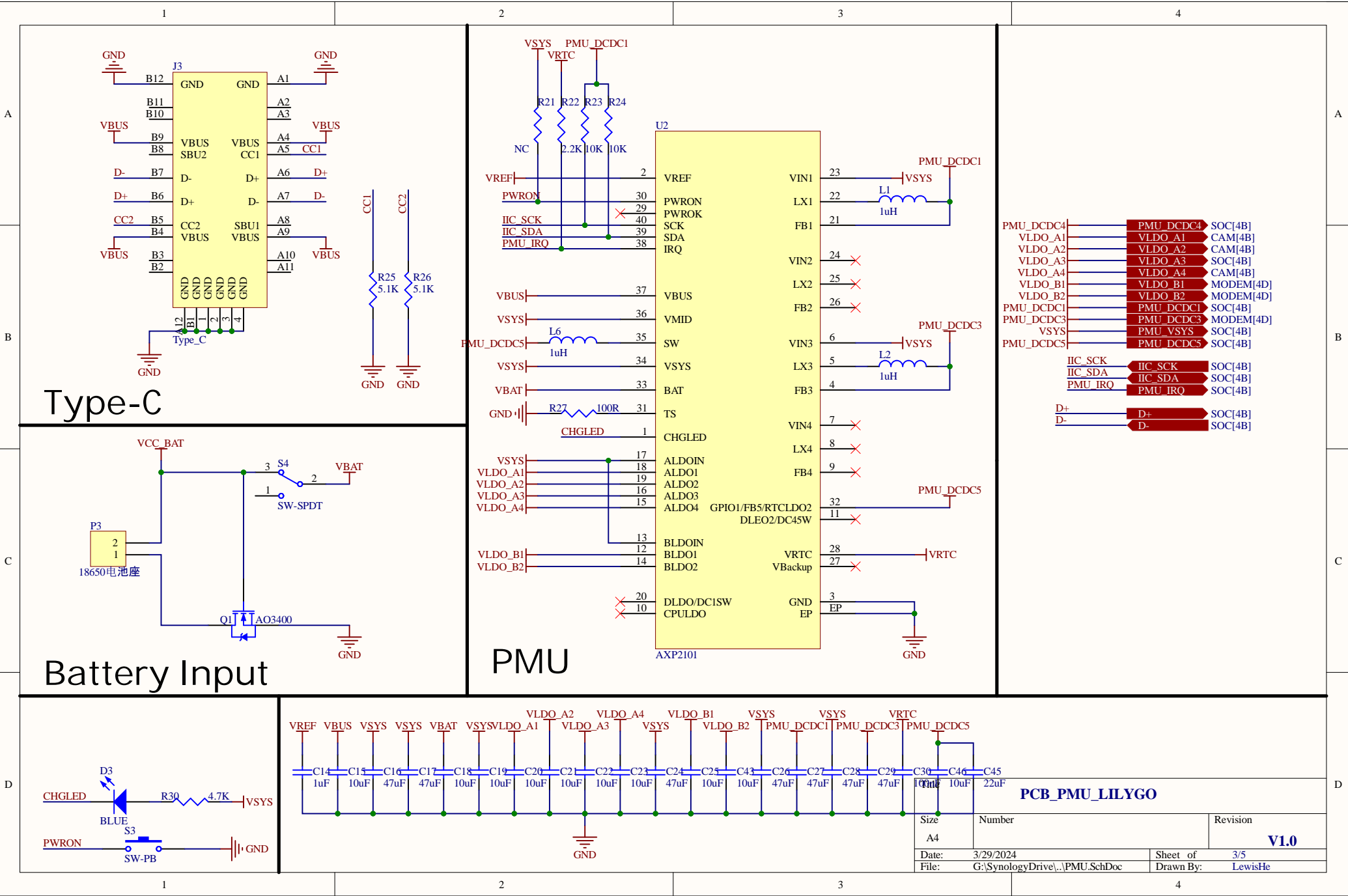


External download



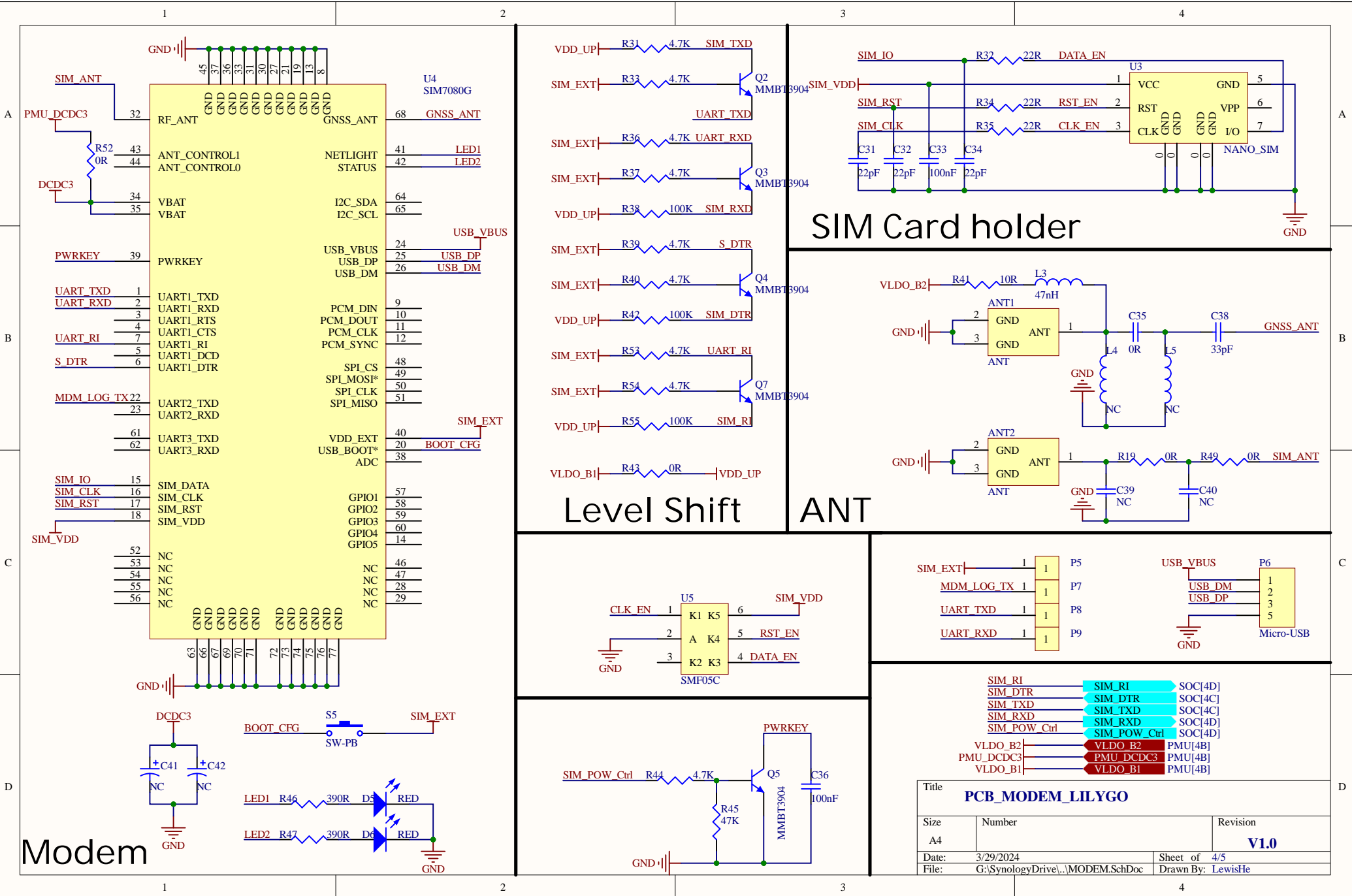
External Pin

Title			
PCB_SOC_LILYGO			
Size	Number	Revision	
A4		V1.0	
Date:	3/29/2024	Sheet of	2/5
File:	G:\SynologyDrive\...\SOC.SchDoc	Drawn By:	LewisHe



PMU_DCDC4	PMU_DCDC4	SOC[4B]
VLDO_A1	VLDO_A1	CAM[4B]
VLDO_A2	VLDO_A2	CAM[4B]
VLDO_A3	VLDO_A3	SOC[4B]
VLDO_A4	VLDO_A4	CAM[4B]
VLDO_B1	VLDO_B1	MODEM[4D]
VLDO_B2	VLDO_B2	MODEM[4D]
PMU_DCDC1	PMU_DCDC1	SOC[4B]
PMU_DCDC3	PMU_DCDC3	MODEM[4D]
VSYS	PMU_VSYS	SOC[4B]
PMU_DCDC5	PMU_DCDC5	SOC[4B]
IIC_SCK	IIC_SCK	SOC[4B]
IIC_SDA	IIC_SDA	SOC[4B]
PMU_IRQ	PMU_IRQ	SOC[4B]
D+	D+	SOC[4B]
D-	D-	SOC[4B]

Size	Number	Revision
A4		V1.0
Date:	3/29/2024	Sheet of 3/5
File:	G:\SynologyDrive\PMU.SchDoc	Drawn By: LewisHe



Modem

SIM Card holder

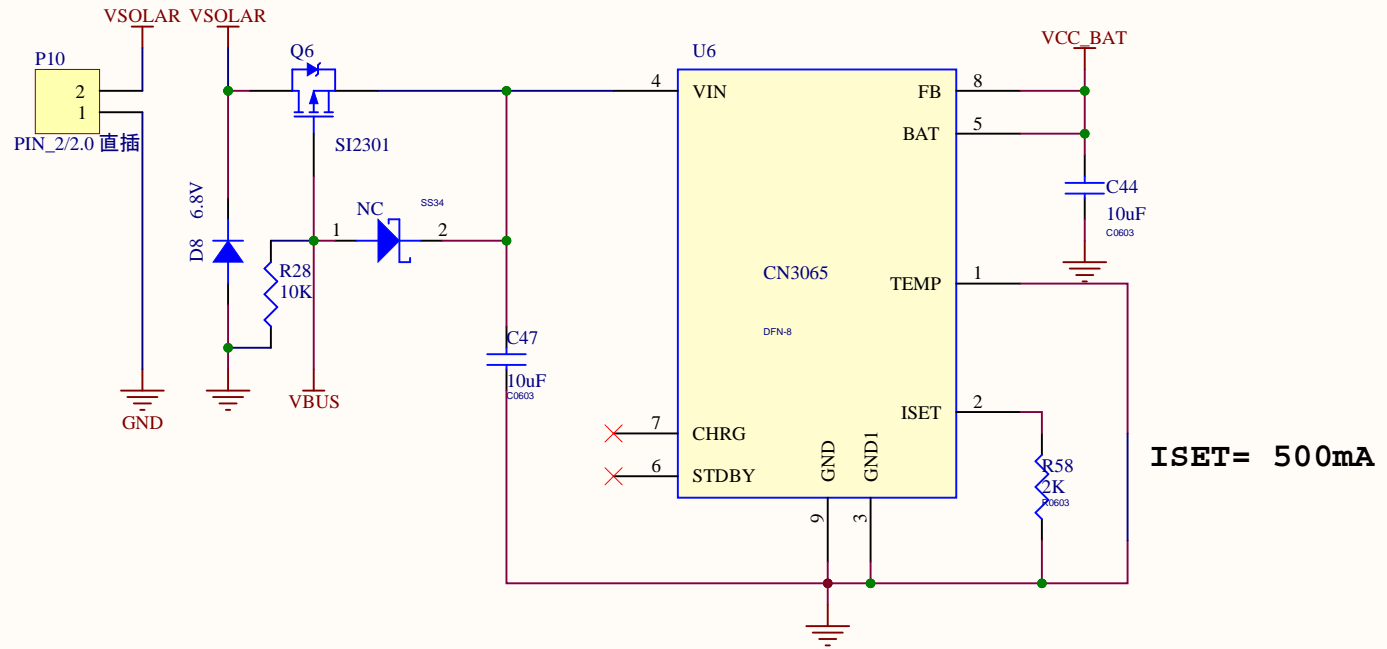
Level Shift

ANT

SIM_RI	SIM_RI	SOC[4D]
SIM_DTR	SIM_DTR	SOC[4C]
SIM_TXD	SIM_TXD	SOC[4C]
SIM_RXD	SIM_RXD	SOC[4D]
SIM_POW_Ctrl	SIM_POW_Ctrl	SOC[4D]
VLDO_B2	PMU[4B]	PMU[4B]
PMU_DCDC3	PMU[4B]	PMU[4B]
VLDO_B1	PMU[4B]	PMU[4B]

Title		PCB_MODEM_LILYGO	
Size	Number	Revision	V1.0
A4			
Date:	3/29/2024	Sheet of	4/5
File:	G:\SynologyDrive\...\MODEM.SchDoc	Drawn By:	LewisHe

Input voltage 4.4-6V



Solar Energy Input

Title			PCB_SOLAR_LILYGO	
Size	Number		Revision	
A			V1.0	
Date:	3/29/2024		Sheet of	5/5
File:	G:\SynologyDrive\...\Solar.SchDoc		Drawn By:	LewisHe