H Bharath Bhat

Address: No 209, Yashaskaram, 3rd Cross, Vidyamanya Nagar, Bengaluru 560091

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Personal Details

Date of Birth:	28.05.2002
Language Proficiencies	English, Kannada, Tulu

Professional Experience

Research Intern   *On-site*
Indian Institute of Science(IISc), Bengaluru   *June 2023 - Present*

Worked under the guidance of **Prof. Sumit Kumar Mandal** *on* **Energy Efficient Hardware Accelerators for Machine Learning Algorithms**
Currently working on Hardware implementation of **Matrix Multiplication using only Addition**
Skills Enhanced: *Verilog · Python · C · RTL Design*

Education

Bachelor of Engineering (B.E)   *JSS Academy of Technical Education*
Electronics and Communication Engineering   *2021 - Present*
University: Visvesvaraya Technological University (VTU)
Aggregate: 8.5 SGPA

Technical Skills

Programming Languages

-Intermediate level understanding of **Verilog**
-Intermediate level understanding of **Python**
-Intermediate level understanding of **C/C++**
-Intermediate level understanding of **HTML5, CSS3**
-Intermediate level understanding of **Embedded C**

Tools/Libraries

-Vega Processors: Aries V2.0 and V3.0 · Arduino UNO
-Visual Studio Code · Git · Xilinx Vivado/ ISE · Cadence · Jupyter Notebook · Spyder · Codeblocks
-Multisim · Tina-Ti · Cedar · MATLAB · Arduino IDE
-Windows 7/10/11 · Ubuntu 22.04
-Matplotlib · Numpy · OpenCV · Pandas

Courses

C Programming Bootcamp-The Complete C Language Course	Udemy
Foundations of Cybersecurity	Coursera · Google
Play It Safe: Manage Security Risks	Coursera · Google
Hardware Modeling Using Verilog	NPTEL
Programming in C	Infosys SpringBoard
CSS3	Infosys SpringBoard
HTML5 - The Language	Infosys SpringBoard
Python Basics	Infosys SpringBoard
Data Analytics Using Power BI Tools	VI Foundation

Workshops

Present and Future Computing Systems	CSA, IISc, Bengaluru
Vega Processor and its Applications	JSSATE, Bengaluru

Projects

AI Based Face Recognition *Feb 2023*

Developed for 30 hrs **Karnataka State Police Hackathon**, *conducted at IIIT-B*
Skills Enhanced: *HTML · CSS · Javascript*

Tech Fusion Cruiser *Jan 2024*

This project was developed on ARIES V3.0 Development Board as a part of Inter-branch workshop at JSS Academy of Technical Education, Bengaluru
Skills Enhanced: *Embedded C*

Extracurricular Activities

Carnatic Music *2017*

Karnataka State Board
Have Cleared Karnataka Music (Vocal) Junior Examination

Social Media Presence

LinkedIn	https://www.linkedin.com/in/hbharathbhat/
Github	https://github.com/bhatbharath
Portfolio	https://bhatbharath.github.io/Potfolio/

Interests

Geo Politics
Hackathons
Cycling