John Paul Perrera

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Career Objective:

• To work in a globally competitive environment on challenging assignments that shall yield the twin benefits of the job satisfaction and a steady-pace professional growth.

Educational Qualification:

Degree	Board/Institute/University	Year of Passing	Result
B.Tech.	Sardar Vallabhbhai National Institute of Technology, Surat	June 2018	8 CGPA
HSC	Kendriya Vidhyalay, Goregaon	April 2014	85%
SSC	Kendriya Vidhyalay, Goregaon	April 2012	90%

Training:

• Organization: BHEL – Heavy Fabrication Block, Hardwar.

Duration: 3 month

Description: This project was intended to study the various manufacturing processes in the

block.

• **Organization:** N.T.PC – Boiler Maintenance Department, Dadri.

Duration: 3 month

Description: This project was intended to conduct a detailed study of BMD and other

activities in the Power plant

Technical Skills:

- Embedded Systems (Microcontroller 8051)
- Programming in Assembly language
- Basic programming in VHDL language and VHDL systems.
- C Language

Extra-Curricular Activities:

- Student Head of the quarterly newsletter `AAROHAN' and Member of Creative Writing Team in the Editorial board for the Institute magazine.
- Secured Second position in Situation Reaction Test held under Engineer's Day celebrations in SVNIT, Surat
- Member of organizing committee in `TECHFEST' (Institute Technical Festival)
- Won prizes in various competitions organized by LITERARY CLUB of the Institute.
- Secured 1st rank at 'Inter School Debate Competition'.

Strength:

- Self motivated, focused and dedicated
- Good Managerial and interpersonal skills
- Good communication Skills

Personal profile:

- Date of Birth: 24 september, 1997
- Gender: Male
- Nationality: Indian
- Languages Known: English, Hindi, Marathi, Konkani
- Postal Address: B-12 Rajhans Apartment, B/h Radian hotel, Andheri East, Mumbai-400047

Reference:

Prof. Shyam Agrawal

Head, Electronics and Communication Department

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