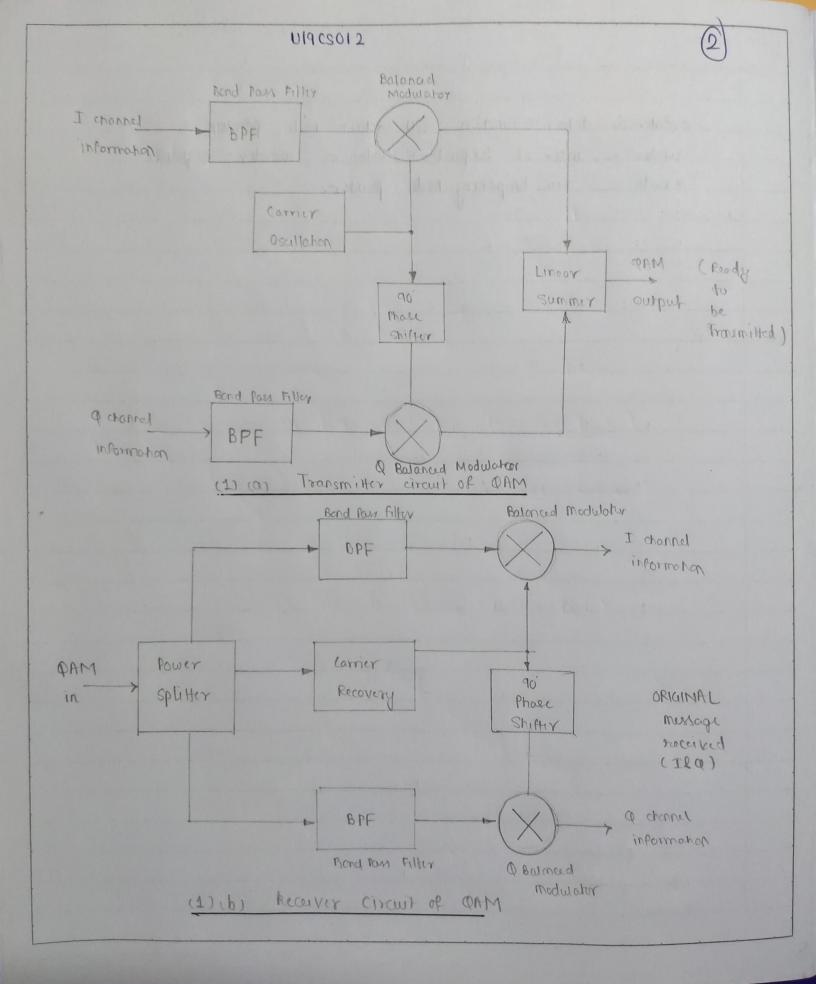
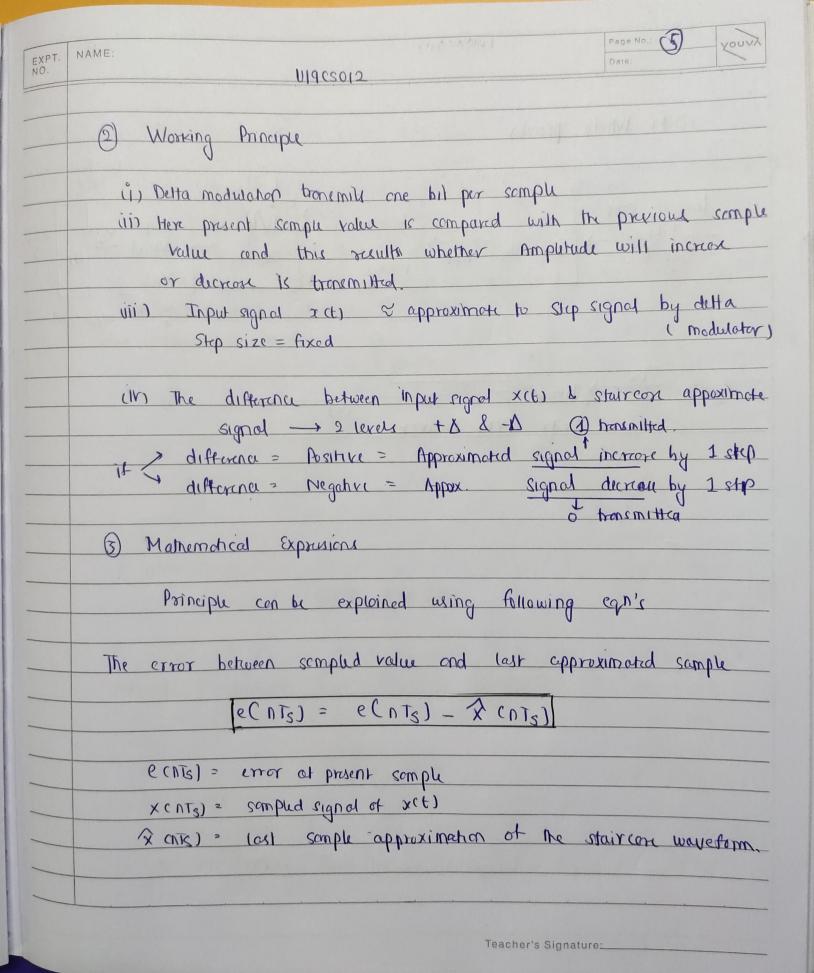
107	NAME:	Page No.: 01	YOUV
NO.	NAME:	Date: 8/12/20	
	UI9CSO12	, , , , , ,	
	Subject Name: DIGITAL COMMUNICATION (DCOM) Admission Number: U19CSO12 Full Name: BHAGYA VINOD RANA	EC209	
	Division: A		
	Total Pages: 11		
_01>	Transmitter and Receiver of two separate balances two carrier waves of same freq, but a		1
	phase by 90°.		
	1 OAM (quadrature Ampuitude Modulation)		
	A signal in which two carriers shifted i	in phase	by
	90 degrees (i.e. sine and cosine) are	modulahd o	nd
		.,	
	combined.		
	@ one signal is called In-phase or "I" signal	(Sine)	
	other signal is called quadrature or "q" signal	(cosine)	
	QAM Transmitter (Figure 1 (a))		
	3 Idea derived from Basic OAM theory, two corner	signal with	
	phase snift 90. These are then amplitude modulate		
	data streams known as I (Inphase) or O (quadr		
	C generated in Basebond		1
		· ·	
	(4) The two resultant signals are summed and t	processed	d
	as required in RF signal chain,		
	RF Amplifier must be linear to preserve the integr	rity of signi	ul.
	Any non-linearities will after -> relative leve	a of signals	
	-> phase difte		
	Possibility of Error.		

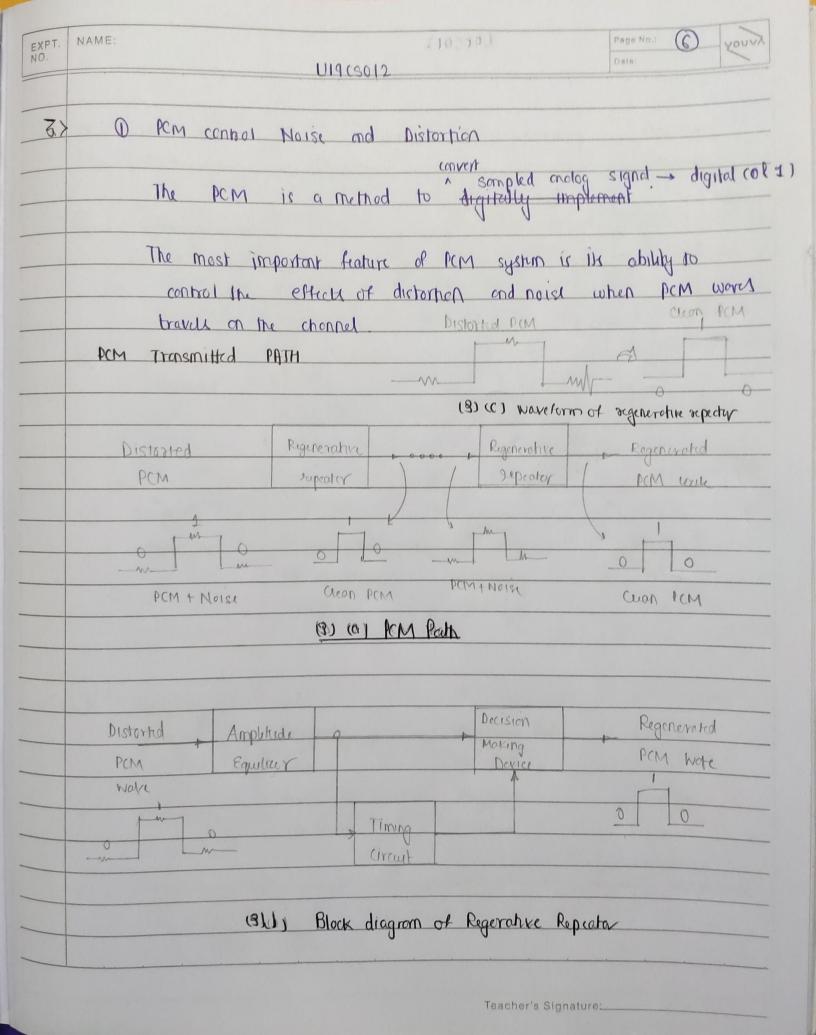
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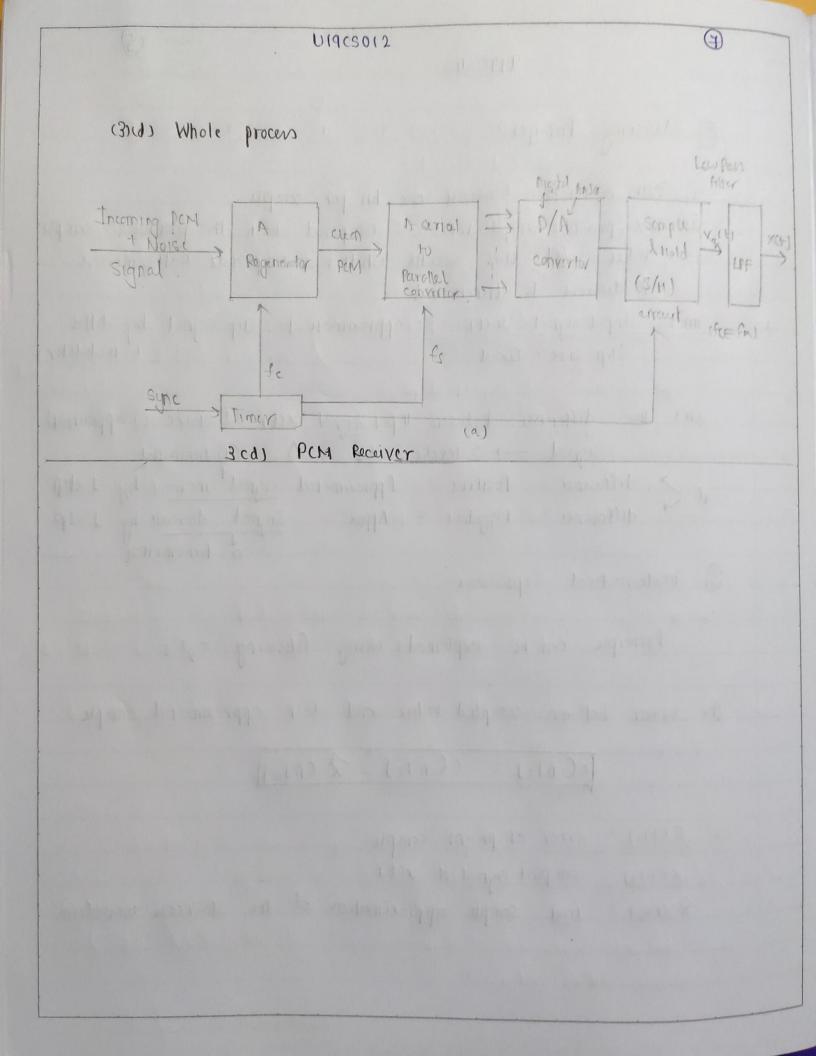


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No.	U19CSQ12 Date:					
	det two carrier signals be					
	Ves = Ve cos (wet)					
	Vc2 = Vc SIN(wct)					
	(orresponding QAM (After tronsmitter summeter)					
	PAM We cas (wet) + Vm2 Ve sin (wet)					
	in phase quadrature					
	RECEIVER (QAM) (Figure 1 (b))					
	6 mestage signal are recovered using coherent detection.	(wel				
	all put of Analog multiplet I phase	sin (wet) cos				
	$\beta_i = V_{\text{PAM}} V_c' \cos(\omega_{ct}) = \frac{V_{\text{m_1}} V_c V_c'}{V_{\text{m_2}} V_c V_c'} \cos(2\omega_{ct}) + V_c$	m2 VcVc				
	2 2					
	scaled rursian Low pain filter					
	Output of Analog Multiplier + Q Phoss					
	Sy = Very Very sin(wet) = Vm2 Vere, A Time Very Sin(2wet) + Vm1 Very Sin	(Wict) (M)				
	QAM 2					
	In this way, we can transmit two independent message signe	all				
	on the come bondwidth with help of two carrier (with 90)					
	On the some process.					
00	Tronsmitter 2 Pecciver block single bit per somple digital	mod				
2.7	1) Wester form with methemetrical expression					
	3 Working					
-	O D I con O alta S a land a land in h					
-	Delta modulation (Sampung rote >> stepsice after quentization)					
	> The design of modulator l'abmodutor is simple					
-	> Starcase approx of control workform.					
	I bit rate can be decided by user. (Single bit/sample					
	(1 bit DPCM schene)	-				

Teacher's Signature:







EXPT.	NAME:			199. PPJ	Page No.: YouvA
			Ulacso12		
4.7	Sr Nc-	Parameter of comparision	Ideal or Instantaneous	Matural sampling	Flat top Sempung
	1	Generation araut Carant of sampler	76	July sith	ran Discharge
	2	Marcforms	XC+1 XC+1	X (VA?)	X(H) X(NH)
	3	Sompung	It uses multiplicated by on impulse teenthon	The used chepping principle	Somph l Hord Circuit
	4	Sampung	Sampung rate tends to In finity.	Scompung Pake schehose Nyquist Criteria	Semping Rate Sahshed Nyquist Contena

