CO-Quiz 5 of 5

Logisim gives _____ color to wire whenever there are problems with incompatible bus width or conflicting value, *

- ☑ Orange
- ☐ Brown
- ☑ Red
- ☑ Black

The following truth table is the implementation of _____. *

A	B	C	Y
0	0	0	1
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	0

- ○ Y = (A' + BC)
- ○ Y = (B' + AC)
- ○ Y = A + B + C
- ◉ B'

_____ can combine multiple wires into a single wire with a wider bit width. *

(●) Splitter

(○) Separator

(○) Combiner

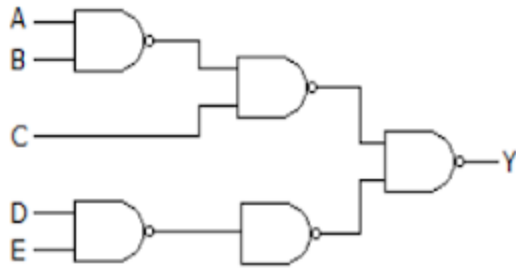The logical expression C=A+A'B is not equivalent to *

[✓] C=A'B

[✓] C=AB

[ ] C=A+B

[✓] C=A'+B

Once you ready with preparation of circuit in logisim circuit, none of your circuit wires should be _____ or _____. *

[ ] Brown

[✓] Blue

[ ] Black

[✓] Gray

The following circuit in logisim is the implementation of _____. *

○ Y = A' + B' + C' + D' + E'

◉ Y = (A' + B')C + (DE)'

○ Y = AB + C + DE

○ Y = AB + C(D+E)

In logisim, both AND and OR gates can have only two inputs. *

◉ True

○ False

A full adder can be made using _____ . *

◉ two half adders

○ three half adders

○ two half adders and a OR gate

○ two half adders and a NOT gate

The inputs of a NAND gate are connected together. The resulting circuit becomes of _____ gate. *

○ AND

◉ NOT

○ OR

○ NOR

The unconnected legs of the gate will be of color _____ . *

○ Green

○ Brown

◉ Blue

○ Gray

The minimum number of 2-input NAND gates required to simulate the function S = (A + B) (C + D) is _____. *

○ 6

○ 3

○ 4

◉ 5

The output of the half adder is _____. *

○ carry

○ none of these

◉ sum and a carry

○ sum

In logisim, the circuit can also be made by the repeated use of *

○ NOT

○ OR

◉ NAND

○ NOR

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