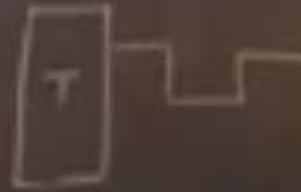


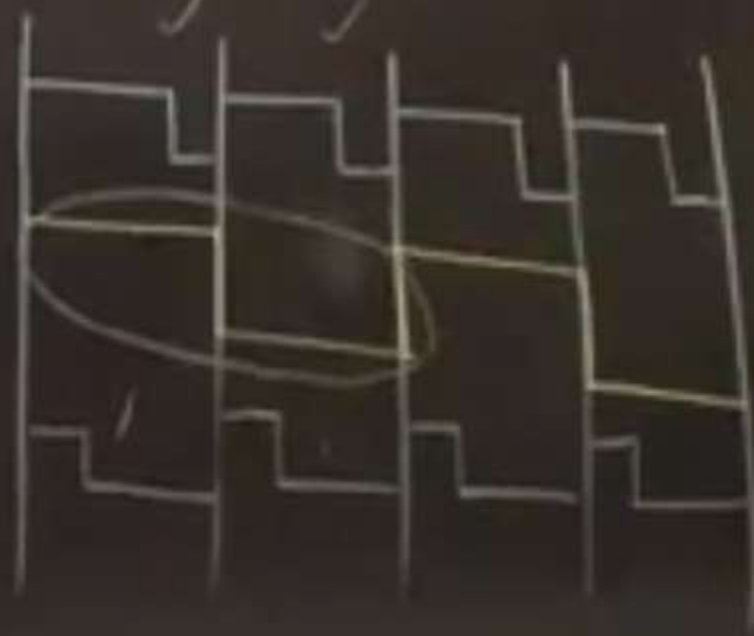
CLK generation

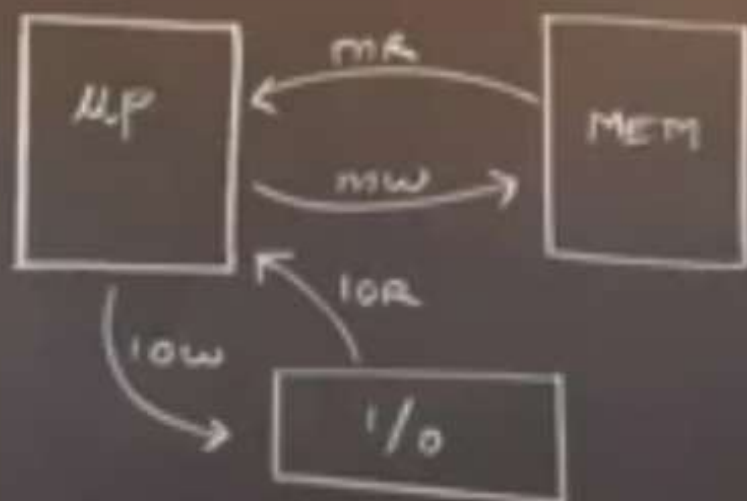


X_1, X_2

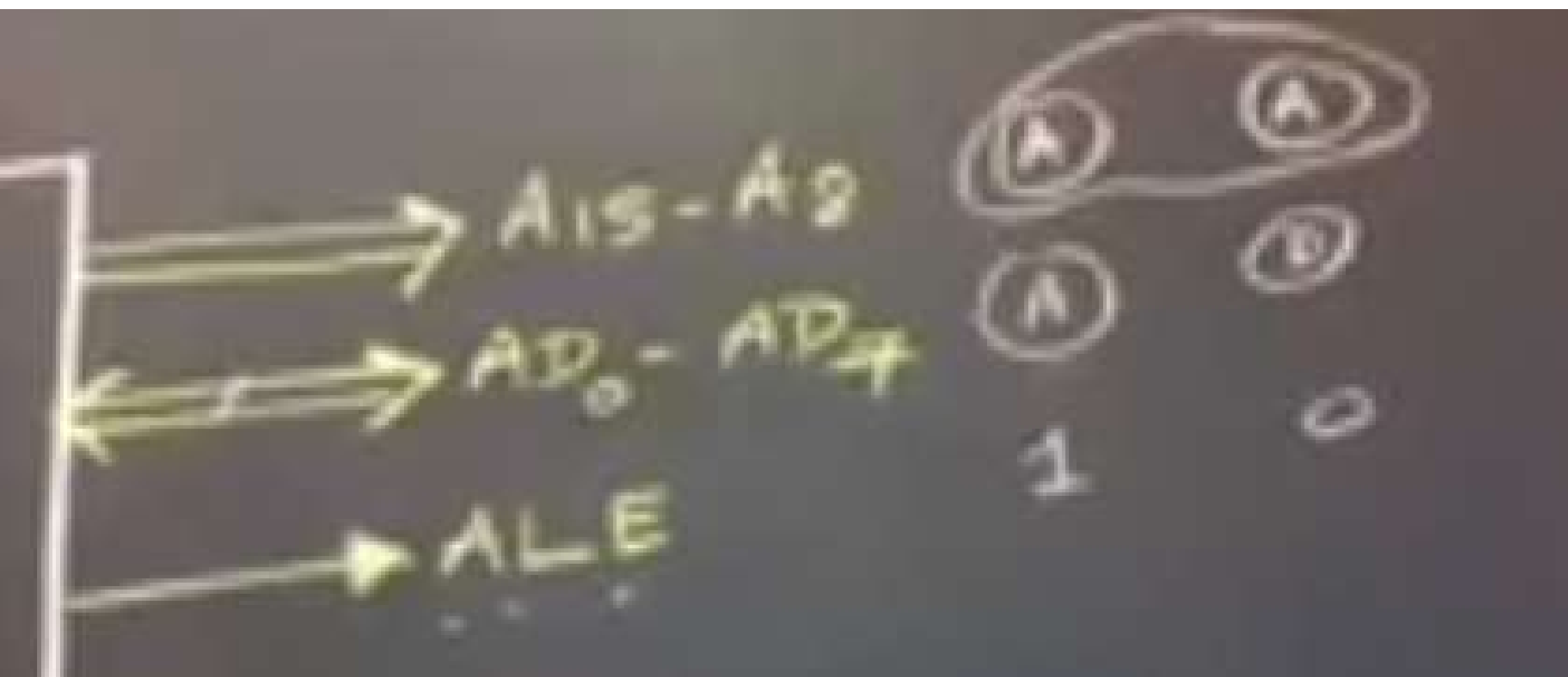
CLK

X_1, X_2

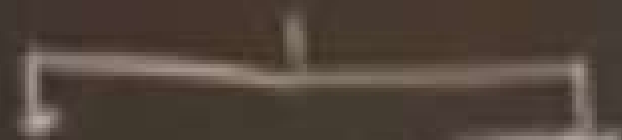




	$\overline{IO/\overline{M}}$	\overline{RD}	\overline{WR}	S_1	S_0
MR	0	0	1	1	0
MW	0	1	0	0	1
IO _R	1	0	1	1	0
IO _w	1	1	0	0	1



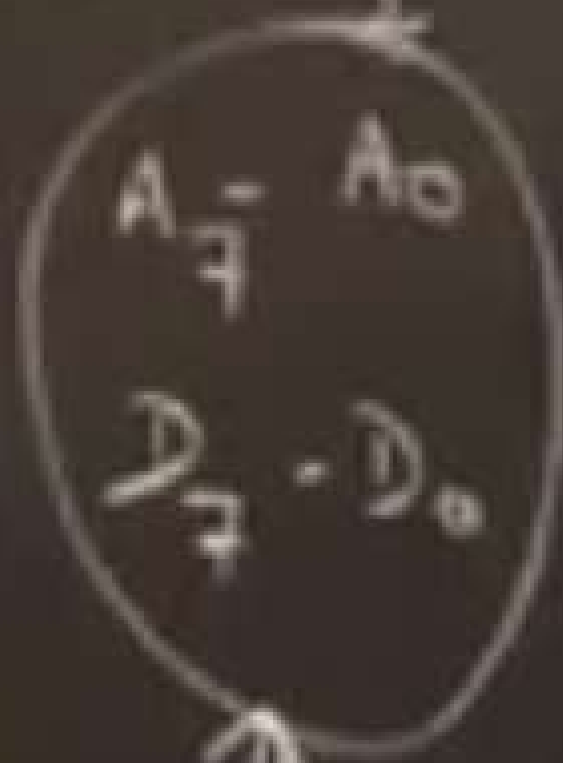
$$A_{15} - A_0$$



$$A_{15} - A_8$$



$$A_{15} - A_8 =$$

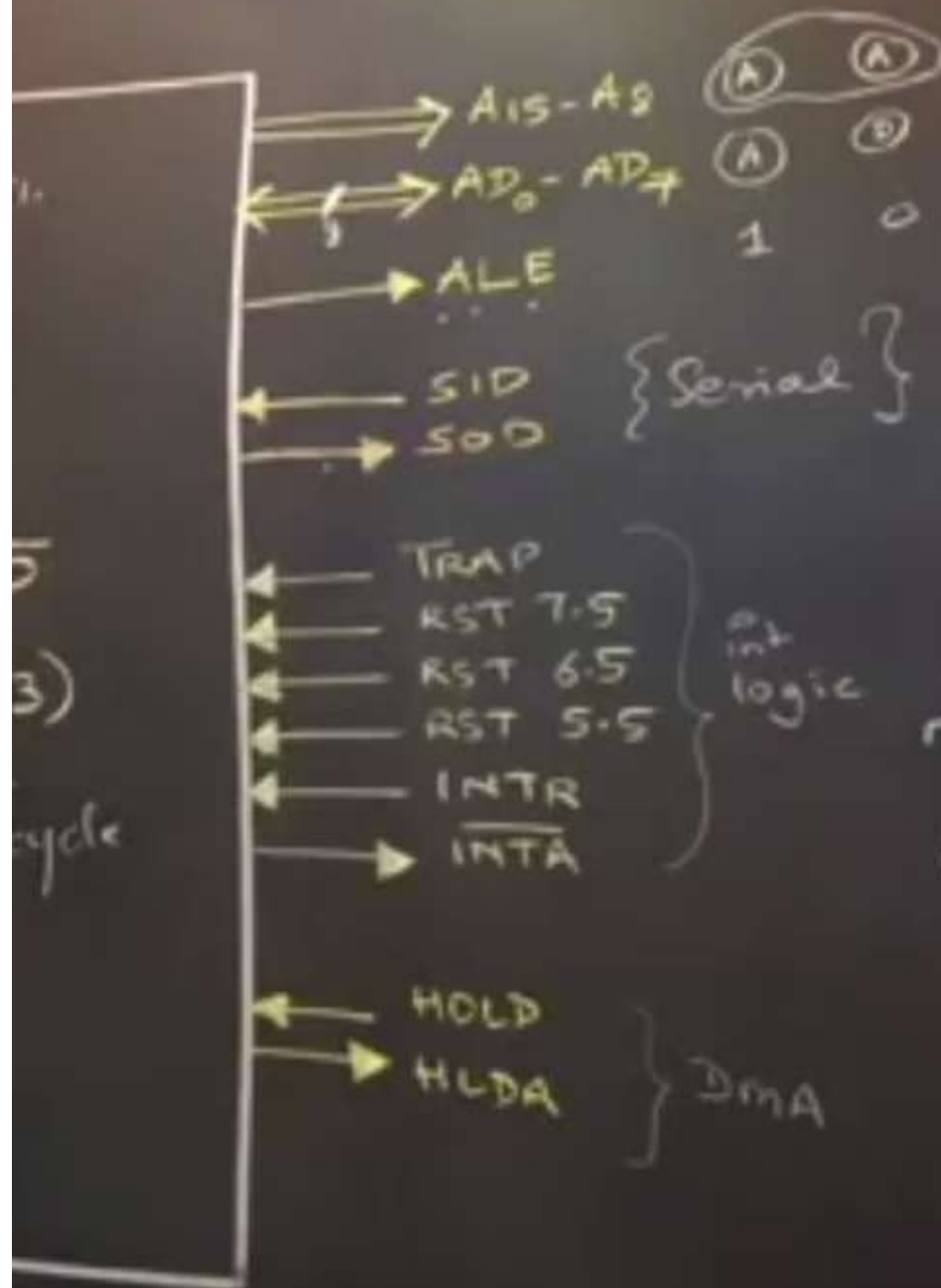


$$A_7 - A_0$$

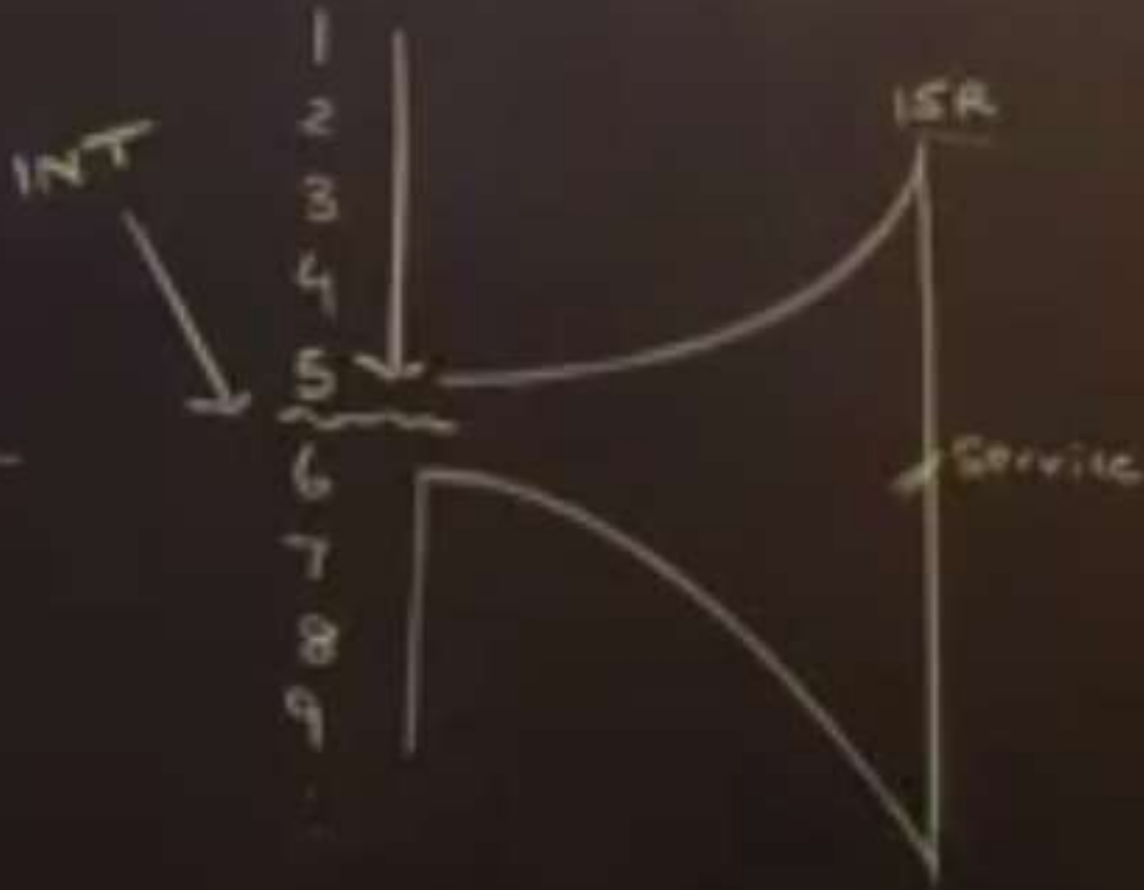
$$D_7 - D_0$$



$$A_{D_7} - A_{D_7}$$



Concept of interrupts



Concept of DMA

