



Sardar Vallabhbhai National Institute of Technology, Surat

ECED Department

Subject: Digital Electronics and Logic Design (EC-207)
B.Tech Computer Engineering, Sem-III, Div - (A)

Date: 27-08-2020

Assignment for Practical - 3

Design and verify their functionality of below circuits with the help of Multisim.

1. Design and implement Half Adder and Half Subtractor (Single Circuit) using Mode Control 'M'.
2. Design and implement the circuit in question '1' by using least number of NAND gates only.
3. Design and implement the circuit in question '1' by using least number of NOR gates only.