



# Sardar Vallabhbhai National Institute of Technology, Surat

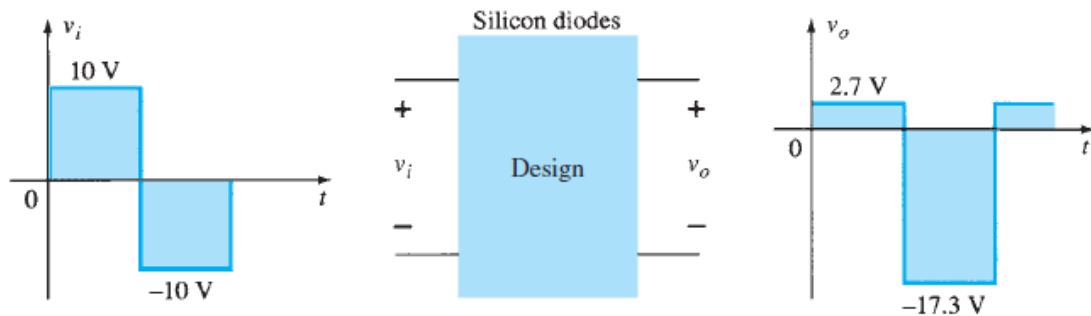
ECED Department

Subject: Digital Electronics and Logic Design (EC-207)  
B.Tech Computer Engineering, Sem-III, Div - (A)

Date: 22-10-2020

## Assignment for Practical - 8 & 9

1. Design a Clamper to perform the following operation.



2. Calculate and Plot  $V_o$  for the following circuit.

