

CO-Quiz 4 of 5

Total questions : 10

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* Required

In which addressing mode the operand is given explicitly in the instruction *

- ☐ None of these
- ☒ Immediate
- ☐ Direct
- ☐ Register

When a subroutine is called, the address of the instruction *

- ☒ following the CALL instructions stored in the stack.
- ☐ following the CALL instructions stored in the PC.
- ☐ of the called subroutine is stored in the stack.
- ☐ which is currently executing is stored in the stack.



The instruction(s) which depend/s on the sign extend unit for their correct operation are _____ *

- ☒ beq
- ☒ j
- ☒ sw
- ☒ lw
- ☐ none of these

Given memory address accesses the multiple words then the memory organization is known as *

- ☐ none of these
- ☐ bytes addressing
- ☒ word addressing
- ☐ bits addressing

In a multi cycle processor, using _____ because fetching the instruction and loading the data can occur in _____. *

- ☒ one memory is possible, two different cycles
- ☐ one memory is not possible, one single cycle
- ☐ none of these
- ☐ both of these



The instruction format that has an advantage of short programs when evaluating arithmetic expressions is _____. *

- ☐ One address
- ☐ Zero address
- ☐ Two address
- ☒ Three address

An M-bit processor has *

- ☐ Both of these
- ☐ M-bit Program Counter
- ☐ None of these
- ☒ M-bit Instruction Register

Branch instructions on MIPS can only jump backward _____ and forward _____ instructions. *

- ☐ 32766, 32767
- ☐ 32768, 32769
- ☒ 32768, 32767
- ☐ 32767, 32768



The addressing mode techniques help *

- ☐ None of these
- ☐ to reduce number of bits in the field of instruction
- ☒ Both of these
- ☐ to specify the rule to interpret address field of the instruction

In RISC machines, the memory access instructions are *

- ☐ PUSH and POP
- ☒ none of these
- ☐ LW and SW
- ☐ CALL and JR

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