



Sardar Vallabhbhai National Institute of Technology, Surat

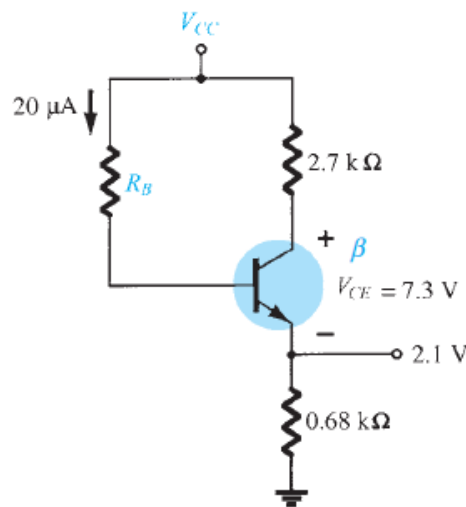
ECED Department

Subject: Digital Electronics and Logic Design (EC-207)
B.Tech Computer Engineering, Sem-III, Div - (A)

Date: 29-10-2020

Assignment for Practical – 10

1. Using the information provided in the figure below, determine the values of Beta, V_{CC} and R_B theoretically. Also compute and verify the values of I_C , V_B , and V_C by implementing the circuit on Multisim.



2. Simulate the below given circuit on Multisim and predict the type of Digital Logic implemented by the same. Use the default transistor available in Multisim. Attach all the four screenshots (00,05,50,55).

