Sardar Vallabhbhai National Institute of Technology, Surat

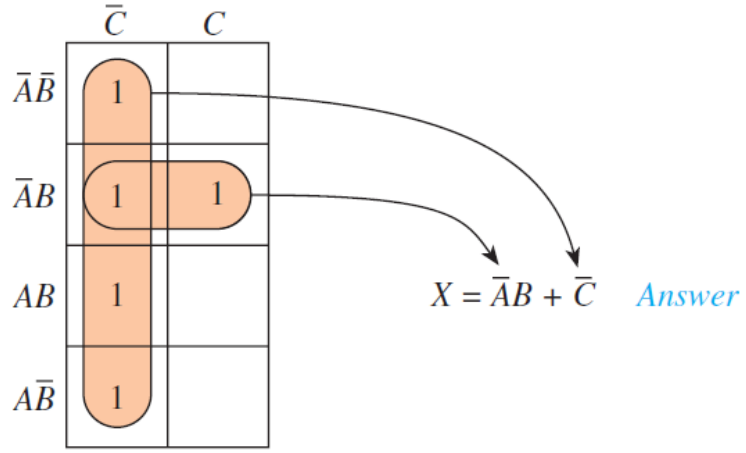
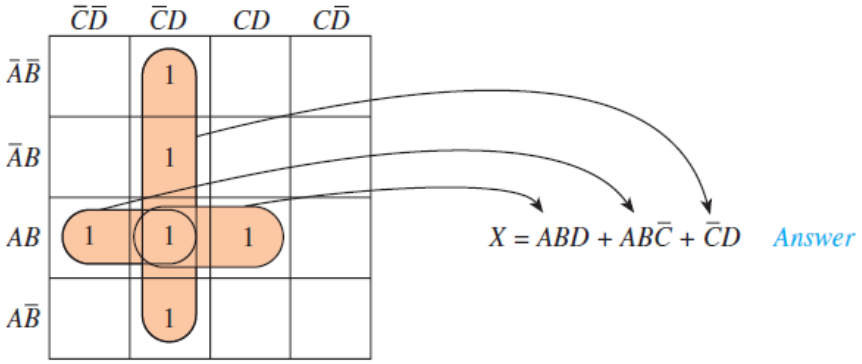
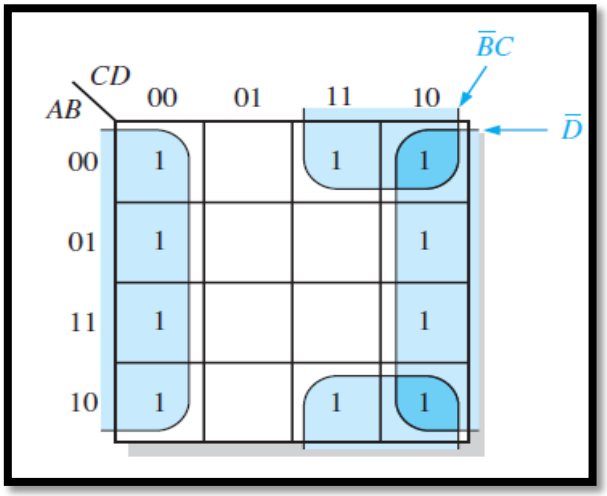
ECED Department

Subject: Digital Electronics & Logic Design (EC-207)

B.Tech Computer, Sem-III, Div – (A&B)

Date: 03-11-2020

Tutorial – 7 Hints/Solutions

1		2
2		3
3	 $\bar{D} + \bar{B}C$	3

<table>
<tr><td>4</td><td>Type: Asynchronous
5-Bit Down Counter
Modulus – 32 Counter</td><td>2</td></tr>
<tr><td>5</td><td>Modulus – 12

Valid States
<table><tr><td>1</td><td>0000</td></tr><tr><td>2</td><td>0001</td></tr><tr><td>3</td><td>0010</td></tr><tr><td>4</td><td>0011</td></tr><tr><td>5</td><td>0100</td></tr><tr><td>6</td><td>0101</td></tr><tr><td>7</td><td>0110</td></tr><tr><td>8</td><td>0111</td></tr><tr><td>9</td><td>1000</td></tr><tr><td>10</td><td>1001</td></tr><tr><td>11</td><td>1010</td></tr><tr><td>12</td><td>1011</td></tr></table></td><td>2</td></tr>
<tr><td>6</td><td>a. 1536 and 175 Hz
b. 1400 and 192 Hz</td><td>3</td></tr>
</table>