



Sardar Vallabhbhai National Institute of Technology, Surat

ECED Department

Subject: Digital Electronics and Logic Design (EC-207)
B.Tech Computer Engineering, Sem-III, Div - (A)

Date: 03-09-2020

Assignment for Practical - 4

Design the below given circuits. Verify their Functionality with the help of Multisim.

1. Full Adder using least number of NAND Gates.
2. Full Subtractor using least number of NOR Gates.
3. Two bit Adder circuit using Full Adders. Attach screenshots for any four input combinations.

