

# TUTORIAL 6

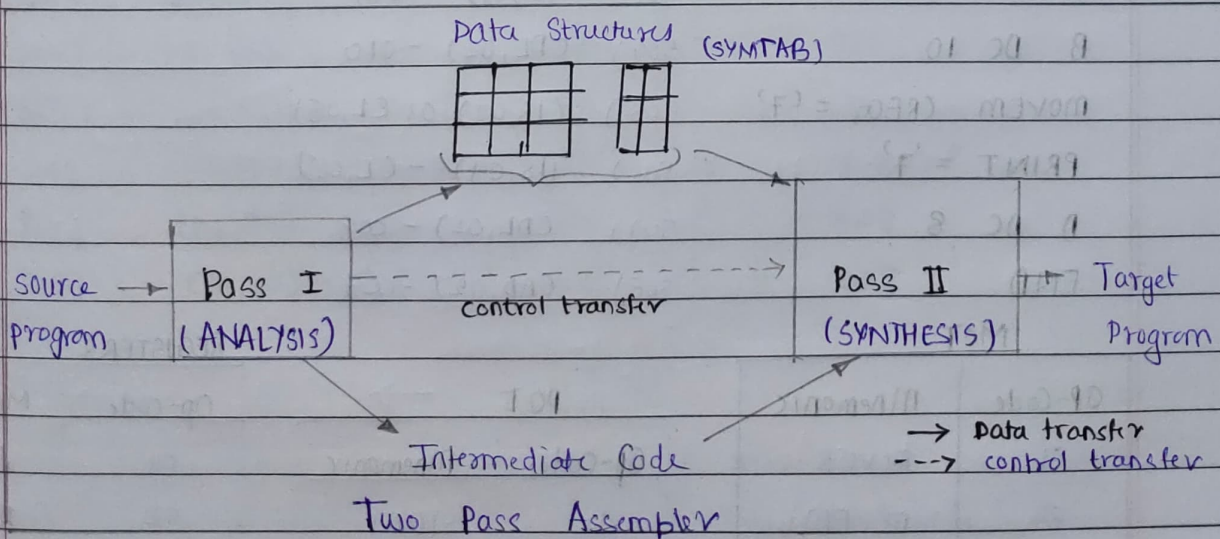
UI9CS012

1. Explain two-pass assembler functions with example program.
1. ① The two-pass assembler scans the input assembly language twice.
- ② These scans are - normally called Pass I and Pass II.  
(ALP → IC)      CIC → machine code)  
[ALP = Assembly Level Program] I.C. = Intermediate Code

Tasks performed by the passes of two pass assembler are as follows:

- Pass 1:
- ① Separate the symbol, mnemonic opcode and operand fields.
  - ② Build the symbol table
  - ③ ~~Perman~~ <sup>form</sup> the LC (location counter) processing
  - ④ Construct Intermediate representation (or IC)

Pass 2: ① Synthesize the target program.



• It can handle forward references easily.

## Example of Two-Pass Assembler Program

### Source Program

START 100

MOVER AREG, A

LOOP: PRINT B

ADD BREG, = '9'

SUB BREG, D

COMP CREG, = '23'

LTORG

A DS 3

LABEL: EQU LOOP

ORIGIN 500

L1: MBLT CREG, = '7'

SUB BREG, = '93'

LTORG

B DC 10

MOVEM CREG, = '7'

PRINT = '7'

D DC 8

END

LC

(Symbol table)

(AD, 01) - (C, 100)

Sym-no Symbol Address

100) (15, 01) 01 (S, 01)

1 A 107

101) (15, 09) - (S, 03)

2 LOOP 101

102) (15, 03) 02 (L, 1)

3 B 504

103) (15, 04) 02 (S, 04)

4 D 507

104) (15, 08) 03 (L, 02)

5 LABEL 101

105) (AD, 05) - 009

6 L1 500

106) (AD, 05) - 023

107) (DL, 01) - 03

(Literal table)

Lit-no Literal Address

1 = '9' 105

2 = '23' 106

3 = '7' 502

4 = '93' 503

5 = '7' 508

504) (PL, 02) - 010

505) (15, 02) 03 (L, 05)

506) (15, 09) - (L, 05)

507) (DL, 02) - 008

508) (AD, 02) - 007

MOT

REGISTERS

OP-Code	Mnemonic	POT	Op-Code	Mnemonic	Op-Code	Mnemonic
01	MOVER	Op-Code Mnemonic	01	AREG	01	AREG
02	MOVEM	01 START	02	BREG	02	BREG
03	ADD	02 END	03	CREG	03	CREG
04	SUB	03 EQU	04	DREG	04	DREG
05	MBLT	04 ORIGIN				
06	DIV	05 LTORG				
07	BC	DL { 01 DS				
08	COMP	{ 02 DC				



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Pass II

Intermediate Code	Target Code
CAD, 01) - CC, 100)	01 - 100
100) CIS, 01) 01 (S, 01)	100) 01 01 107
101) CIS, 09) - (S, 03)	101) 09 - 504
102) CIS, 03) 02 (L, 1)	102) 03 02 105
103) CIS, 04) 02 (S, 04)	103) 04 02 507
104) CIS, 08) 03 (L, 02)	104) 08 03 106
105) CAD, 05) - 009	105) - - 009
106) CAD, 05) - 023	106) - - 023
107) CDL, 01) - 03	107) - - -
500) CIS, 05) 03 (L, 03)	500) 05 03 502
501) CIS, 04) 02 (L, 04)	501) 04 02 503
502) CAD, 05) - 007	502) - - 007
503) CAD, 05) - 093	503) - - 093
504) CDL, 02) - 010	504) - - 010
505) CIS, 02) 03 (L, 05)	505) 02 03 508
506) CIS, 09) - (L, 05)	506) 09 - 508
507) CDL, 02) - 008	507) - - 008
508) CAD, 02) - 007	508) - - 007

output of pass-II

{ Target Code / Object code }

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2.7 What are some advantages of assembly languages over high level languages?

2.8 Advantages of Assembly Languages over High Level Languages  
(ARM, MIPS, x86) (C, C++, Fortran, Prolog)

- ① Extremely Fast
- ② Second closest programming language to hardware components
- ③ Minimalistic syntax, just few mnemonic keywords (except binary)
- ④ It is memory efficient, as it requires less memory.
- ⑤ Supports low-level operation & access machine-dependent registers & I/O
- ⑥ Helps to build compilers
- ⑦ You can get access to unusual programming modes of processor (16 bit mode to interface startup, firmware or legacy code in Intel AC)
- ⑧ You can break the conventions of usual compiler, which might allow some optimization (like temporarily breaking rules about memory allocation, threading, calling conventions)

3.7 What tools are used for Compiler Construction?

3.8

① Parser Generator  
Eg: YACC, PIC, ERM

Context free grammar

Parser generator

tokens

Syntax Analyzer

Parse tree

② Scanner Generator

Specifications of regular expression

scanner generator

source program

Lexical Analyzer

tokens

③ Syntax directed translation engine

④ Automatic code generators

⑤ Data-Flow Analysis Engine

⑥ Compiler Construction Toolkits



4. > What are Applications of Compiler? Explain.

4. > Applications of Compiler are:

- ① Full Implementation of High Level Programming Languages
- ② Support optimization for Computer Architecture Parallelism.
- ③ Design a New Memory Hierarchies of Machine.
- ④ Widely used for Translating Programs.
- ⑤ Used with other Software Productivity Tools.

5. > Differentiate between a macro and a subroutine. And explain macro definition and macro expansion using an example.

5. > MACRO

SUB ROUTINE

- |   |   |
|---|---|
| ① Macro can be called only in the program it is defined.                            | ① Sub-routine can be called from other programs also. |
| ② Macro can have max 9 parameters.  | ② can have any no. of parameters.                     |
| ③ Macro can be called only after its definition.                                    | ③ This is not true for sub-routine.                   |
| ④ Macro is used when same thing is to be done in a program a number of times.       | ④ Sub-routine is used for modularization.             |
| ⑤ Called by its name.   | ⑤ A subroutine is called by BSR or JSR instruction.   |
| ⑥ Simple to write and use.  | ⑥ subroutines are more complex (stacks are used).     |
| ⑦ Macros are faster than subroutines (no overheads, no saving of return addresses). | ⑦ Slower  |

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**Macro Definition:** A macro definition is enclosed between a macro header statement and a macro end statement.

① Macro definitions are typically at start of program.

② A macro definition consist of

- Macro Prototype Statement - declares name of macro, <sup>Kind of params</sup> name &
- One or more model statement - Assembly level statement generated during macro expansion
- Macro preprocessor statement - Perform Auxillary function

Example of definition of macro INCR

macro header →

MACRO

INCR &MEM-VAL, &INCR-VAL, &REG

MOVER &REG, &MEM-VAL

ADD &REG, &INCR-VAL

MOVEM &REG, &MEM-VAL

macro end →

MEND

Macro Expansion

- A macro call leads to macro expansion
- During macro expansion, Macro <sup>replaced</sup> sequence of call stmt → assembly stmts

Macro Expansion

Lexical Expansion

- replacement of character string by another character string during program execution.
- typically employed to replace occurrence of formal parameter by corresponding actual parameter

Semantic Expansion

- generation of instruction tailored to requirement of specific usage
- Eg: generation of type specific instruction for manipulation of byte and word operands