

# Operating System [CS-301]

## Tutorial 1

1119CS012

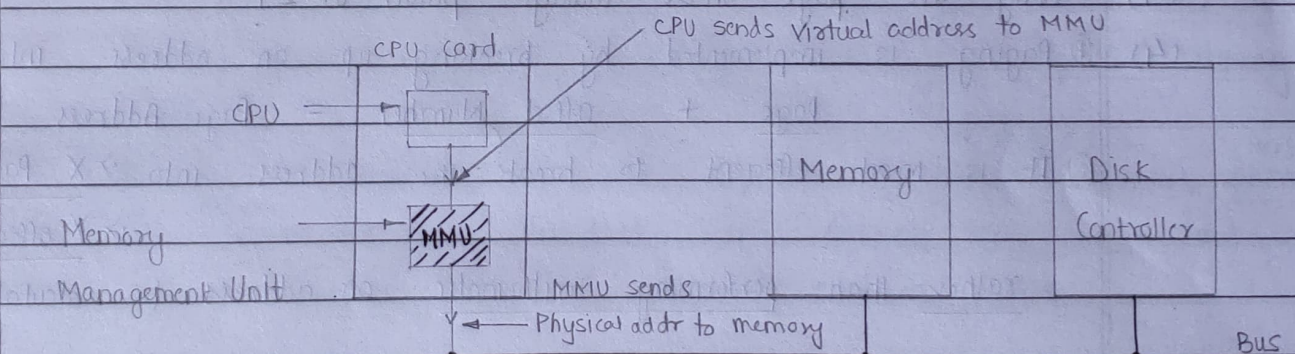
(1) An address generated by the CPU is also referred to as Physical address. True or False?

(1) False.

Since the address generated by CPU is called Logical address [Virtual Address], whereas the physical address is computed by the Memory Management Unit (MMU). [Address seen by memory unit is a physical address.]

(2) What is the hardware device that maps virtual to Physical address?

(2) Memory management Unit (MMU)



- In MMU scheme, the value in relocation register is added to every address generated by user process at the time it is sent to memory.
- The user program deals with logical address (it never sees the real physical address).

(3) Name two differences between logical and physical address.

**BASICS:** A logical address does not refer to an actual existing address, rather it refers to an abstract address in an abstract address space.

A physical address refers to actual physical address in memory.



| Parameter       | LOGICAL ADDRESS  | PHYSICAL ADDRESS  |
|-----------------|--|---|
| ① Basic         | generated by CPU   | Location in a memory unit   |
| ② Address space | Logical address space is set of all logical addresses generated by CPU in reference to a program | Physical address is set of all physical addresses mapped to corresponding logical addresses |
| ③ Visibility    | User can view logical address of a program   | User can never view physical address of a program   |
| ④ Generation    | generated by the CPU   | Computed by MMU   |
| ⑤ Access        | The user can use the logical address to access the physical                                      | The user can indirectly access Physical addr but not directly                               |

(4) Why are page sizes always power of 2?

(4) ① Paging is implemented by breaking up an address into a  
 $\text{Page} + \text{Offset Number} = \text{Page Address}$

② It is most efficient to break the address into  $\rightarrow X$  Page bits &  $\rightarrow Y$  offset bits rather than performing arithmetic on address to calculate Page no. & offset.

③  $\text{Page Number} = \frac{(\text{Virtual address})}{(\text{Page size})}$  &  $\text{Page offset} = (\text{Virtual address}) \% \text{Page size}$

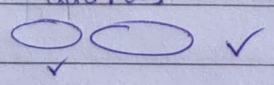
④ So, if page size is not of form  $(2^k)$ , there would be extra computation for the same No. offset

Eg: if address is 84 (1010100) & Page size = 16 ( $2^4$ )  
 the page no = 101  
 page offset = 0100  
 $\left. \begin{aligned} &VA/2^k = VA \gg k \\ &VA \% (2^k) = VA \& (2^k - 1) \end{aligned} \right\}$

Since Each bit position represents a power of 2.

⑤ Therefore, each page sizes are always in power of 2 addresses

[Avoid extra computation]





11/9/2012

(5) An application Program is being designed and developed for a microprocessor based controller for an automobile. The application is required to perform the following functions:

- > Monitor and display speed of the automobile
- > Monitor the fuel level and raise an alarm (if necessary)
- > Display the Fuel efficiency
- > Monitor the engine condition and raise an alarm if unusual condition is detected
- > Periodically record some auxiliary information like speed, fuel etc.

(a) Is this a real time application? Justify your answer.

(a) Yes, this is a Real time application.

Real time system is a system that is put through real time which means response is obtained within a specified timing constraint.

In above Application, we are required to monitor various parameters like engine speed and fuel Periodically in current time frame and also raise alarm if unusual condition arises.

(b) It is proposed to create multiple processes to reduce the response time of the application. Enlist the process that should be in it. Specify the priorities.

- ① Monitor Engine Condition
- ② Alarm if Engine is in unusual condition
- ③ Monitor Fuel levels
- ④ Display Fuel levels
- ⑤ Monitor Speed
- ⑥ Display Speed
- ⑦ Repeat the step ① to ⑥ periodically.



- (6) Explain the differences between (a) single user single tasking OS  
(b) Multitasking OS  
(c) Multiprogramming OS

(a) Single user single tasking OS

- ① An operating system that allows a single user to perform only one task at a time is called single user single tasking OS.
- ② ~~Eg~~ Function like printing a document, downloading images can be performed only one at a time.
- ③ Eg: MS-DOS & Palm OS.

Advantage = uses less memory

Disadvantage = CPU is super fast device and keeping it occupied for a single task is never a good idea.

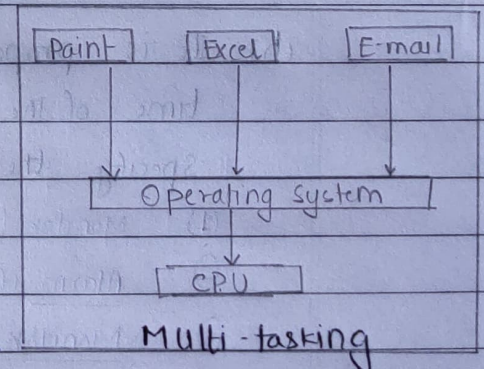
(b) Multi-tasking OS

- ① Multi-tasking is a logical extension of multiprogramming.

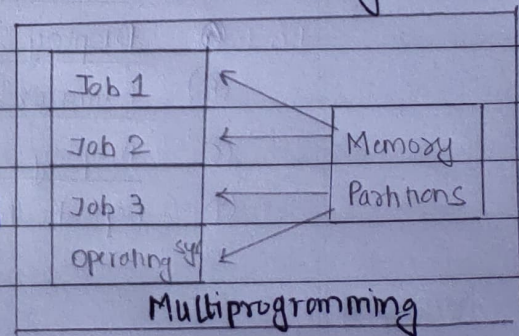
In multi-tasking, the CPU executes multiple jobs by switching among them typically using a small time quantum, and the switches occur so frequently that the user can interact with each program while it is running.

(c) Multi programming OS

- ① multiprogramming increases CPU utilization by organization jobs (code and data) so that CPU always has one to execute.



- ② The idea is to keep create multiple jobs in main memory. If one job gets occupied with IO, CPU can be assigned to other job.





| Single tasking   | Multiprogramming  | Multi-tasking  |
|--|---|--|
| ① No concept of Context switching <sup>only 1 task</sup>           | ① Concept of context switching is used.   | ① Context switching & Time Sharing is used.  |
| ② OS simply focussed on <u>one task</u> until its completed.       | ② The OS simply switches to, and executes, another job when <u>current job</u> when <u>current job</u> needs to wait. | ② The processor is typically used in <u>time sharing</u> . Switching happens when either <u>allowed time expires</u> or <u>process need IO</u> . |
| ③ CPU utilization & <del>is</del> efficiency is <u>decreased</u> . | ③ It <u>increases</u> CPU utilization by organization jobs.   | ③ It <del>is</del> increases <u>responsiveness</u> and also <u>increase CPU utilization</u> .  |
| ④ CPU idle time is <u>maximum</u> among all three OS.              | ④ The idea is to reduce <del>*</del> CPU idle time for as long as possible.   | ④ The idea is to further extend the <u>CPU utilization</u> concept by increasing <u>responsiveness</u> & <u>Time sharing</u> .                   |

(7) What do you mean by kernel and Microkernel?

- (7) Kernel
- (i) Manages communication between hardware and software
  - (ii) Responsible for managing memory, I/O to memory, <sup>hard drive</sup> cache,
  - (iii) Handles device signals, task scheduling, & other essential duties.
  - (iv) The kernel is one of the first components loaded into memory during the boot process and remain active as long as computer is operational.
  - (v) Kernels are Flexible, i.e. (UNIX, administrators, can tweak the kernels to best suit their requirements). [kernel affects OS's capabilities] computers.

Microkernel

- (i) It is compact, performing only basic functions universal to all <sup>func</sup>.
- (ii) Microkernel works with OS-specific servers that provide higher level
- (iii) This component-based structure improves a system's portability, but at expense of performance. Eg: Tau64 Unix, GNU Hurd, Mac OS X.