

SREELEKHA CHOPPAVARAPU

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Career Objective:

Seeking a challenging professional career in an organization in VLSI domain where I can constantly improve, develop and grow within the company by acquiring specific skills at each level and give my best efforts to work for overall stability and prosperity of the company.

Total work

Experience 1 years 6 months

Current

Position Assistant System Engineer, Tata Consultancy Services

Work Experience

From July 2021 to till now, Assistant System Engineer, TCS

Professional Training:

Course Name : Advanced VLSI Design and Verification

Place : Maven Silicon VLSI Design and Training Center, Bangalore

Position : Trainee

Duration : March 2022 to till date

Education:

Qualification	Institution /School	Board/University	Year of Passing	Percentage /Cgpa
B. Tech (ECE)	VIT-AP University	VIT-AP University	2021	8.61
H.S.C	Narayana Junior College	B.I.E Of AP	2017	95.7%
S.S.C	St. Lasalle English Medium School	State Board Of AP	2015	8.8

Technical Skills:

- **HDL:** Verilog
- **EDA Tool:** Quartus prime 18.1, Modelsim – Intel FPGA Starter Edition 10.5b, Mentor Graphics – Questasim
- **Core Skills:** RTL coding using synthesizable constructs of Verilog, FSM based design, Simulation, Code Coverage, Functional Coverage, Synthesis
- **Digital Electronics:** Combinational & Sequential circuits, FSM, Memories.
- **Programming Languages:** Java (Good knowledge of OOPs concept), Python basics
- **Operating System:** Linux, Windows
- **Development Languages:** Unix Basics
- **General Tools:** Microsoft Word/Excel/PowerPoint

Projects undertaken:

Router 1X3, RTL Design and Verification (July 2022 – Aug 2022)

- A router is a networking device that forwards data packets between computer networks. This project mainly emphasizes upon the study of router device, its top-level architecture, and how various sub-modules of router i.e., Register, FIFO, FSM and Synchronizer are synthesized, and simulated and finally connected to its top module
- Tools & Language used: Modelsim, Quartus Prime & Verilog

United Services Automobile Association (USAA) (July 2021 – Till date)

- USAA offers banking, investing and insurance to people who serve or served, in the United States Armed Forces.
- Developed Frontend and backend applications for Auto Insurance
- Used tools like Eclipse IDE, SoapUI, Postman for testing RESTful APIs
- Learned company standards of application software development, Agile development methodologies.

Multisensor System Using LPC2148 Microcontroller (Nov 2020 – Feb 2021)

- Designed for sensing physical parameters such as distance, temperature, and detection of object with the help of various analog sensors.
- Tools & Language used: Keil μ vision, Proteus 8 professional & C

Achievements/Extra-curricular activities:

- Awarded an appreciation for completing the PRA conducted by TCS.
- Achieved 1st prize in chess, which was conducted in VIT-AP.
- Achieved **1st rank** in SSC at school level
- Student Co-Ordinator for registration process for VITOPIA 2018 in VIT-AP.

Behavioural Skills/Hobbies:

- Team adaptable, flexible, good leadership skills.
- Playing Chess

Personal data:

- **Date of Birth:** 12/02/2000
- **Sex:** Female
- **Languages Known:** English, Telugu, Hindi

Declaration:

I declare that the information mentioned here is correct to the best of my knowledge and nothing has been concealed or distorted.

Place : Pendyala

Name : Ch. Sreelekha