

# KRISHNA CHAITANYA BHAGAVATHI

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B.Tech, ECE  
Gayatri Vidya Parishad College of Engineering

## Work Experience:

### **Assistant Systems Engineer, TCS**

Aug 2021 – Present      Hyderabad, India

- Currently working as a UI developer
- Responsible for developing, optimizing and refactoring the code base
- **Tech Stack:** HTML, CSS, learning React js.

### **Assistant Systems Engineer Trainee, TCS**

Jun 2021 – Aug 2021      Hyderabad, India

- Worked on 5G ATSSS support for (amf-smf) and added some key features in it.
- Learned c++, aws, docker.

## Projects:

### **Router project (sep - 2022)**

- Created a 1x3 router by creating different sub modules and integrating them to top module and verifying the DUT.
- This consists of 4 sub modules instantiated to the top module.
- **Tech Stack:** Verilog, Modelsim, quartus prime.

### **Synchronous Dual port RAM 16x8 (sep - 2022)**

- Created a dual port ram which is of depth 16 and the width with 8bits and verified it with test bench.
- **Tech Stack:** Verilog, Modelsim, quartus prime.

### **Density based Traffic control system using IOT and Image processing**

- Created a real-time application which detects the density of the traffic and controls the traffic.

- **Tech Stack:** python, RaspberryPi.

## **Home Automation**

- Created an IOT application which automates various tasks at home.
- It is a real time project installed at home
- **Tech Stack:** C, Node MCU, Sensors.

## Academic Qualifications:

### **B.Tech – ECE, 2021**

Gayatri Vidya Parishad College of Engineering  
CGPA: 7.87

### **XIIth Standard - MPC, 2017**

Sri Chaitanya Junior College  
Percentage: 84.6%

### **Xth Standard , 2015**

Aditya Talent School  
CGPA: 9.2

## Skills:

### **Programming Languages:**

- C, Python, Verilog, basics of SV.

### **Development:**

- HTML, CSS.

### **Tools and Technologies:**

- Modelsim, quartus-prime, RPA for Uipath Studio, VS Code, Eclipse, MS Office, Linux

### **Interpersonal Skills:**

- Good Communicator, Hardworking and Problem Solving

## Trainings:

Getting trained from MAVEN SILICON for VLSI design and verification

## Achievements & Activities:

- Active member of Rotract club, GVPCE (A), conducted events on creating awareness to students.
- Active member of IETE Club, GVPCE(A).
- Active member of wer4help club, GVPCE(A).