SHASHANK MURKI

ASSOCIATE SOFTWARE ENGINEER

CONTACT

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EDUCATION

2015 - 2019

Sreyas Institute of Engineering & Technology
Bachelor of Technology in Electronics and Communication Engineering

SKILLS

HDL: Verilog

HVL: System Verilog

TB Methodology: UVM

Protocols: AXI, AHB, UART, I2C, SPI

Programming Languages: C, C++

Operating System: Linux

Scripting Languages: Perl Scripting

Core Skills: Static Timing Analysis, Assertion Based Verification using System Verilog Assertions.

Others: MATLAB, Arduino IDE

- Multilinguistic - English, Telugu & Hindi

PERSONAL PROFILE

I am a highly motivated full stack developer with a year of experience. I have strong communication skills. Works well under pressure and consistently meets the deadlines while delivering the work

WORK EXPERIENCE

2021 - Present

Tecnics Integration Technologies Private Limited

Associate Software Engineer

- Created Pages and Reusable components in react using react reducer. Played an active role in abstracting the project
- Created REST Api's using spring boot.
- Helped the team in migrating the project from django to spring boot.
- Also I have some hands on work on database.

PERSONAL PROJECTS

Design and Implementation of Error Correction and Detection using BCH Codes and Syndrome Computation

Objectives: This antenna was designed for GSM and WiMAX Applications using a basic swastika shaped antenna.

Software: CST Studio Suite

This research is accepted and published at IEEE which is one my greatest achievements.