

Rajeshwari R



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CAREER OBJECTIVE

To achieve high career growth through continuous learning process and keep myself dynamic, visionary and competitive with changing scenario of technology.

PROFESSIONAL TRAINING

Advanced VLSI Design and Verification course

Maven Silicon VLSI Design and Training Center, Bangalore.

March 2022 till date.

VLSI DOMAIN SKILLS

- **Programming Languages:** HDL: Verilog.
- **EDA TOOL-**Mentor Graphics-Model Sim, Questa sim.
- **Operating System:** Linux.
- **Core Skills:** Digital Circuits design, Concepts of memory, RTL Coding using Synthesizable constructs of Verilog, Simulation, FSM based design, CMOS fundamentals, Code coverage synthesis.
- **Worked on 1 *3 Router coding project.**

WORK EXPERIENCE

1. Bharat Electronics Limited, Bengaluru

Role: Testing Engineer.

Duration: 4 August 2021 to present.

- Currently working as a Testing Engineer in Testing avionics division in Electronics and warfare department at Bharat Electronics Limited (BEL) .
- Involved in LCA Tejas aircraft project and testing of avionics related items, Project handled FSDU (Functional and sensor display unit), Data Link.
- Testing of LRU, SRU, FRU and carrying out ESS activities with respect to the profiles in Acceptance Test Procedure(ATP).
- Worked in **VVpat machines** and Participated in the election conducted in Uttar Pradesh in Feb 2022 representing BEL.
- Worked in **CV 200 Ventilator System Compressor unit** part, assembling, inspection and testing of Compressor part during the covid crisis.

Duration: February 2020 to March 2021.

- Worked as Trainee Engineer in Quality Assurance division in Naval Systems department at Bharat Electronics Limited (BEL) .
- Involved in Inspection of materials as per the standard AS9100 Rev D.

- Responsible for raising SAP OL, Co-ordination with CQAE persons for clearance of OL's with in Delivery Date (DD) and on-time dispatch to destination through Finished Goods stores to shipping as per the Quality Objective set in the SBU.
- Managing Material Inspection activities completely with on-time delivery schedule by systematic approach, transparency by documentation.
- Planning and Working as per ROP (Roll On Plan) set in the SBU – Monthly & Quarterly and Maintaining the status, attending meeting for Material Review Committee (MRC).

ACADEMIC CREDENTIALS

- **Sapthagiri college of Engineering Bangalore** - Bachelor of Engineering in Electronics and communication.**2016-2019**
University -VTU, Belagavi.
CGPA -7.31
- **M N Technical Institute Bangalore** – Diploma in Electronics and communication.**2013-2016**
University: Department of Technical Education.
Percentage-83%
- **B N R International public School, Mallasandra, Bangalore** -S.S.L.C **2013**
Board: Board of Secondary Education, Karnataka.
Percentage-79.36%

ACADEMIC PROJECT

- Completion of final year project in diploma on Assistive clothing pattern recognition for visually impaired people and blind people.
- Completion of final year project in BE on Evidence collection of automobiles black box using smart phone.

PERSONALITY TRAITS

- Focus on quality of work and strong work ethic.
- Curious to learn new things and willing to improve with experience.
- Optimum utilization of the time with best use of knowledge and technology.
- Self-confidence, Ability to work under pressure, Self motivator, Flexibility, Adaptability

DECLARATION

I hereby declare that all the information is true and best of my knowledge and belief.

Place:
Date:

Rajeshwari R