POORNESH L

✉ Poornesh.l2354@gmail.com

📱 9110205474

in linkedin.com/in/poornesh-l-aa102b1a2

📍 Bangalore, India

PROFESSIONAL SKILLS

- Verilog HDL
- System Verilog
- Assertion in System Verilog
- UVM
- C/C++
- Linux
- EDA Tool [Mentor Graphics - Questasim]
- Protocols [I2C | SPI | UART]
- Java/Selenium

COLLEGE SKILLS

- Digital Electronics
- Network Analysis
- Analog Electronics
- Power Electronics
- Arduino
- Digital Image Processing

PERSONAL SKILLS

- Leadership
- Creative spirit
- Reliable and professional
- Organized
- Time management
- Team player
- Fast learner
- Motivated
- Logical Thinking

LANGUAGES

- English
- Kannada
- Hindi
- Tamil

PROFILE

An enthusiastic Electronics and Communication Engineer seeking a career to utilize my knowledge, personal skills to gain a comprehensive understanding of a reputed organization to take responsibility and contribute significantly.

PROFESSIONAL TRAINING

- **ADVANCED VLSI DESIGN AND VERIFICATION**
  Maven Silicon VLSI Design and Verification Training centre, 06/2022 **-** Present

WORK EXPERIENCE

- **AUTOMATION TESTING ENGINEER**
  TCS
  02/2021 **-** 01/2023

  Thorough experience in Agile, Scrum methodologies. Designed, executed and maintained Selenium Web Driver (TestNg, JUnit) automated test cases for regression test cases Selected the Test Cases to be automated and performed functional testing of the front end using Selenium (IDE and Web Driver) & created Data Driven framework using JUnit.

EDUCATION

- **B.E in Electronics and Communication**
  Cambridge Institute of Technology (VTU) , Bangalore
  08/2016 **-** 09/2020
  7.78 CGPA

- **PUC in Science (PCME)**
  St Josephs Pre University College, Bangalore
  06/2015 **-** 03/2016
  78.6 %

- **SSLC**
  Sree Saraswathi Vidya Mandir, Bangalore
  06/2013 **-** 03/2014
  87.64 %

- Travelling
- Basket Ball
- Designing
- Development
- Drawing

PROJECTS

- ### MINI PROJECTS

  - #### Air pollution monitoring using Drone

    System uses air sensors to sense presence of harmful gases/compounds in the air and constantly transmit this data to microcontroller.

  - #### 1X3 Router design using verilog HDL

    Router forwards data packets between computer networks. It drives an incoming packet to an output channel based on the address field contained in the packet header. This is designed using verilog HDL .

- ### ACADEMIC PROJECT

  - #### Smart Locking system using Raspberry PI

    Door Lock Security is of prime importance because it is the first step towards ensuring the safety of valuable assets and people. The model contains the Password based unlocking system along with the facial detection, live streaming and finger print sensor.

ACHIEVEMENTS

- Secured 1st place in CIRCUIT DEBUGGING at Shannon techno fest
- Awarded as BEST DESIGN for the project "Air pollution monitoring using Drone" at INTUIT'19 Project Exhibition

DECLARATION

I here by declare that the information mentioned above are in accordance with the truth and fact as per my knowledge and i hold the responsibility for the correctness of the above mention particulars.