Assignment 1

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***Abstract*—This document shows how to draw logic gates.**

Download all latex-tikz codes from

https://github.com/Bharat437/EE5803−FPGA−LAB/tree/main/Assignment−1

1 Problem

Draw the Logic Circuit for the following Boolean Expression:
$\left(\mathbf{X}^{'}+\mathbf{Y}\right).\mathbf{Z}+\mathbf{W}^{'}$

2 Explanation

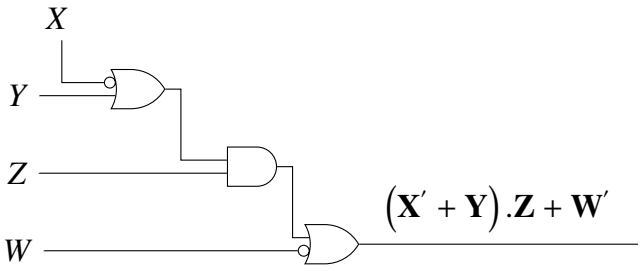

Fig. 1: Logic circuit for $\left(\mathbf{X}^{'}+\mathbf{Y}\right).\mathbf{Z}+\mathbf{W}^{'}$