



RAMAIAH
Institute of Technology



This is to certify that

Bharath . P

has participated in the 5-day workshop on **"VLSI Design Gatesto ICs"** organized by Department of Electronics and Communication Engineering Ramaiah Institute of Technology, Bangalore from 5th to 9th September, 2022

Co-ordinator
Department of ECE, RIT

Professor and Head
Department of ECE, RIT