

Ethernet and Switching



Ross Bagurdes

NETWORK ENGINEER

@bagurdes



Module Goals



CSMA/CD

Collision Domains

Duplex and Speed

The Ethernet Frame

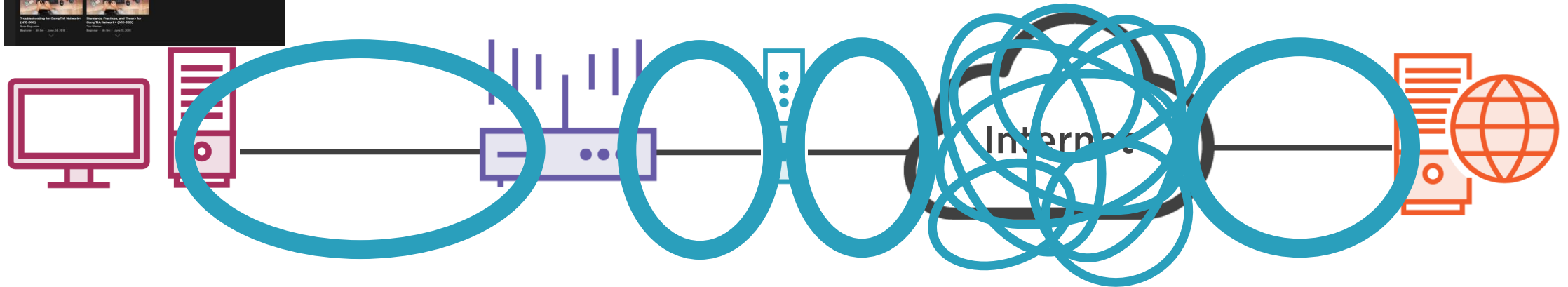
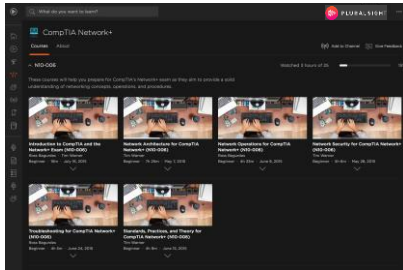
Switch Operation

OSI Model

7	Application Layer
6	Presentation Layer
5	Session Layer
4	Transport Layer
3	Network Layer
2	Data Link Layer
1	Physical Layer

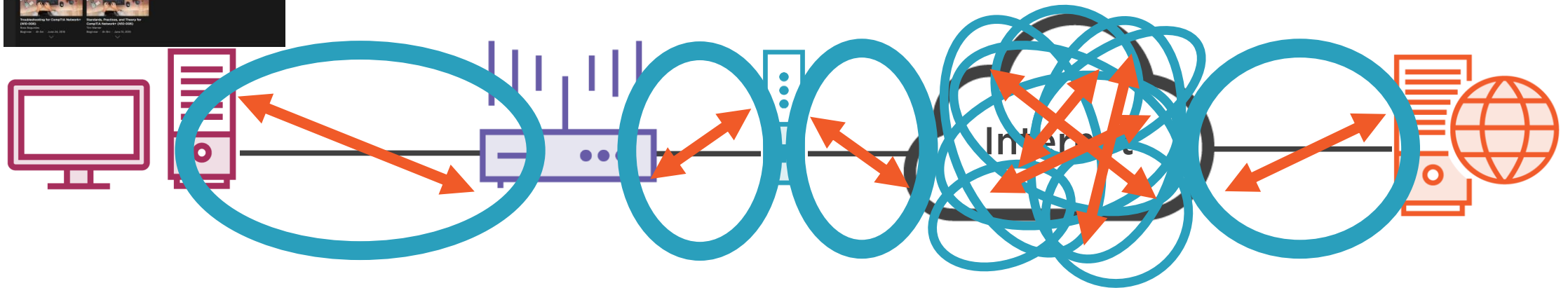
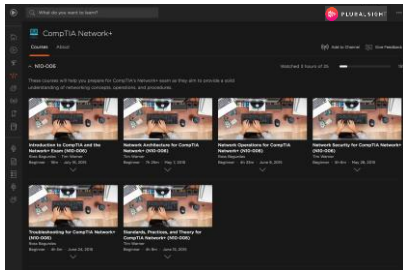


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Data Link Layer

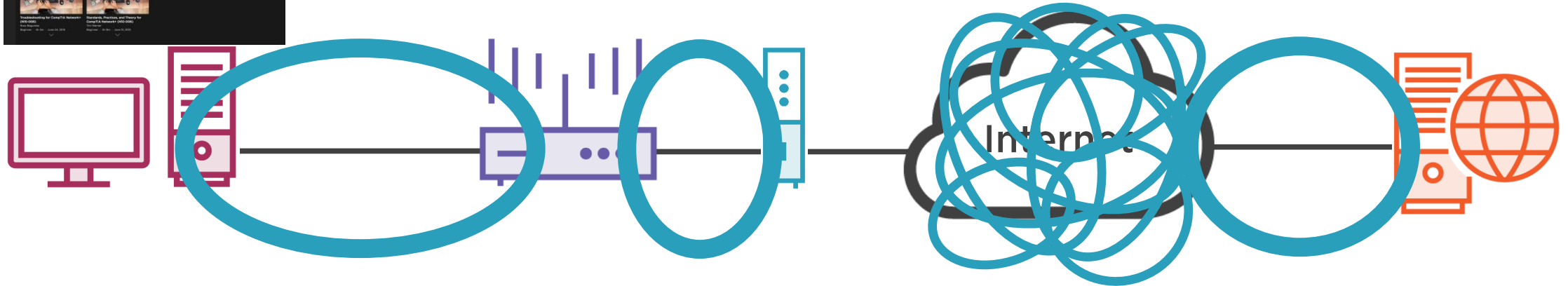
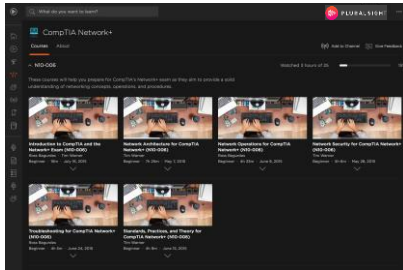
<https://www.pluralsight.com/>



Data Link Layer



<https://www.pluralsight.com/>



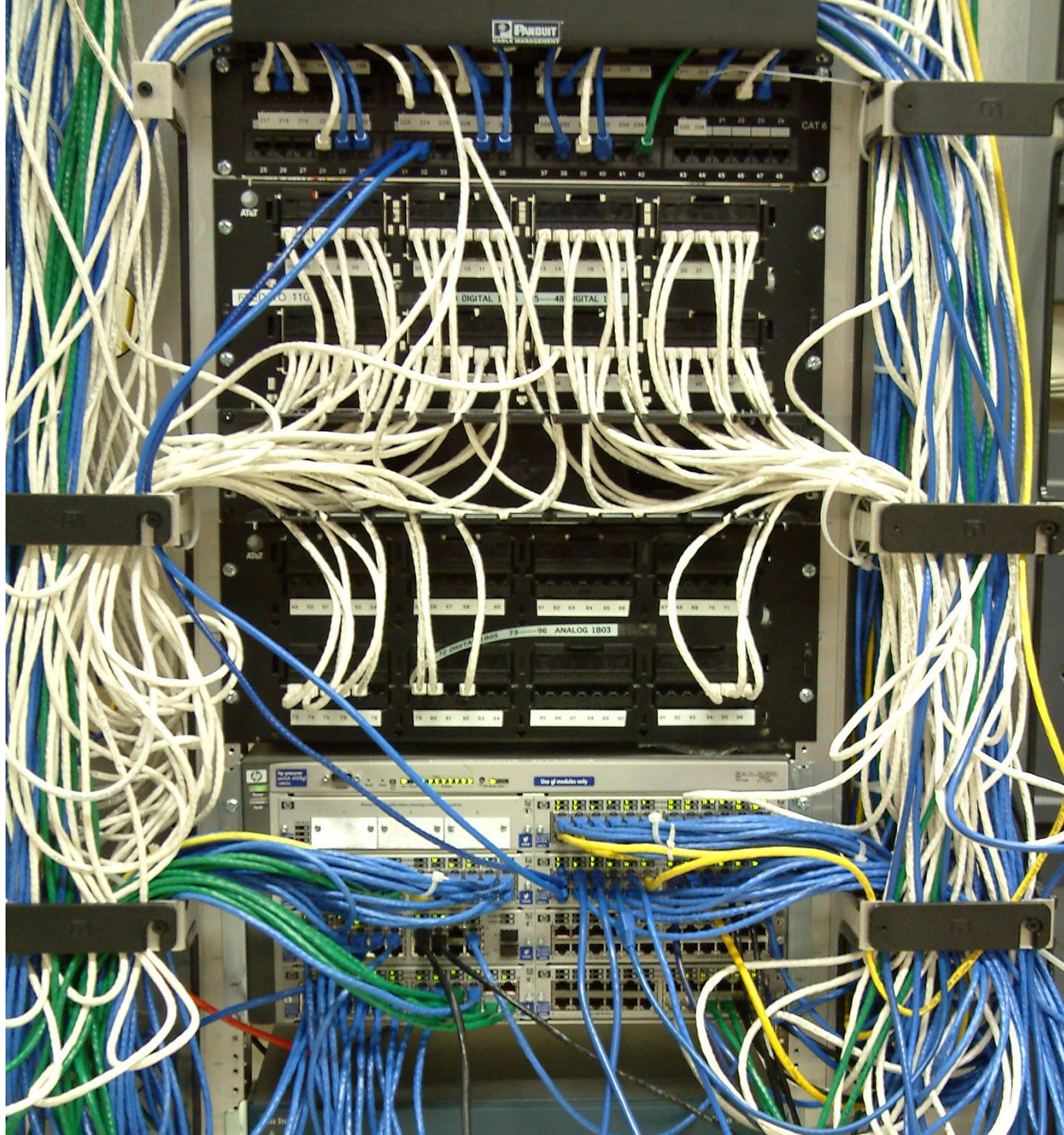
Ethernet











CSMA/CD



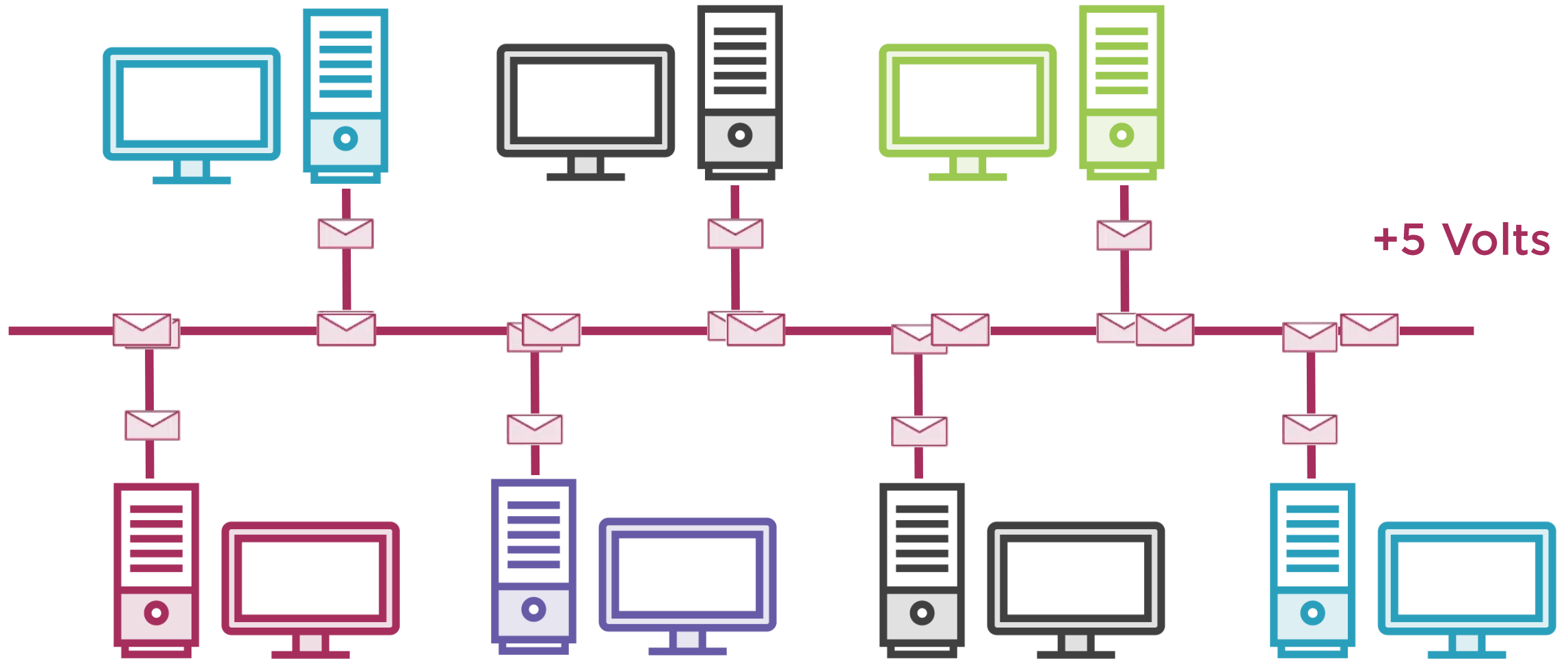
CSMA/CD

Carrier Sense Multiple Access with Collision Detection



CSMA/CD

Carrier Sense Multiple Access with Collision Detection



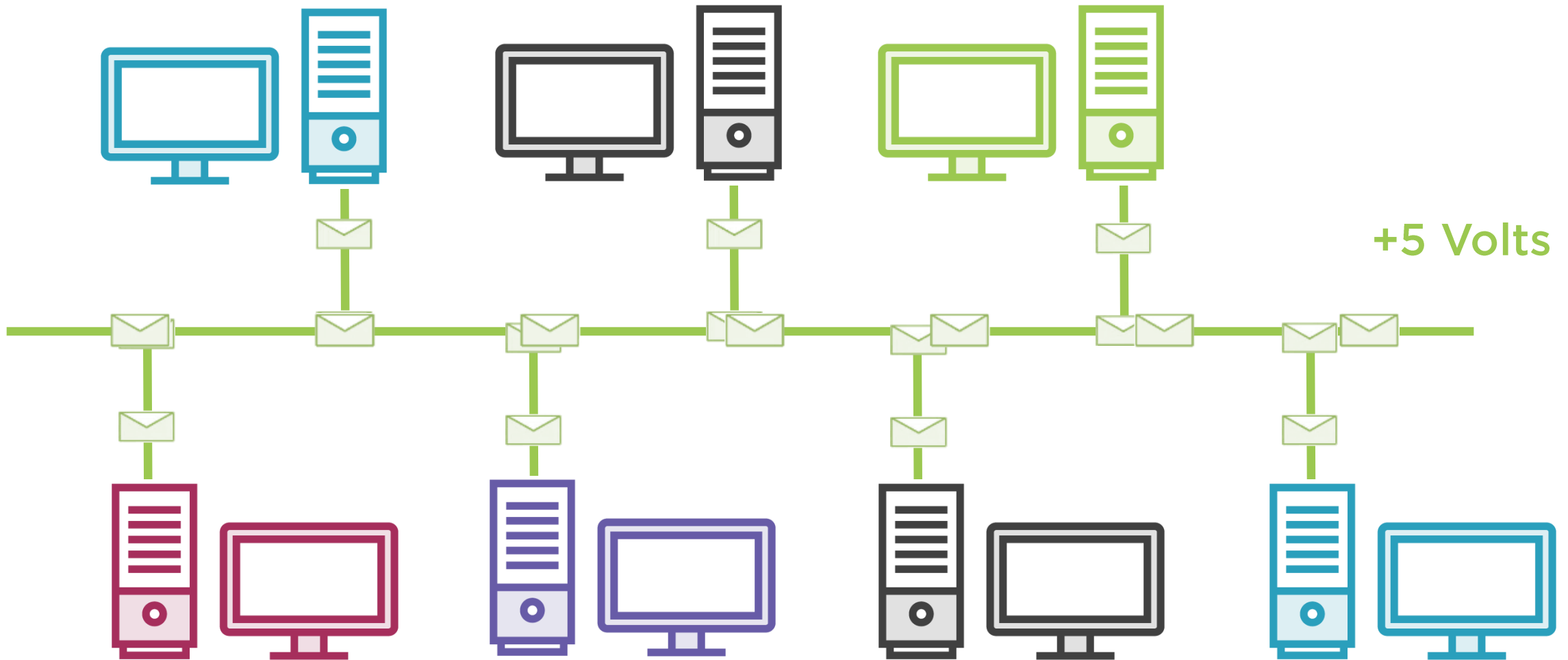
CSMA/CD

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CSMA/CD

Carrier Sense Multiple Access with Collision Detection



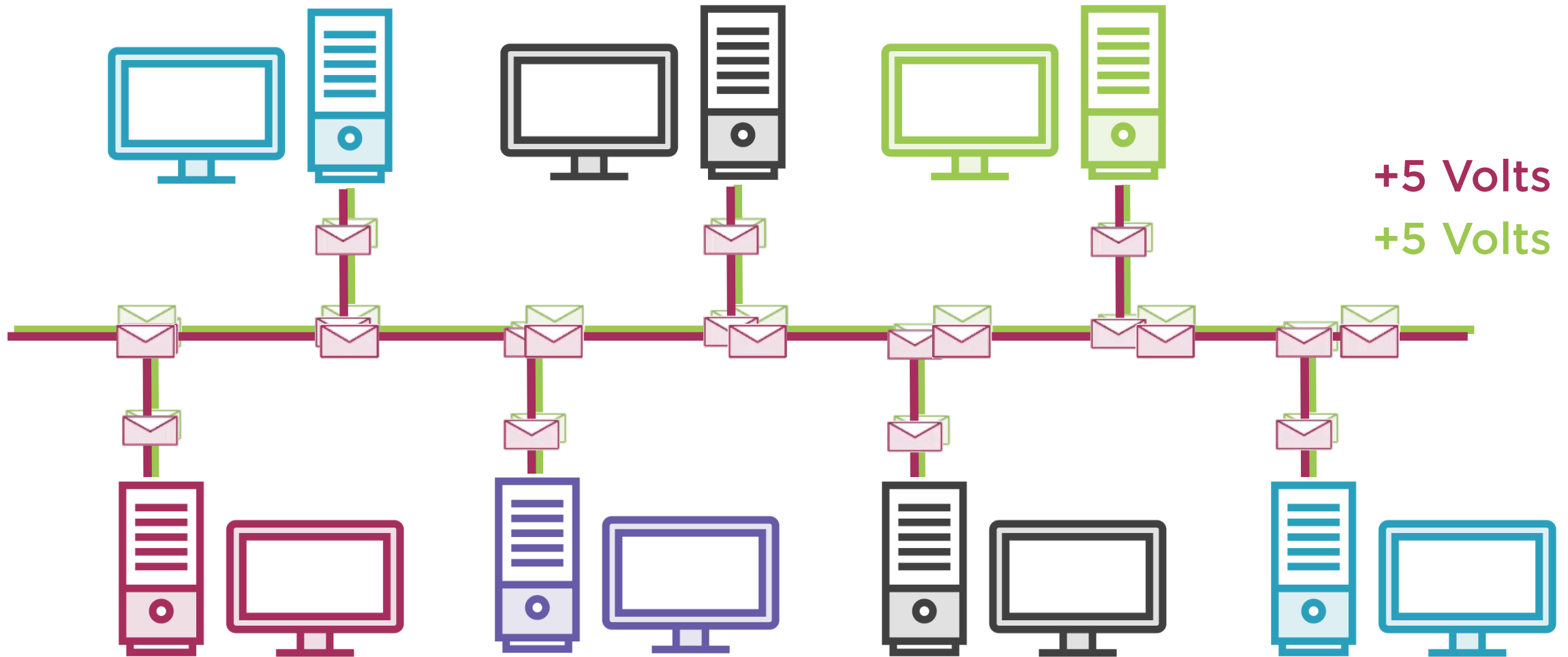
CSMA/CD

Carrier Sense Multiple Access with Collision Detection



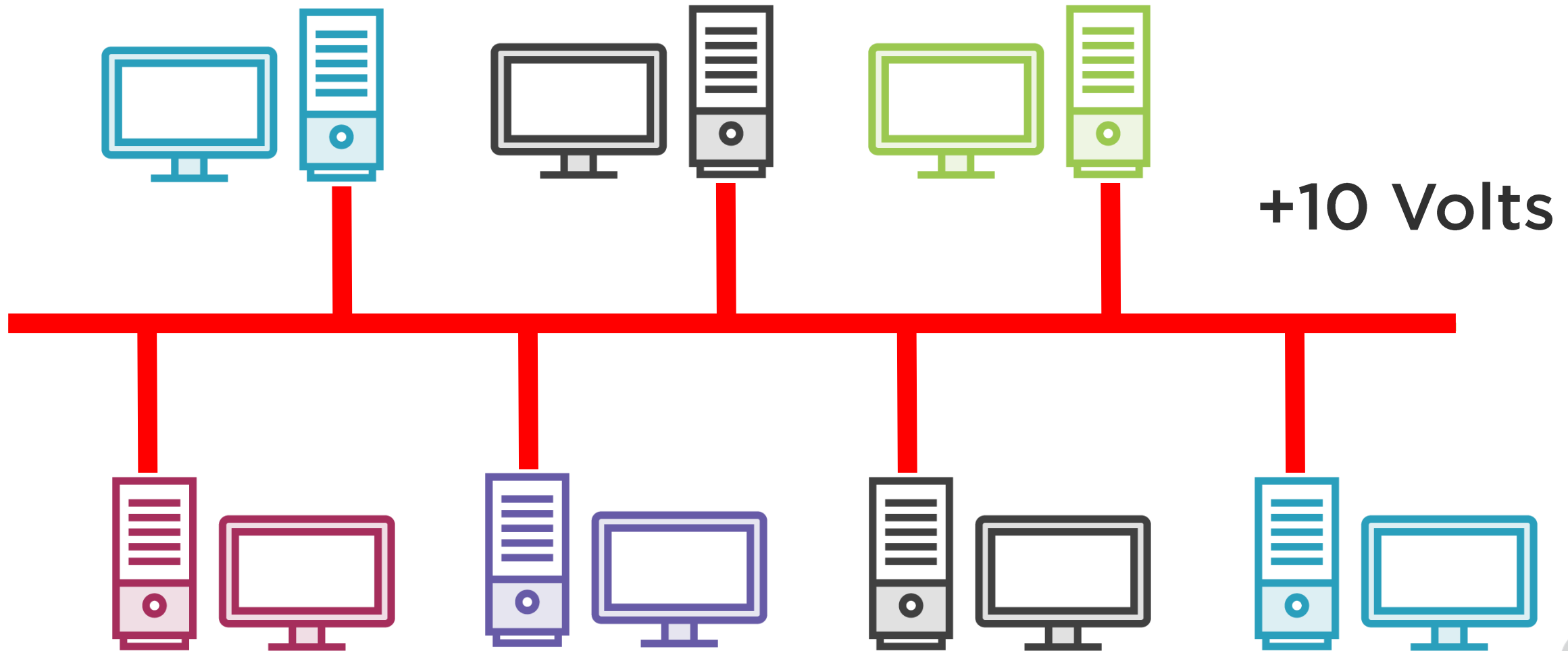
CSMA/CD

Carrier Sense Multiple Access with Collision Detection



CSMA/CD

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CSMA/CD

Carrier Sense Multiple Access with Collision Detection



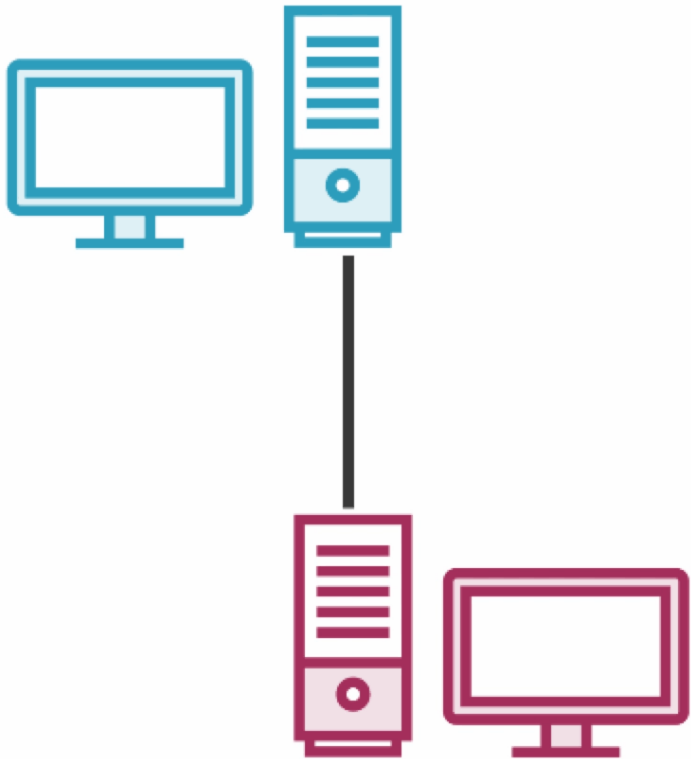
Collision Domain

A group of networked devices that will simultaneously detect a voltage spike.



Duplex and Speed

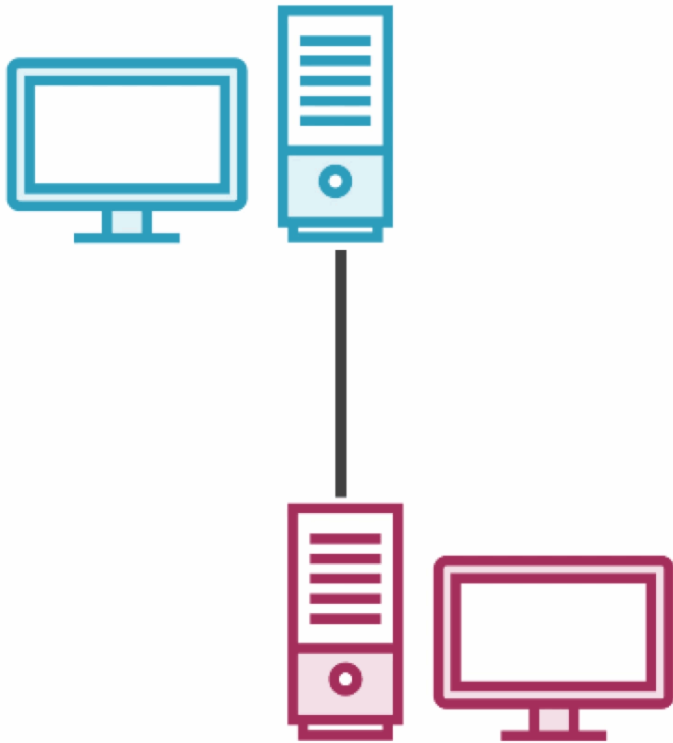




Half duplex

One device communicates at a time

“Walkie talkie”



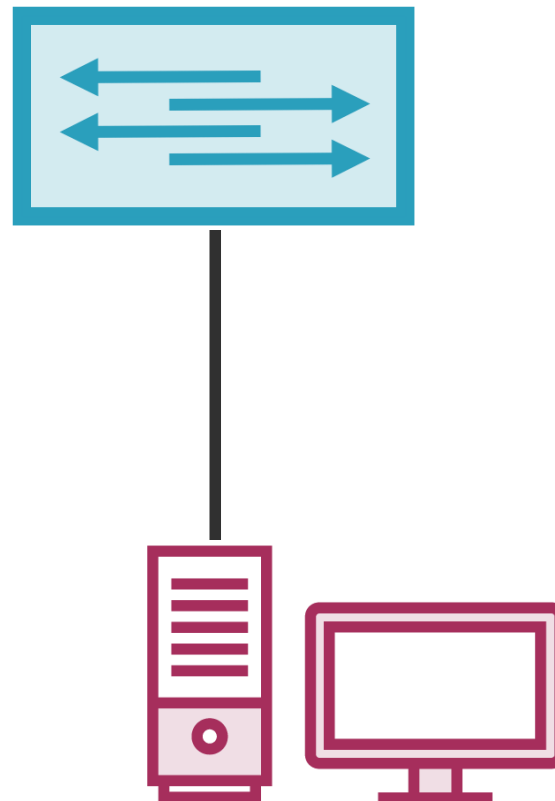
Full duplex

Two devices communicate at same time

“Telephone”

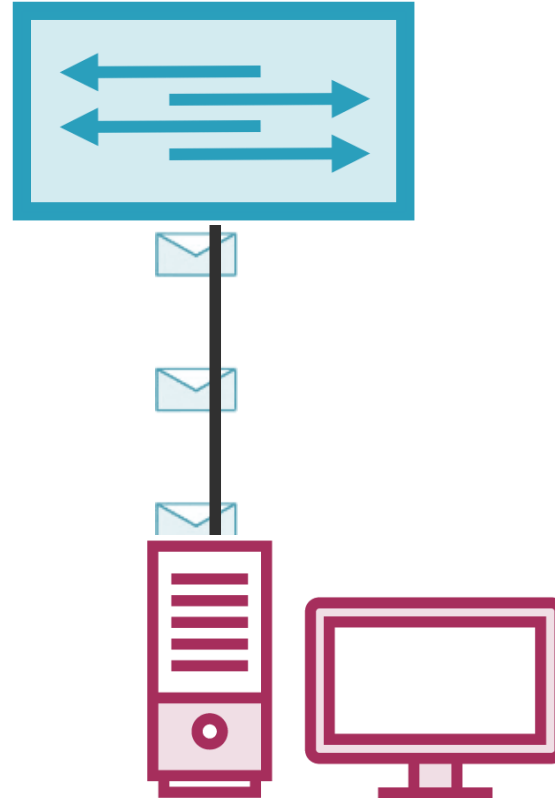
Modern Collision Domain

Half Duplex



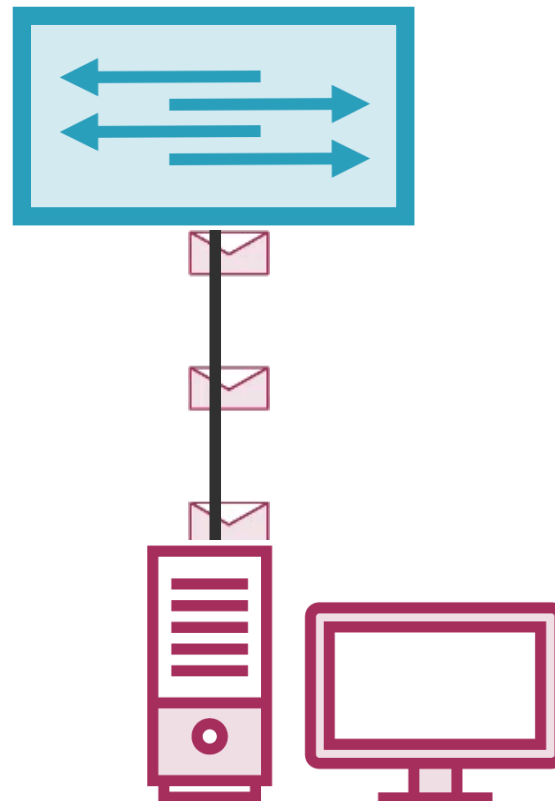
Modern Collision Domain

Half Duplex



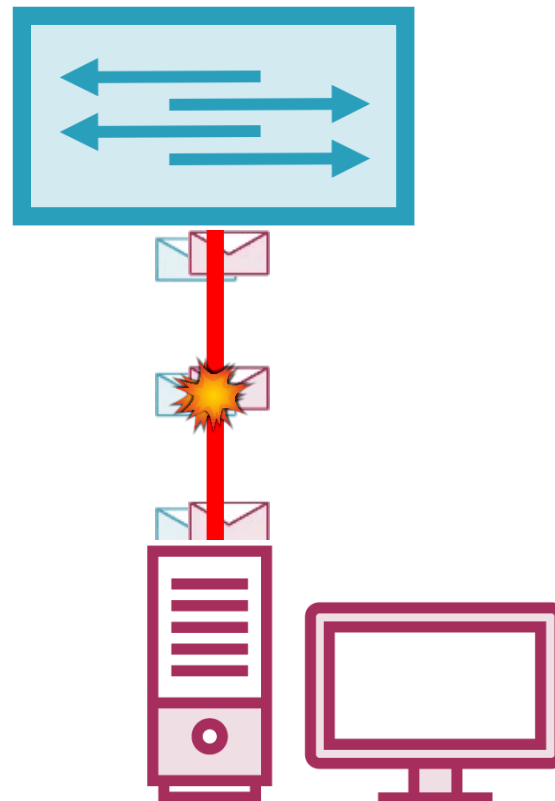
Modern Collision Domain

Half Duplex



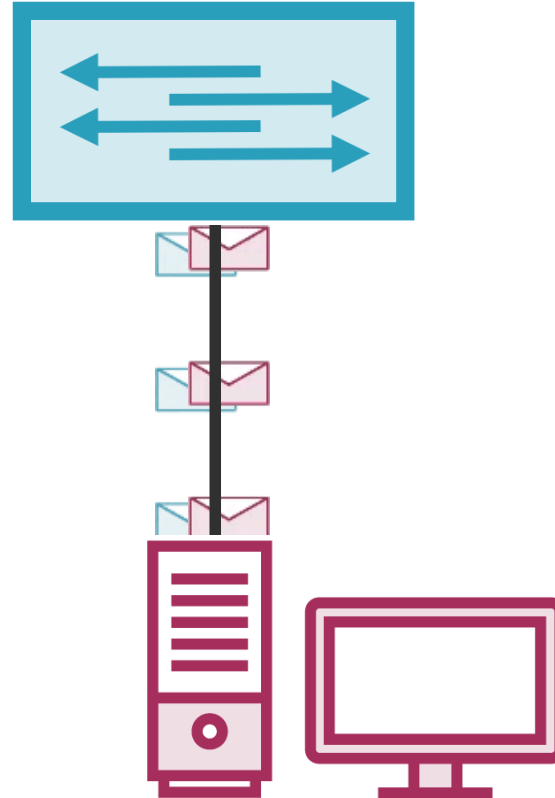
Modern Collision Domain

Half Duplex



Modern Collision Domain

Full Duplex



Ethernet Speed

Name	Speed
Ethernet	10Mbps
FastEthernet	100Mbps
GigabitEthernet	1Gbps
10GigabitEthernet	10Gbps
40GigabitEthernet	40Gbps



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Requires Full Duplex



Ethernet II Frame







Ethernet II Frame

Destination MAC Address	Source MAC Address	Type	Data (Packet)	FCS
48 bits	48 bits	16 bits	MAX 1500 Bytes	32 bits



Ethernet II Frame



Frame

A chunk of data, with a data link layer header



Ethernet II Frame



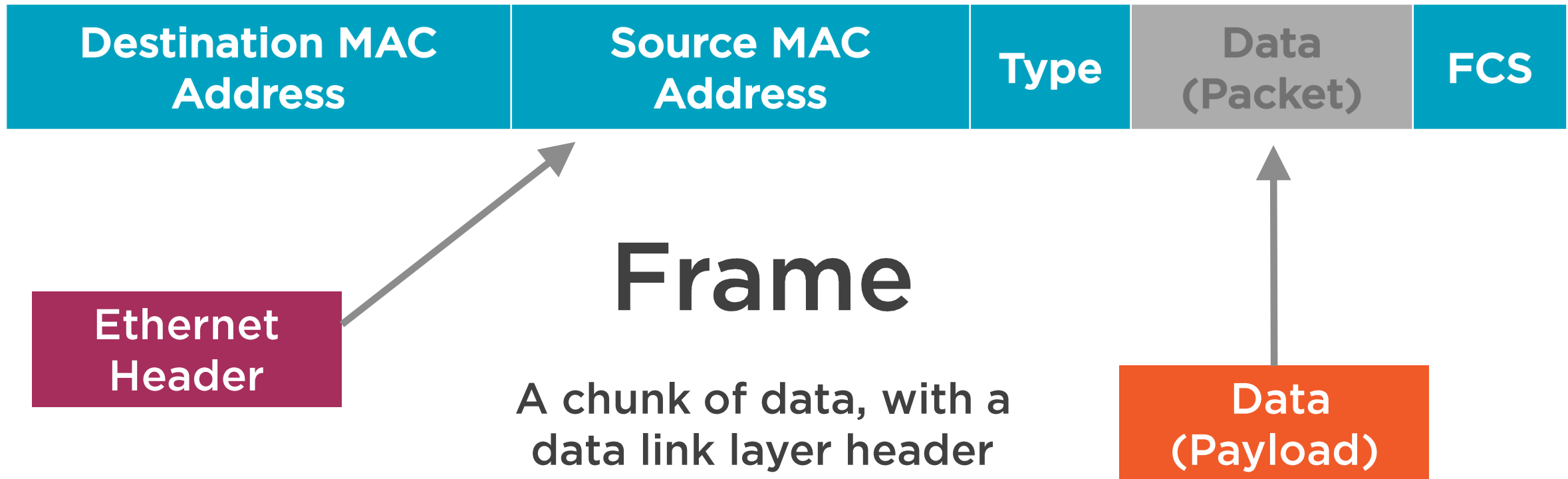
Frame

A chunk of data, with a data link layer header

Ethernet Header



Ethernet II Frame



Ethernet II Frame

Destination MAC Address		Source MAC Address	Type	Data (Packet)	FCS
Manufacturer ID	Serial Number	48 bits	16 bits	MAX 1500 Bytes	32 bits
00:10:D9 : D7:52:7A					
0000 0000 0001 0000 1101 1001 1101 0111 0101 0010 0111 1010					



MAC Address

1.3

Manufacturer ID	Serial Number
00:10:D9 : D7:52:7A	
0000 0000 0001 0000 1101 1001 1101 0111 0101 0010 0111 1010	



Ethernet II Frame

Destination MAC Address		Source MAC Address	Type	Data (Packet)	FCS
Manufacturer ID	Serial Number	48 bits	16 bits	MAX 1500 Bytes	32 bits
00:10:D9 : D7:52:7A					
0000 0000 0001 0000 1101 1001 1101 0111 0101 0010 0111 1010					



Type Field

Destination MAC Address	Source MAC Address	Type	Data (Packet)	FCS
48 bits	48 bits	16 bits	MAX 1500 Bytes	32 bits



Frame Check Sequence Cyclical Redundancy Check (CRC)

Destination MAC Address	Source MAC Address	Type	Data (Packet)	FCS
48 bits	48 bits	16 bits	MAX 1500 Bytes	32 bits



Ethernet II Frame

Destination MAC Address	Source MAC Address	Type	Data (Packet)	FCS
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Maximum
Transmission
Unit



Ethernet II Frame

Destination MAC Address	Source MAC Address	Type	Data (Packet)	FCS
48 bits	48 bits	16 bits	MAX 1500 Bytes	32 bits

Protocol Data Unit



Network Topologies





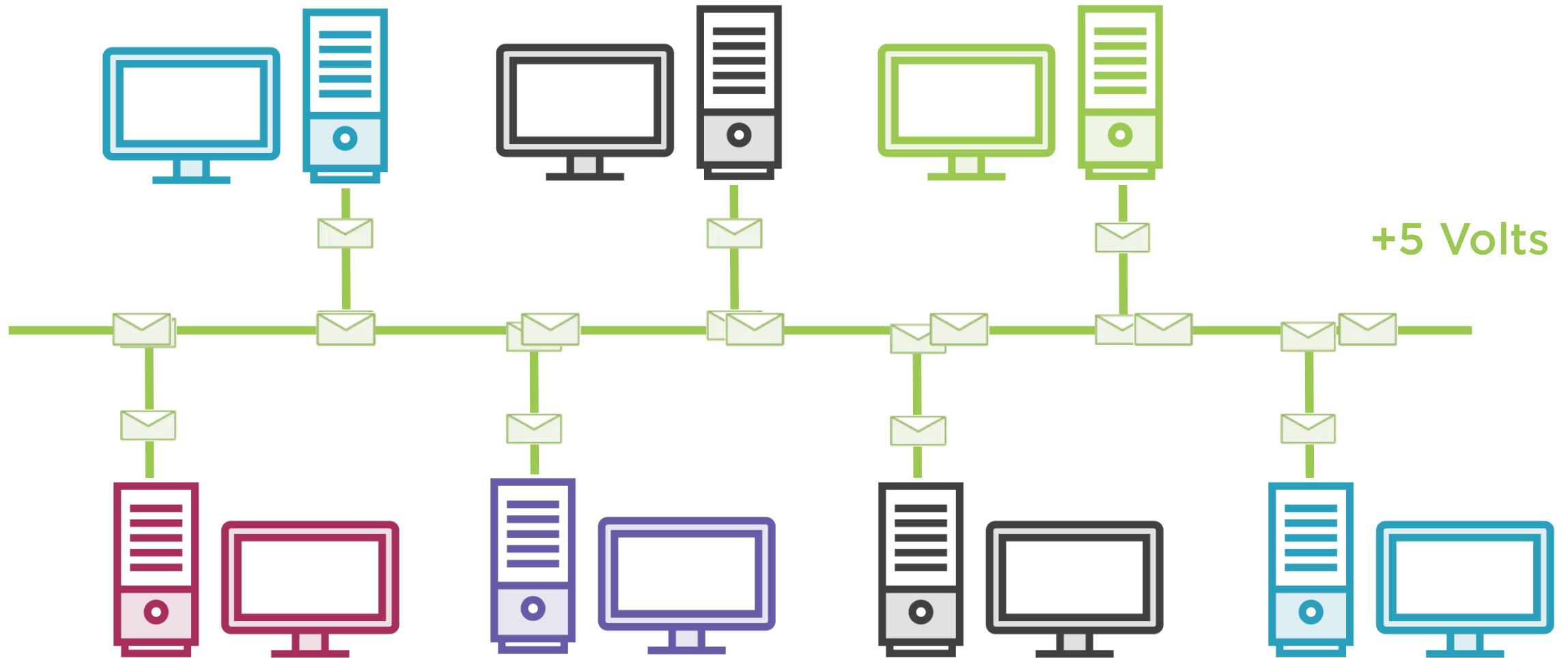
Network Topologies

- Bus
- Ring
- Star

Bus Topology



Bus Topology



Bus Topology



Bus Topology

10Base5 and 10Base2 hardware



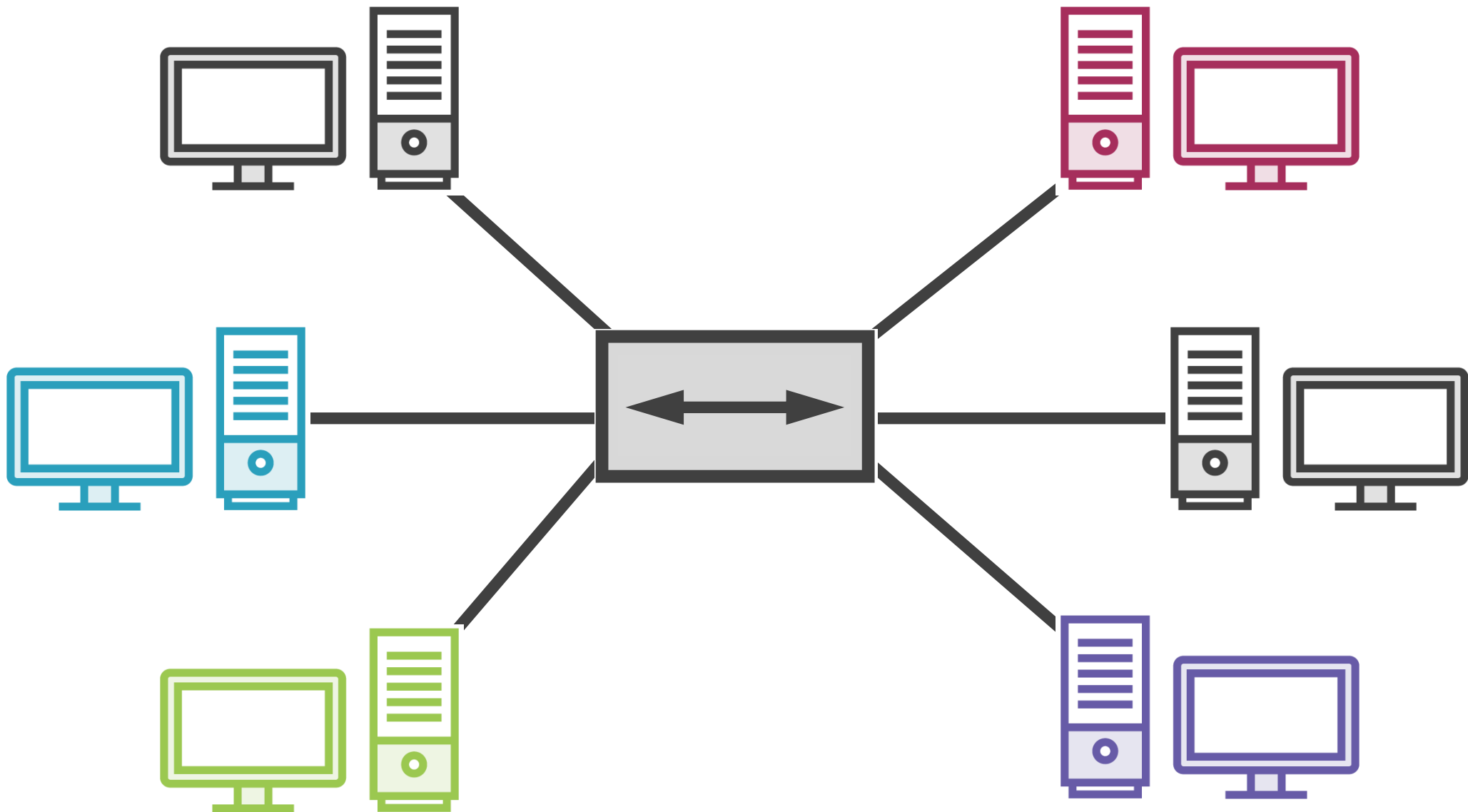
Ring



IBM Token Ring MAU



Star



Star

1.5



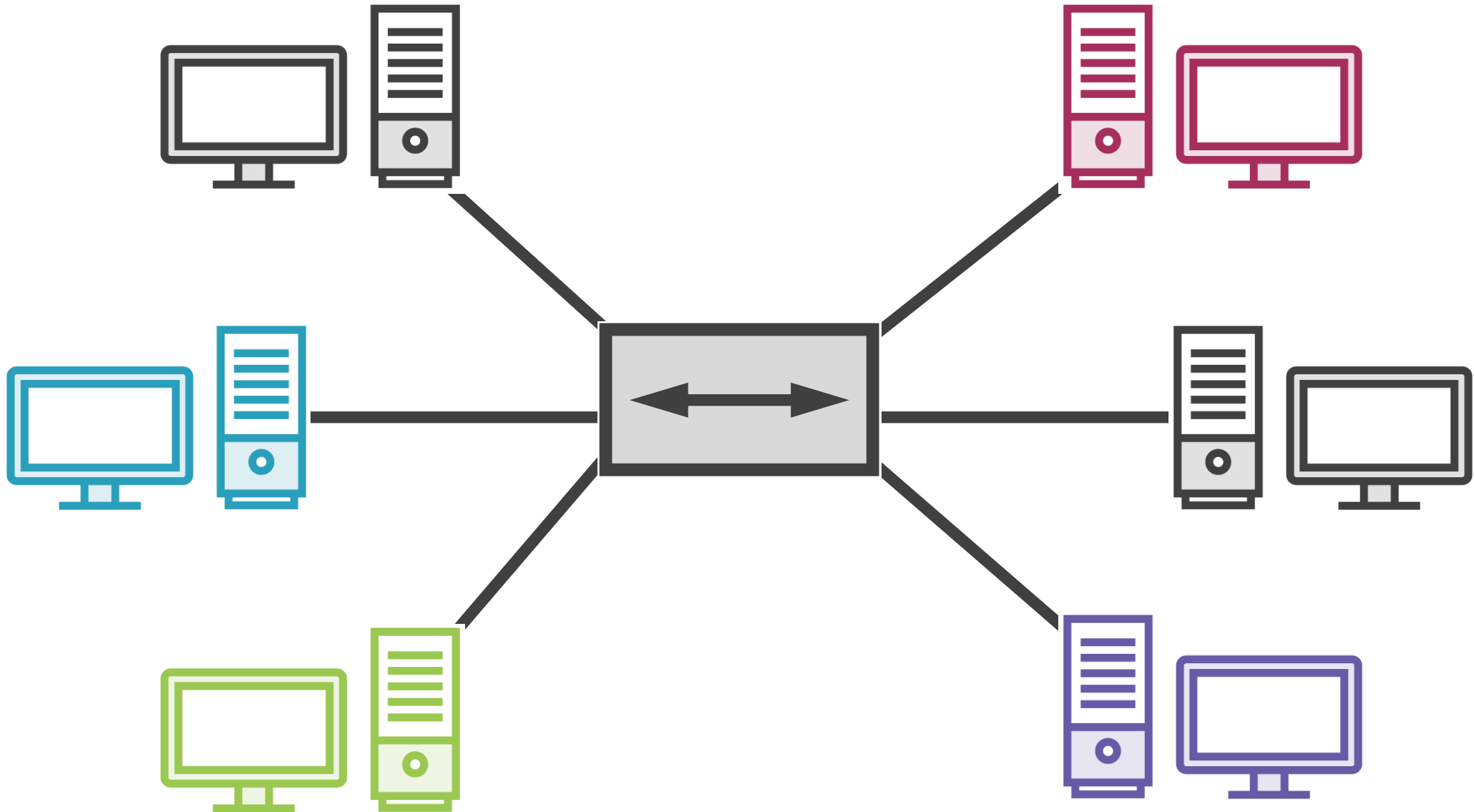
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Layer 2 Switch

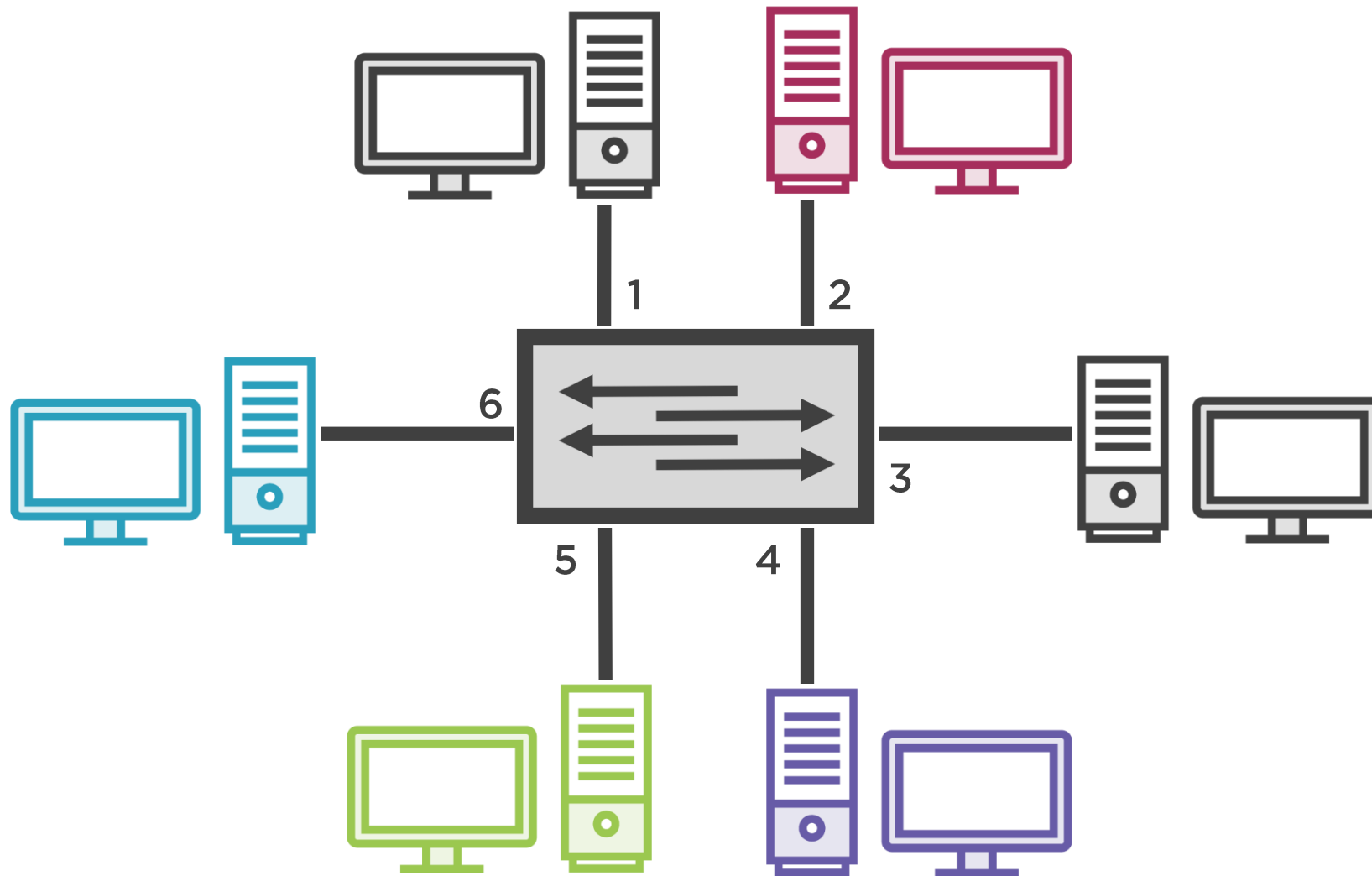


Star with Hub



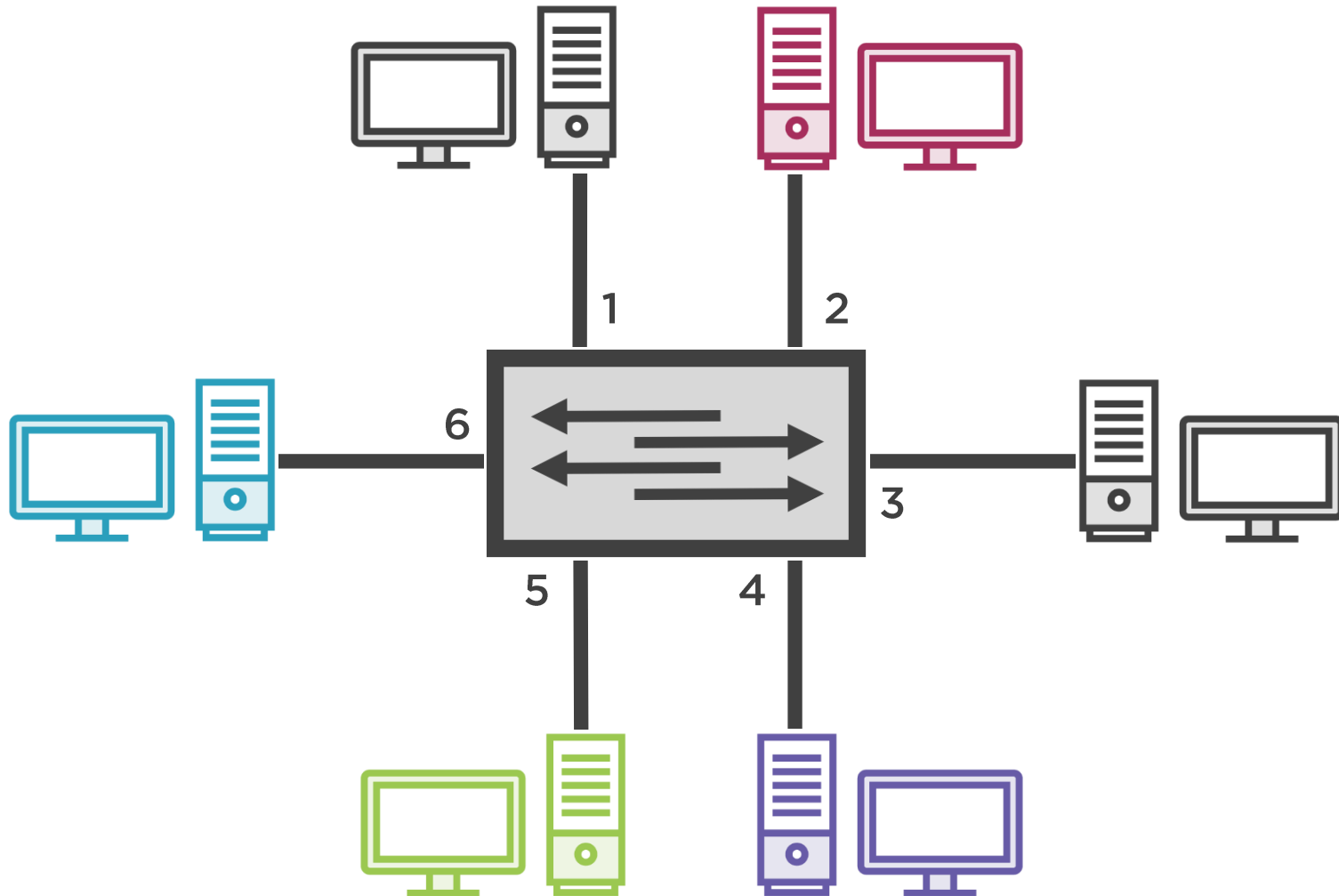
Ethernet Switch

1.3



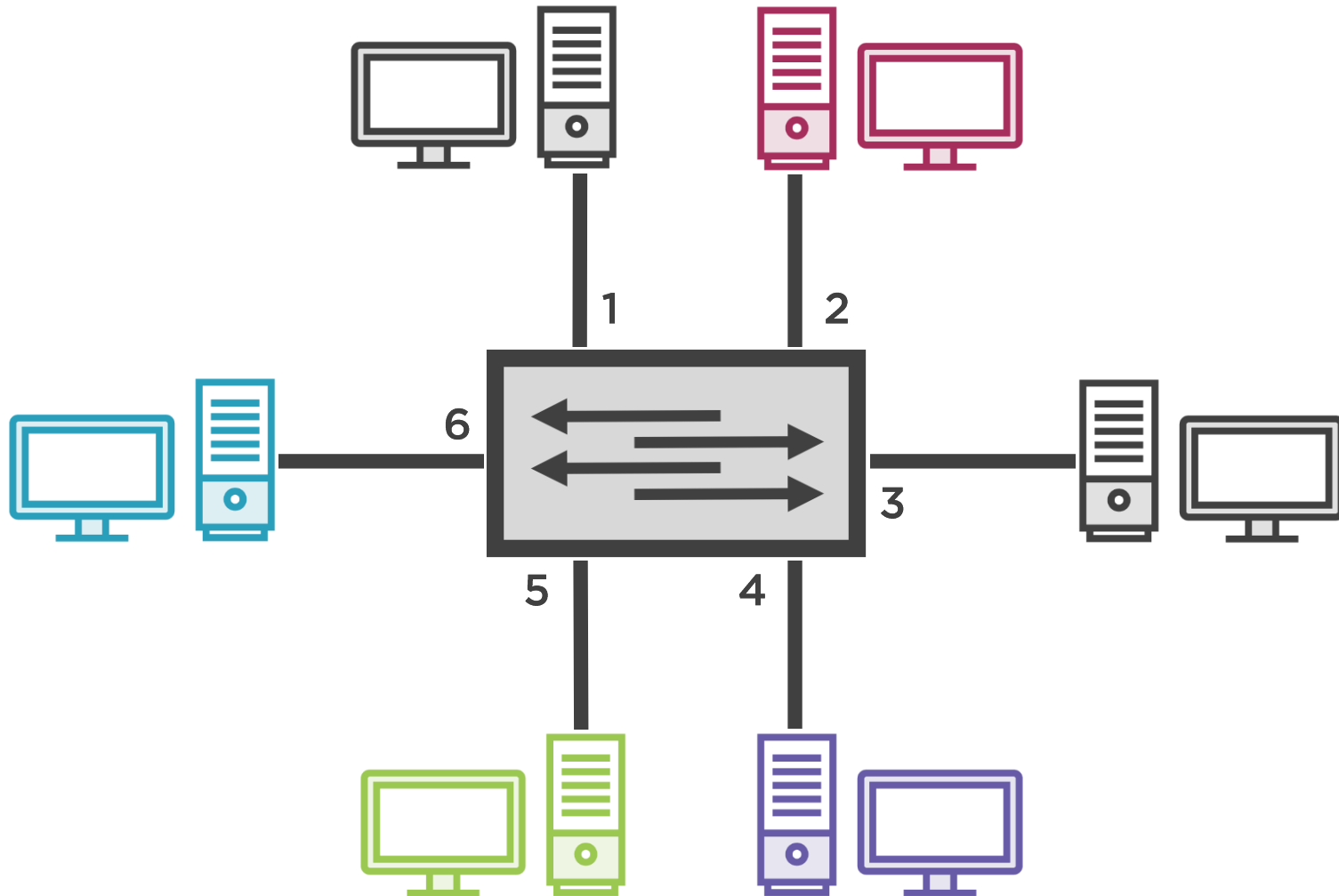
MAC Address Table

1.3



MAC Address Table

1.3

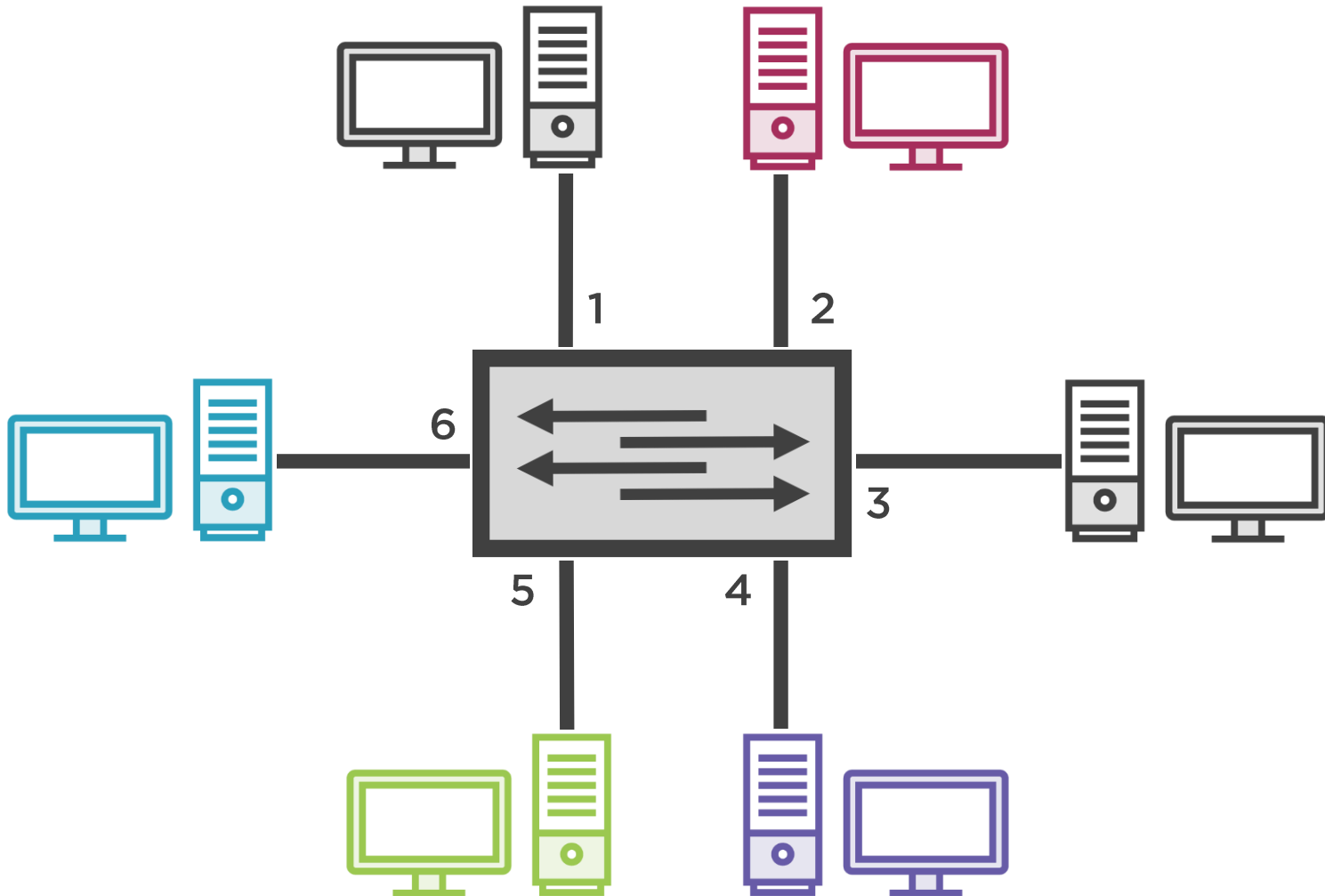


Port	MAC



MAC Address Table

1.3

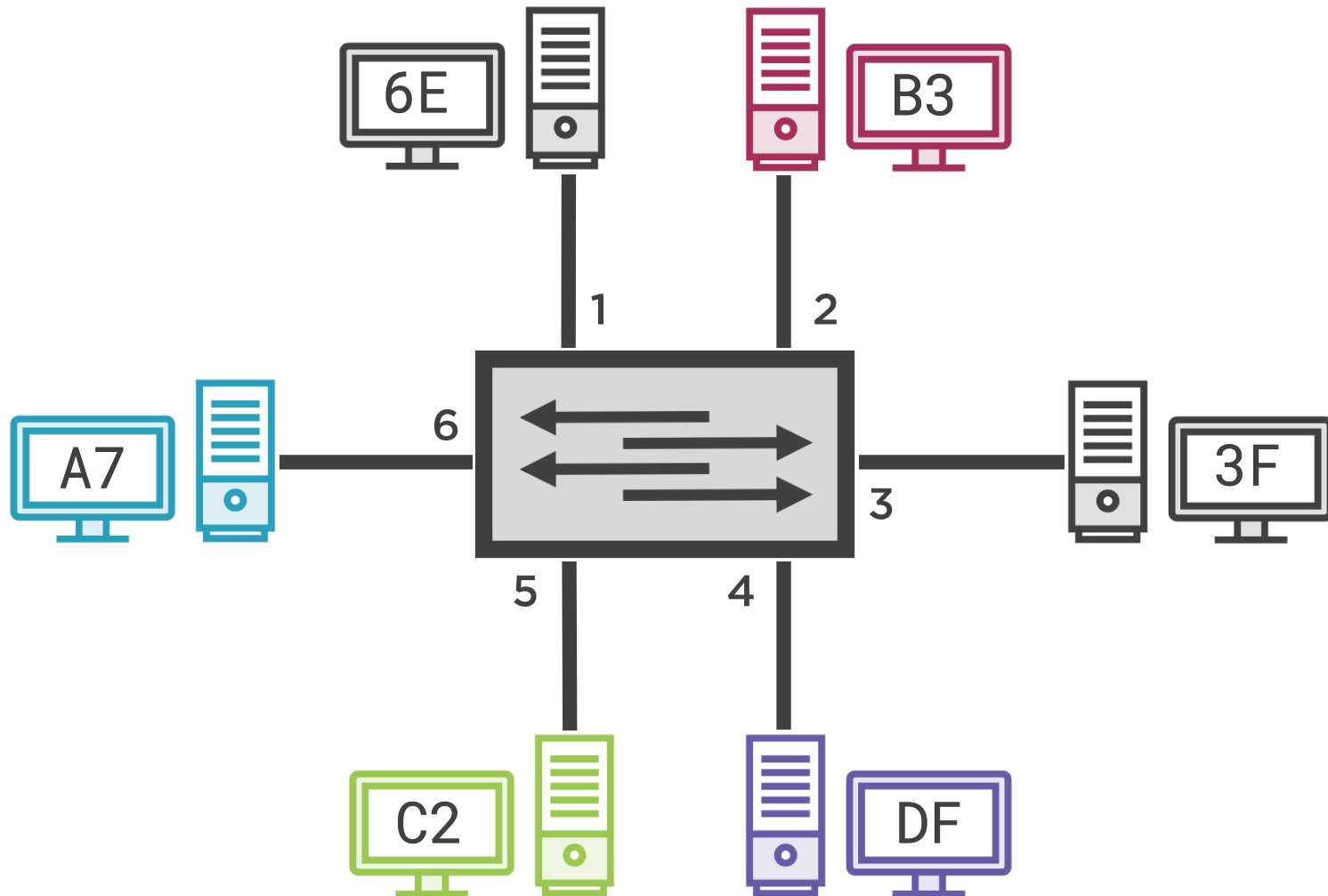


Port	MAC
1	
2	
3	
4	
5	
6	



MAC Address Table

1.3

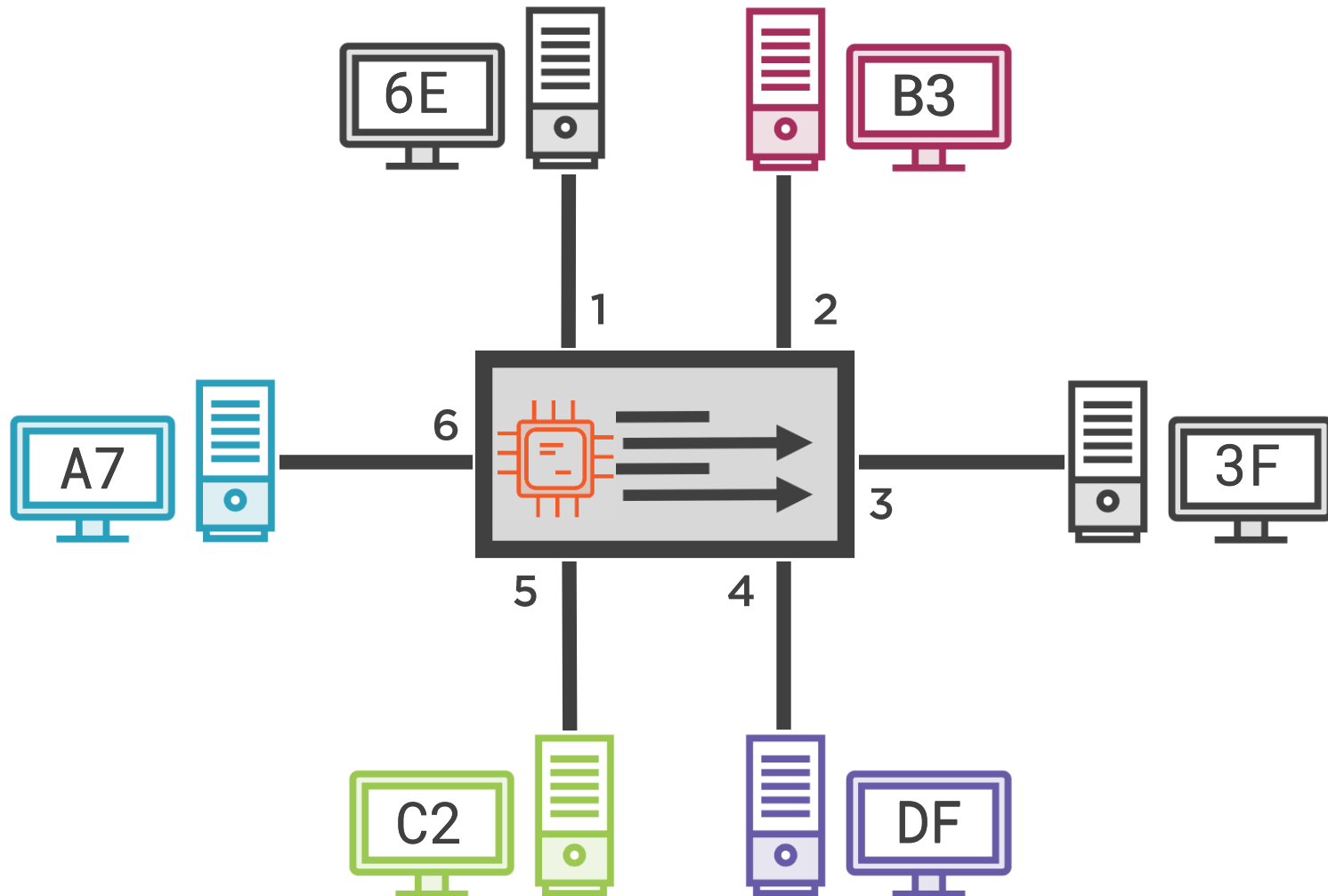


Port	MAC
1	
2	
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MAC Address Table

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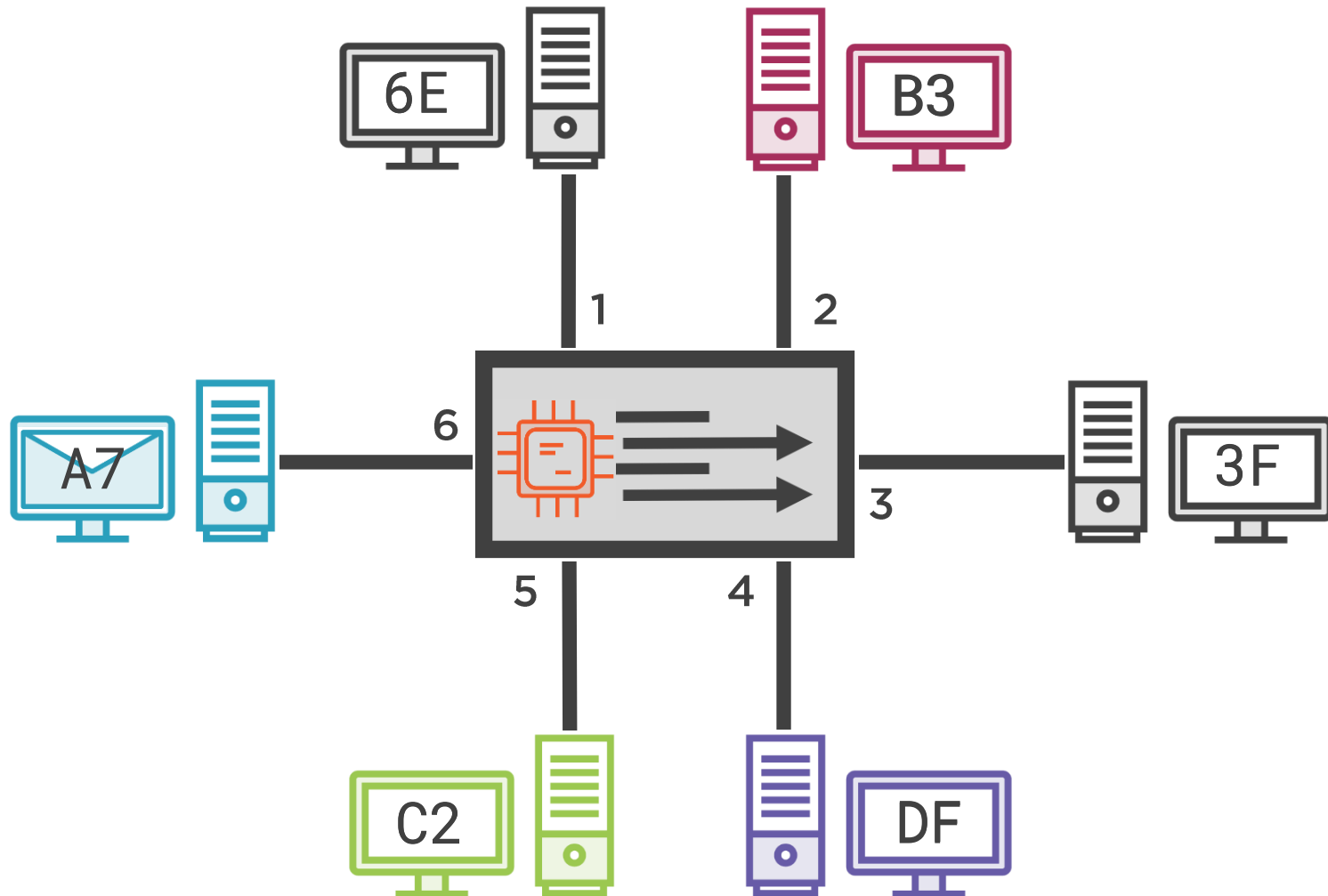


Port	MAC
1	
2	
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MAC Address Table

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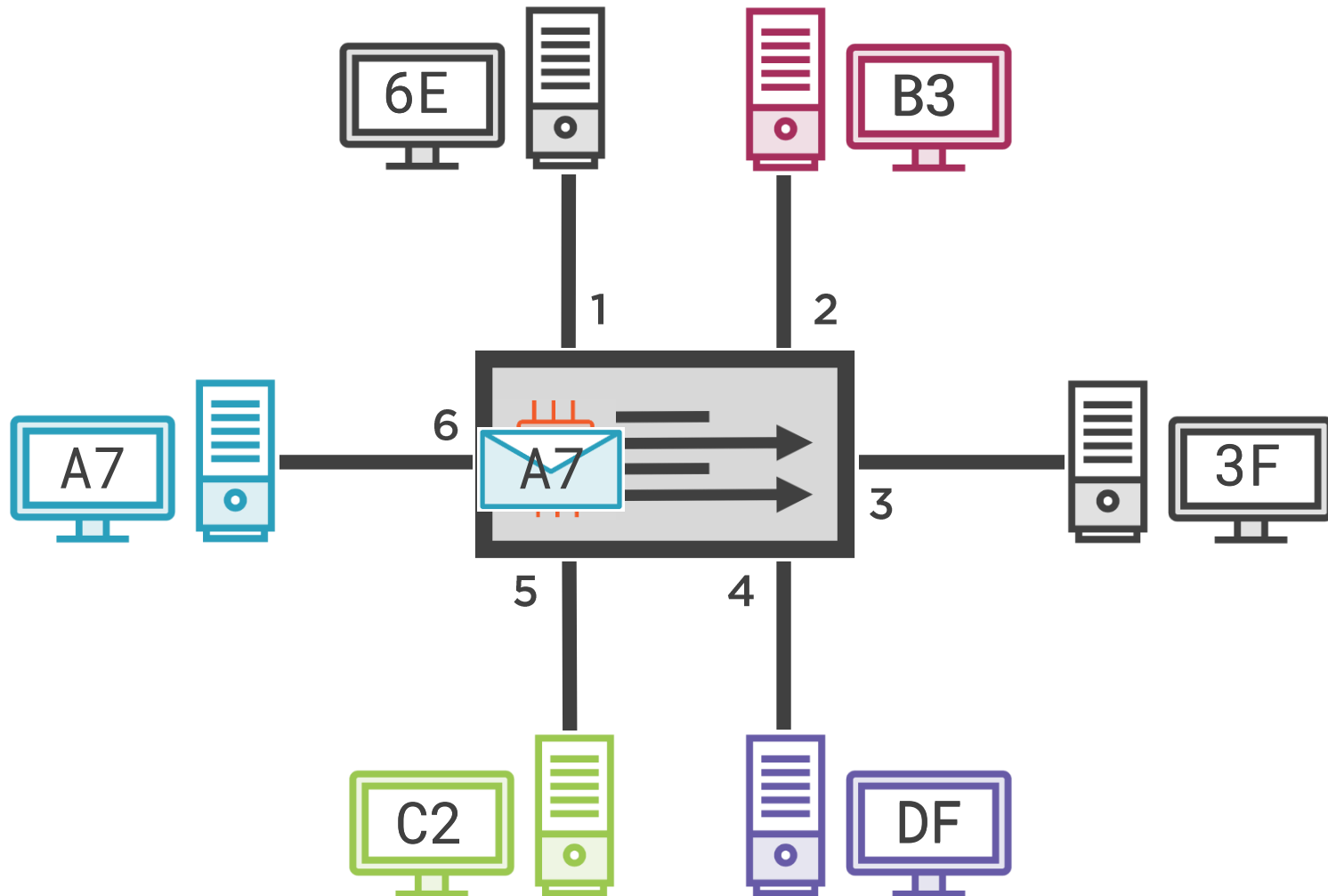


Port	MAC
1	
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MAC Address Table

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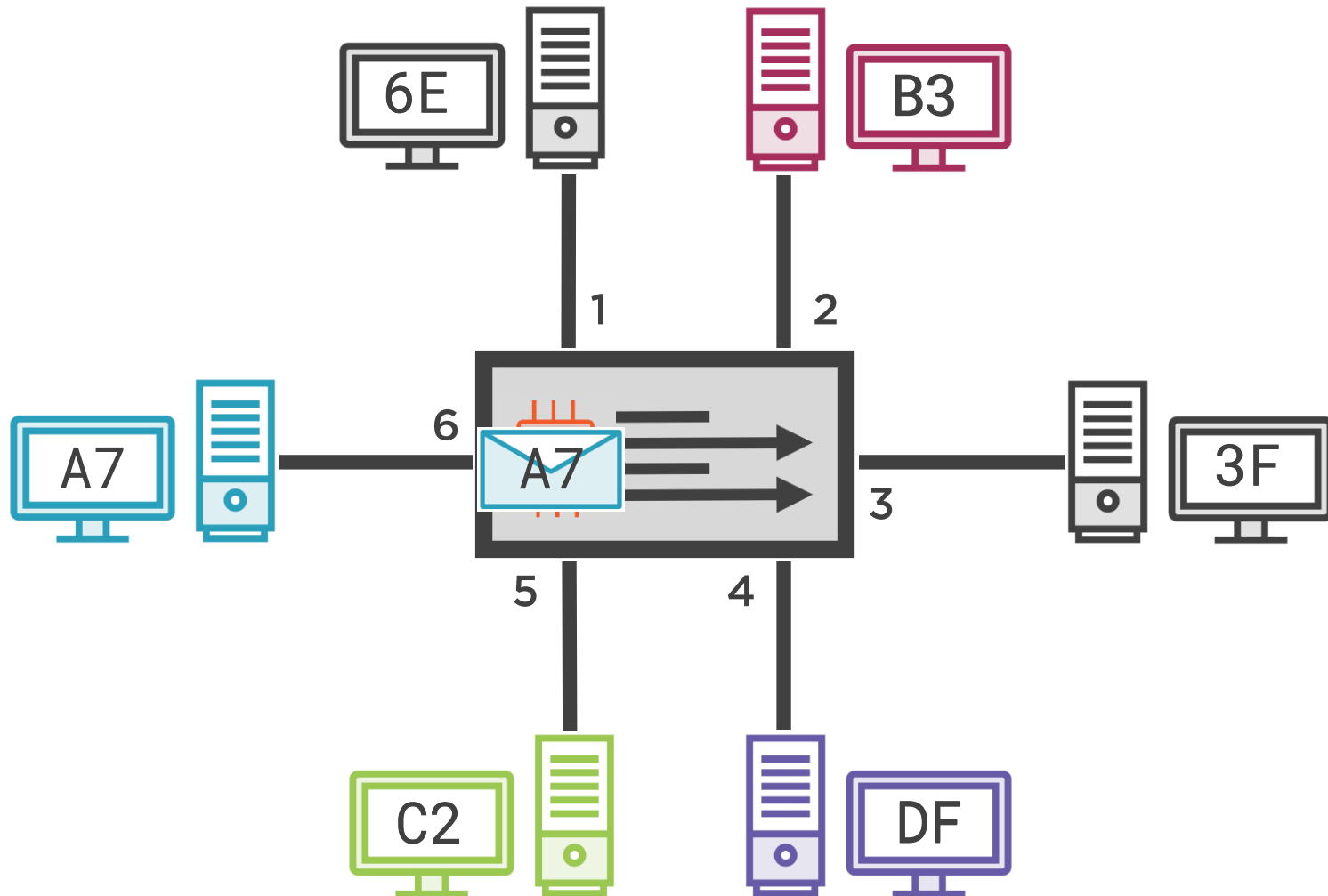


Port	MAC
1	
2	
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MAC Address Table

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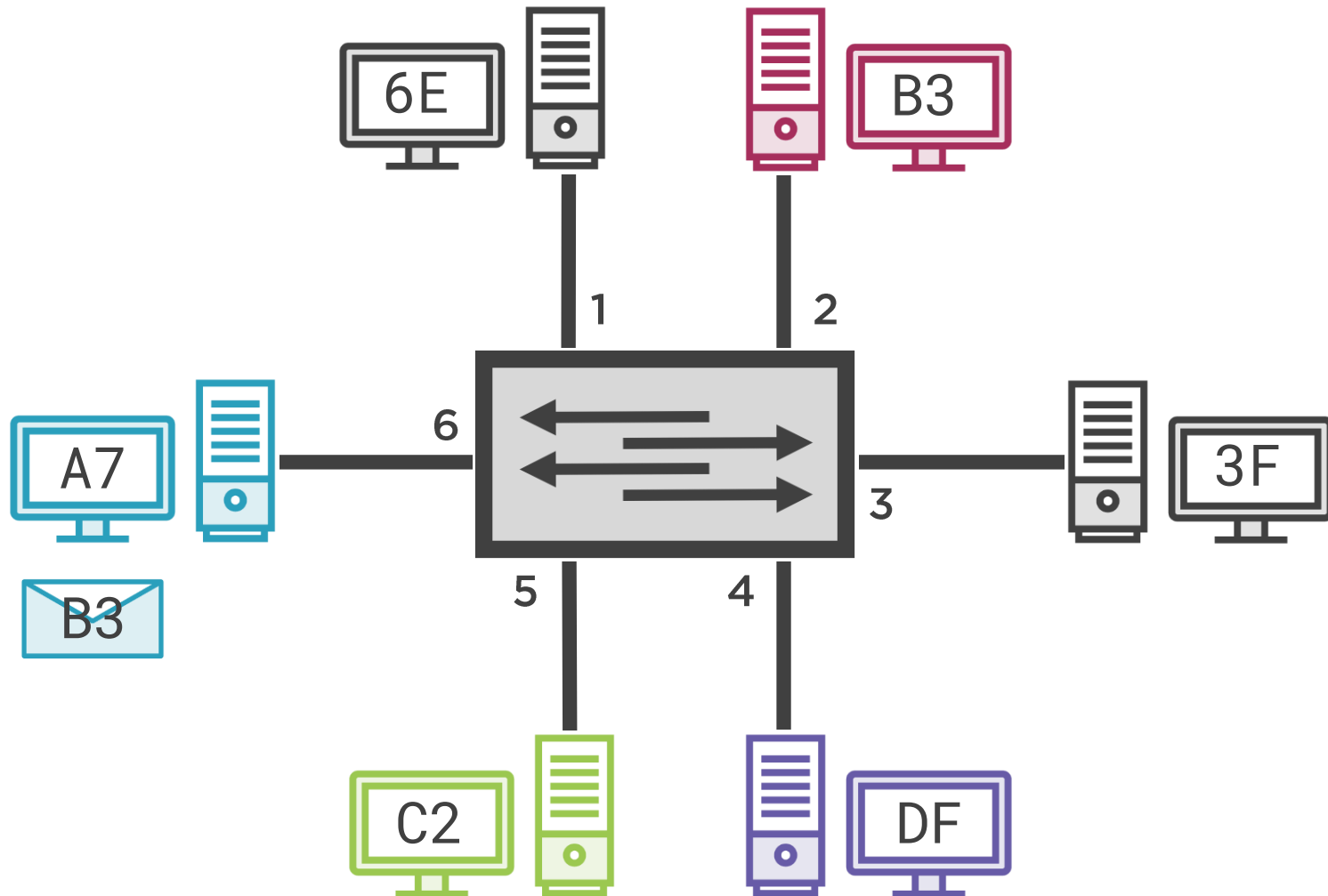


Port	MAC
1	
2	
3	
4	
5	
6	A7



MAC Address Table

1.3

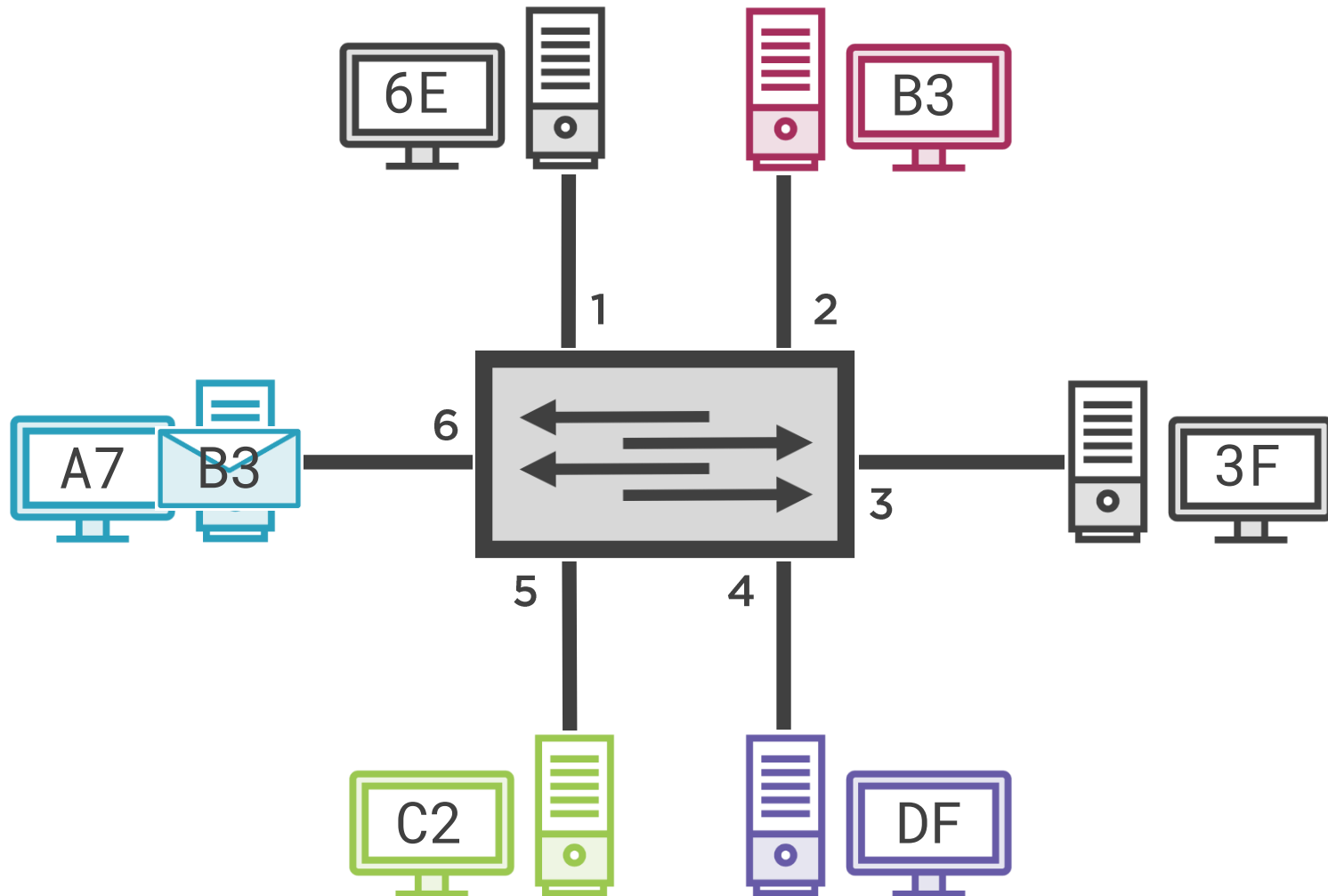


Port	MAC
1	6E
2	B3
3	3F
4	DF
5	C2
6	A7



MAC Address Table

1.3

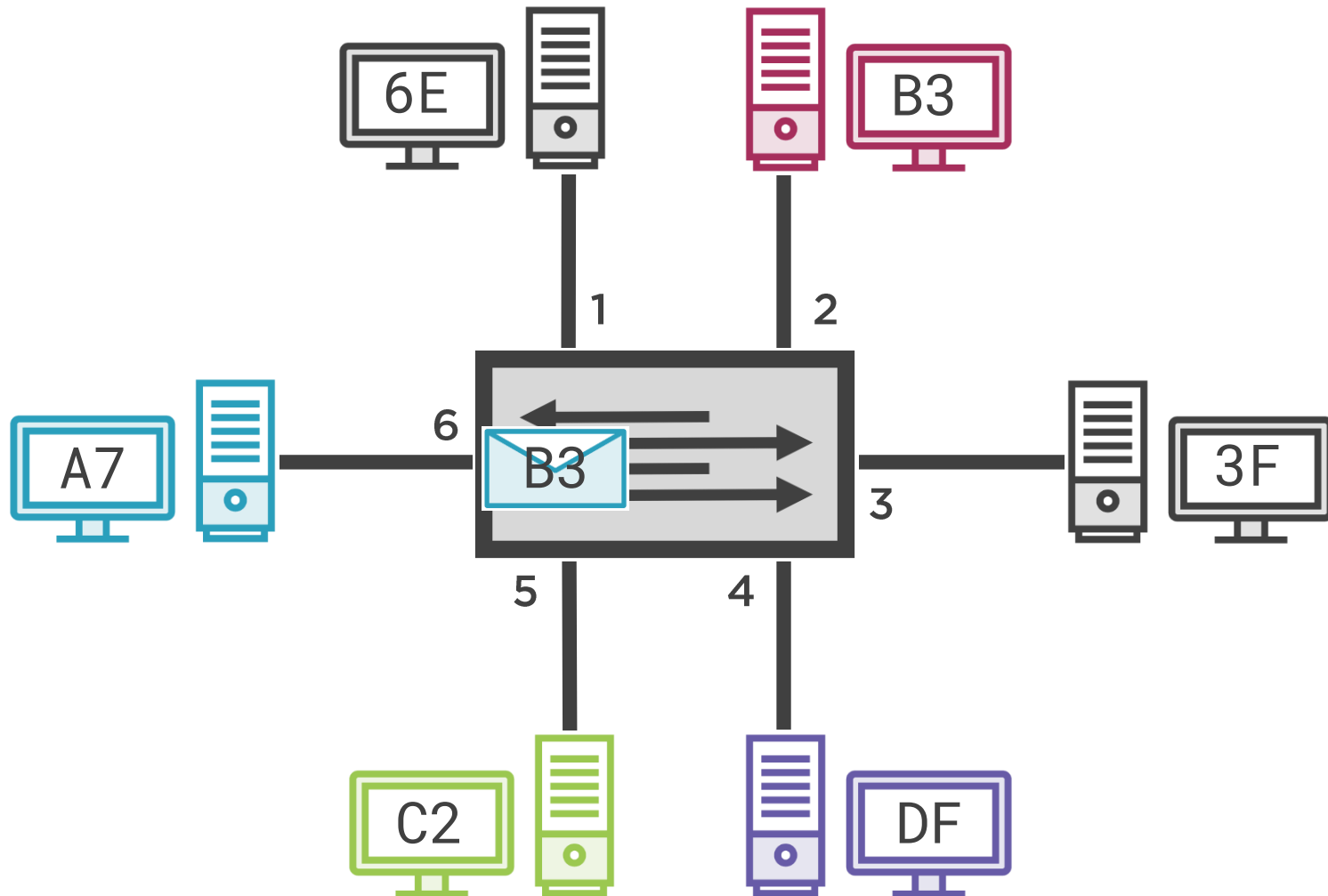


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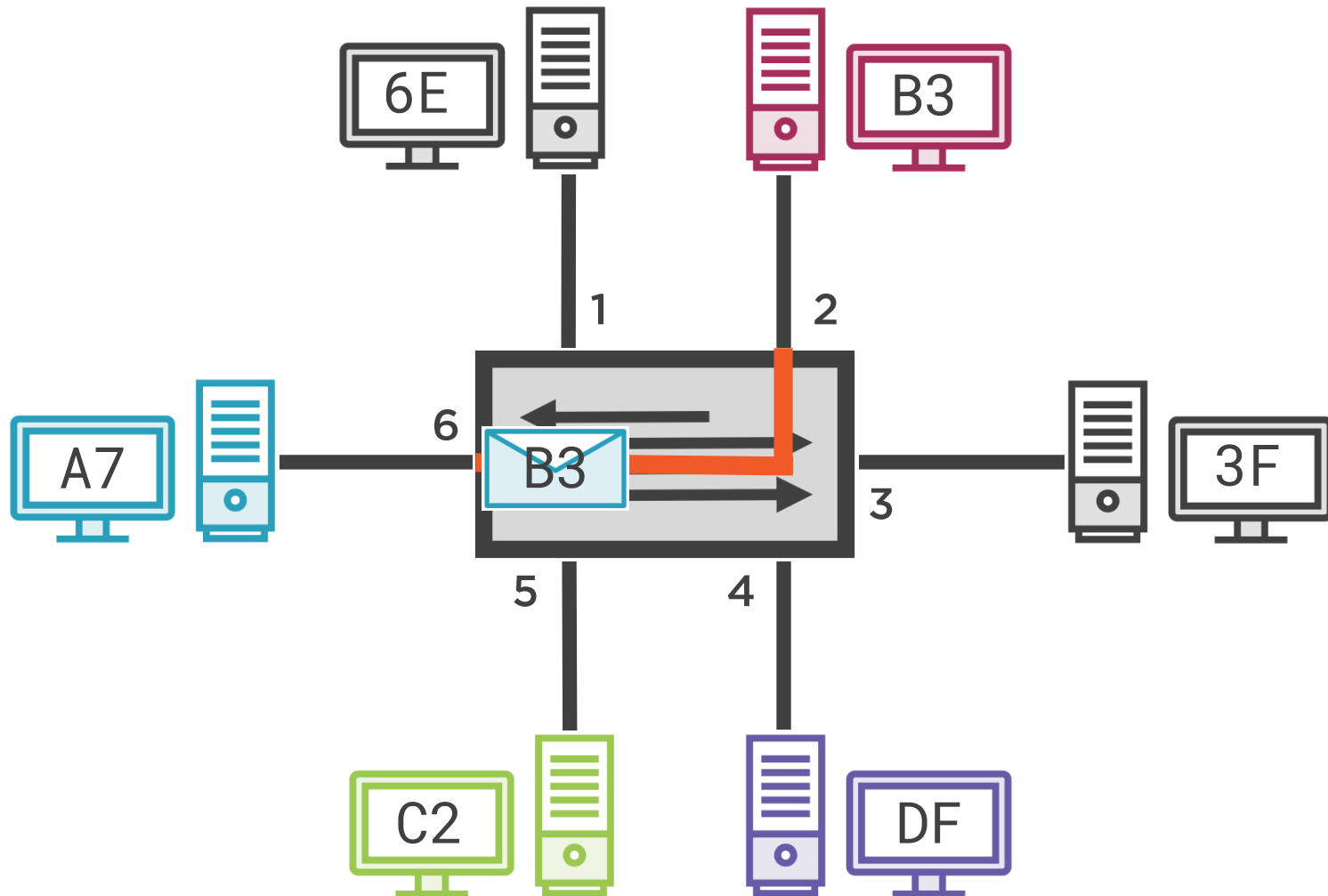


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MAC Address Table

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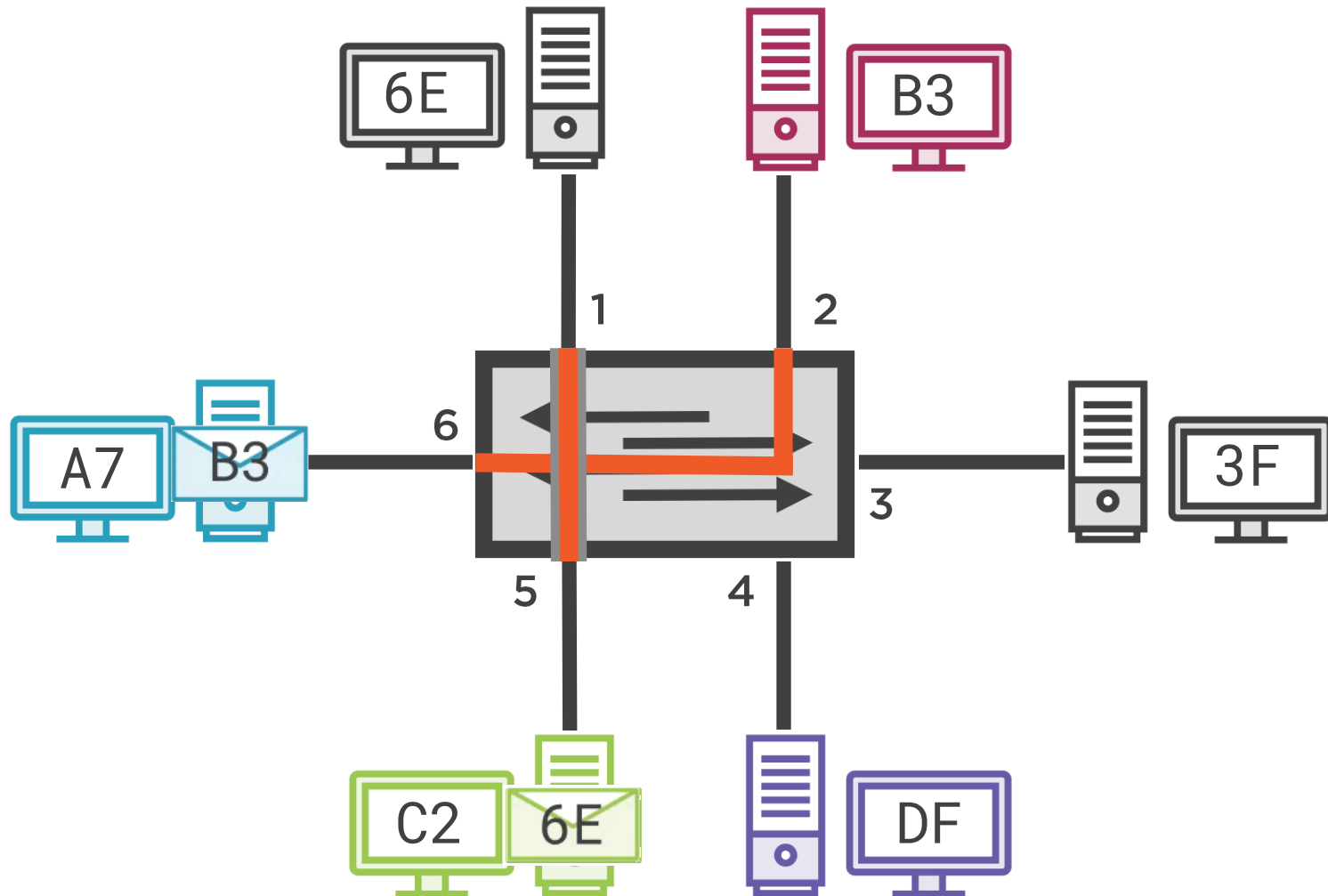


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MAC Address Table

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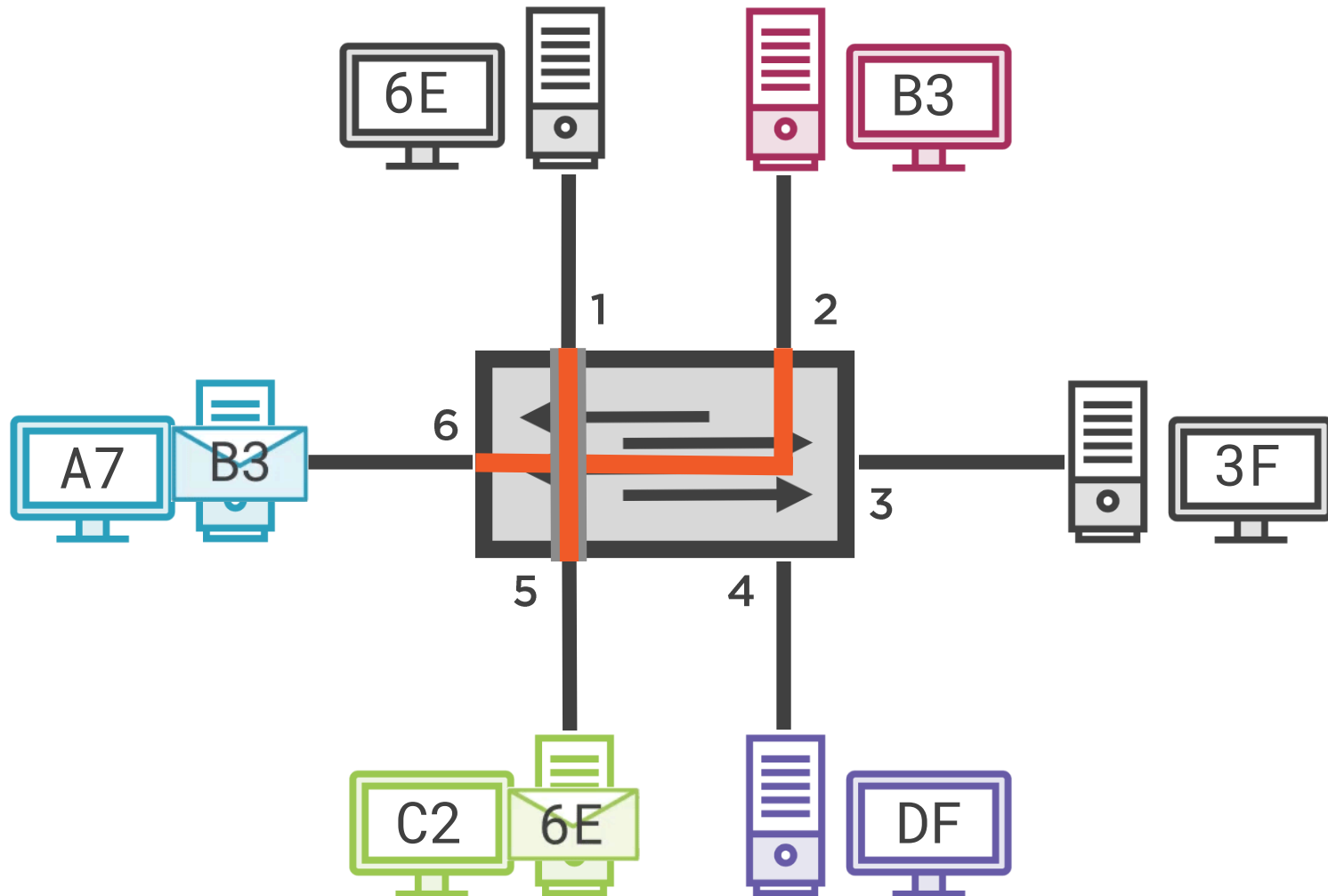


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MAC Address Table

1.3



Port	MAC
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Broadcasts



Ethernet II Frame

Destination MAC Address	Source MAC Address	Type	Data (Packet)	FCS
FFFF FFFF FFFF	48 bits	16 bits	MAX 1500 Bytes	32 bits



Layer 2 Broadcast Address



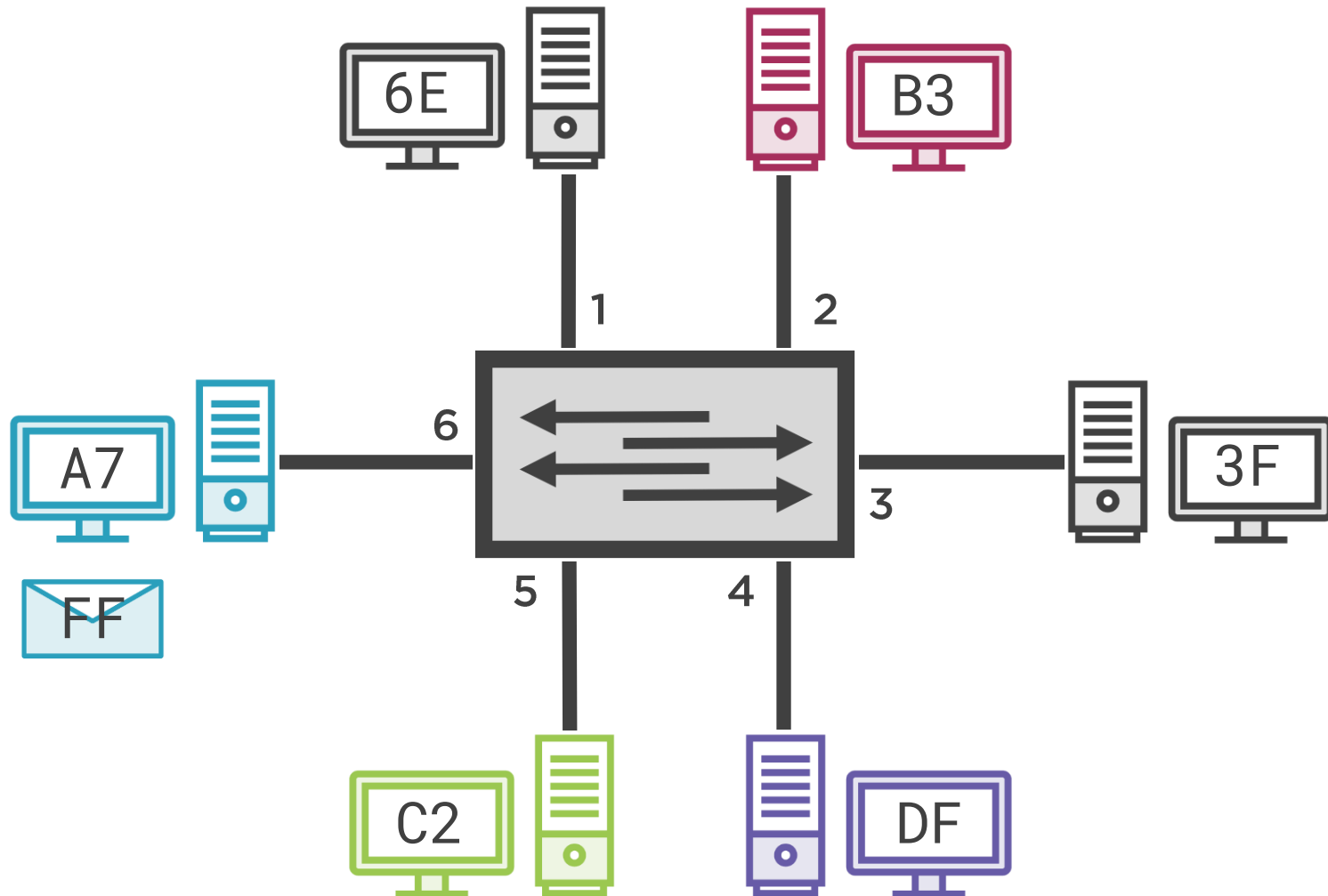
Broadcast

When the destination MAC address of the frame is all F's, the frame is sent out all active interfaces, except the receiving interface.



MAC Address Table

1.3

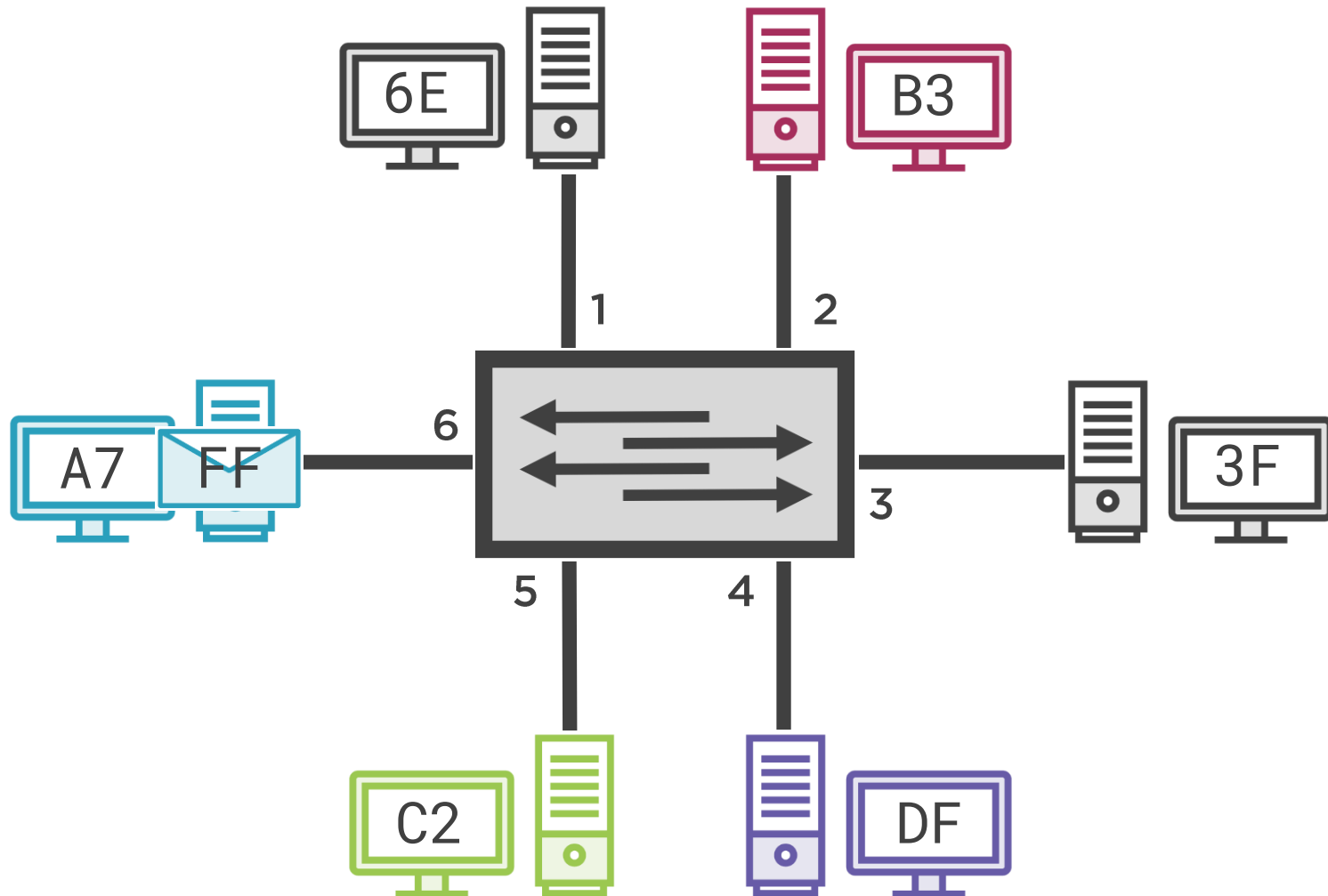


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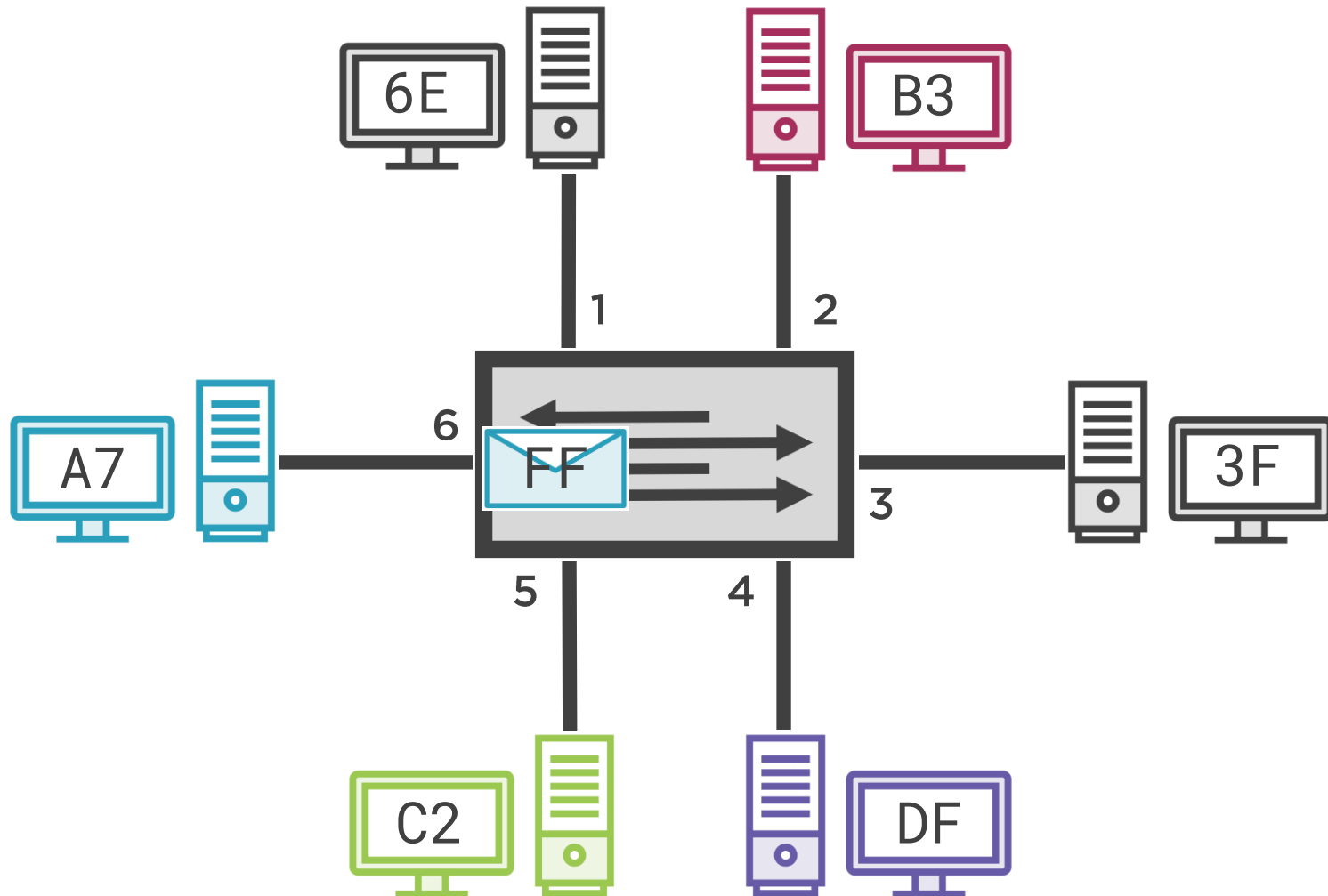


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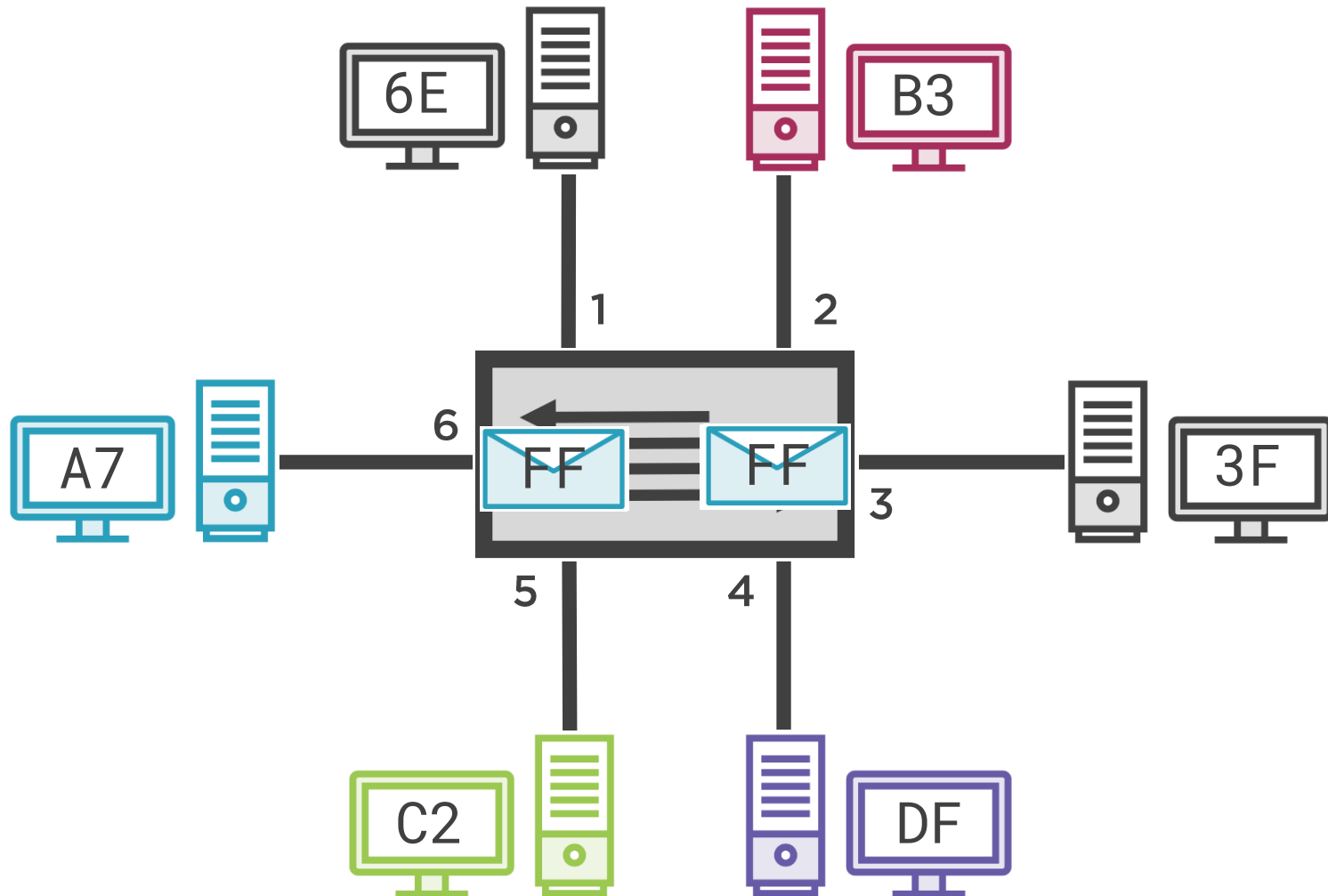


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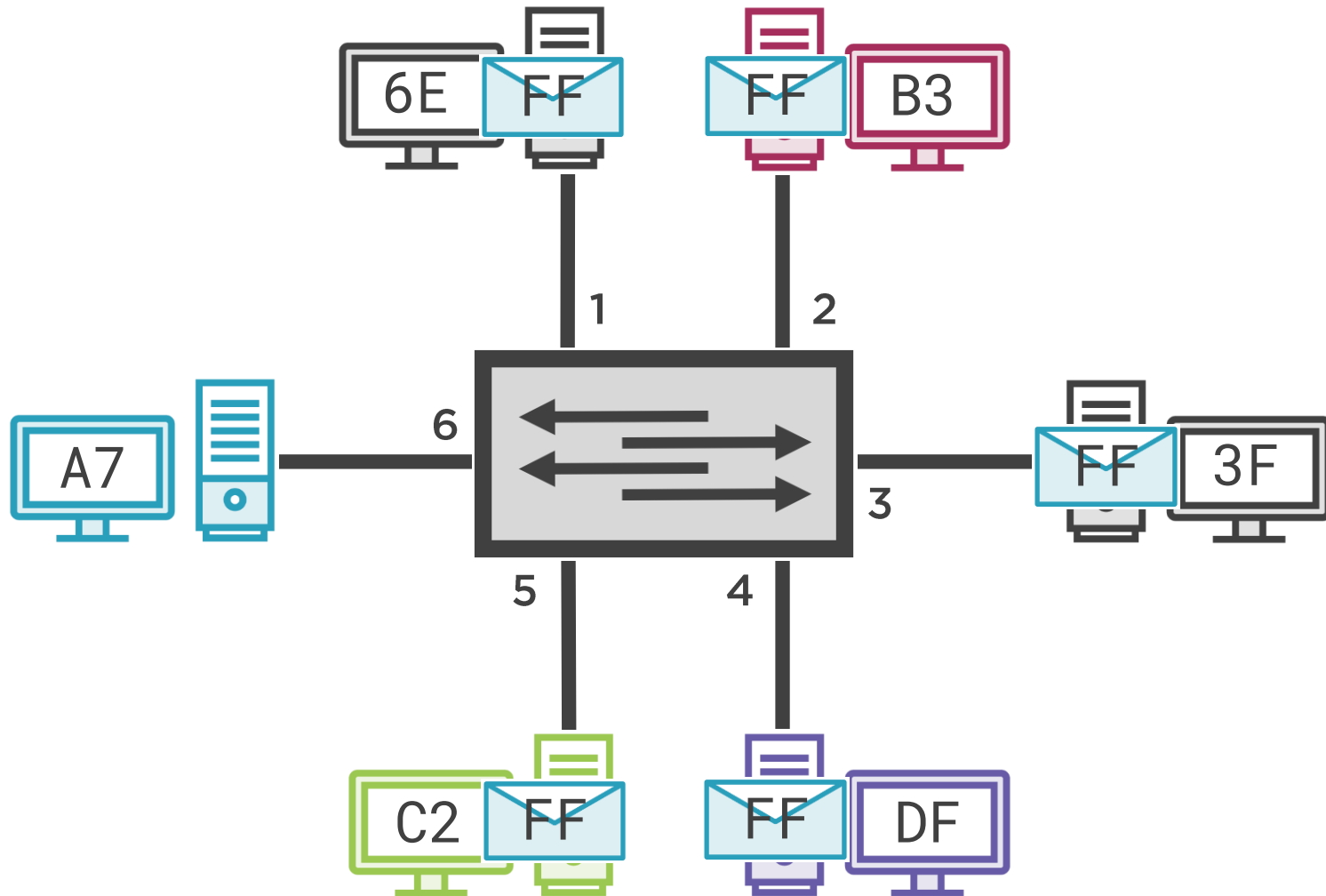


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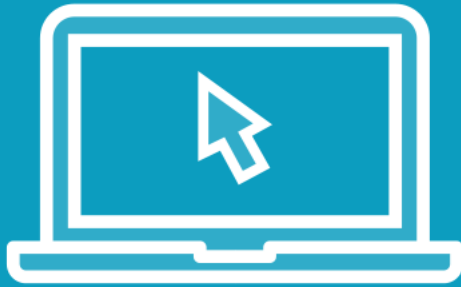


Broadcast Domain

Group of networked devices which will receive a layer 2 broadcast message.



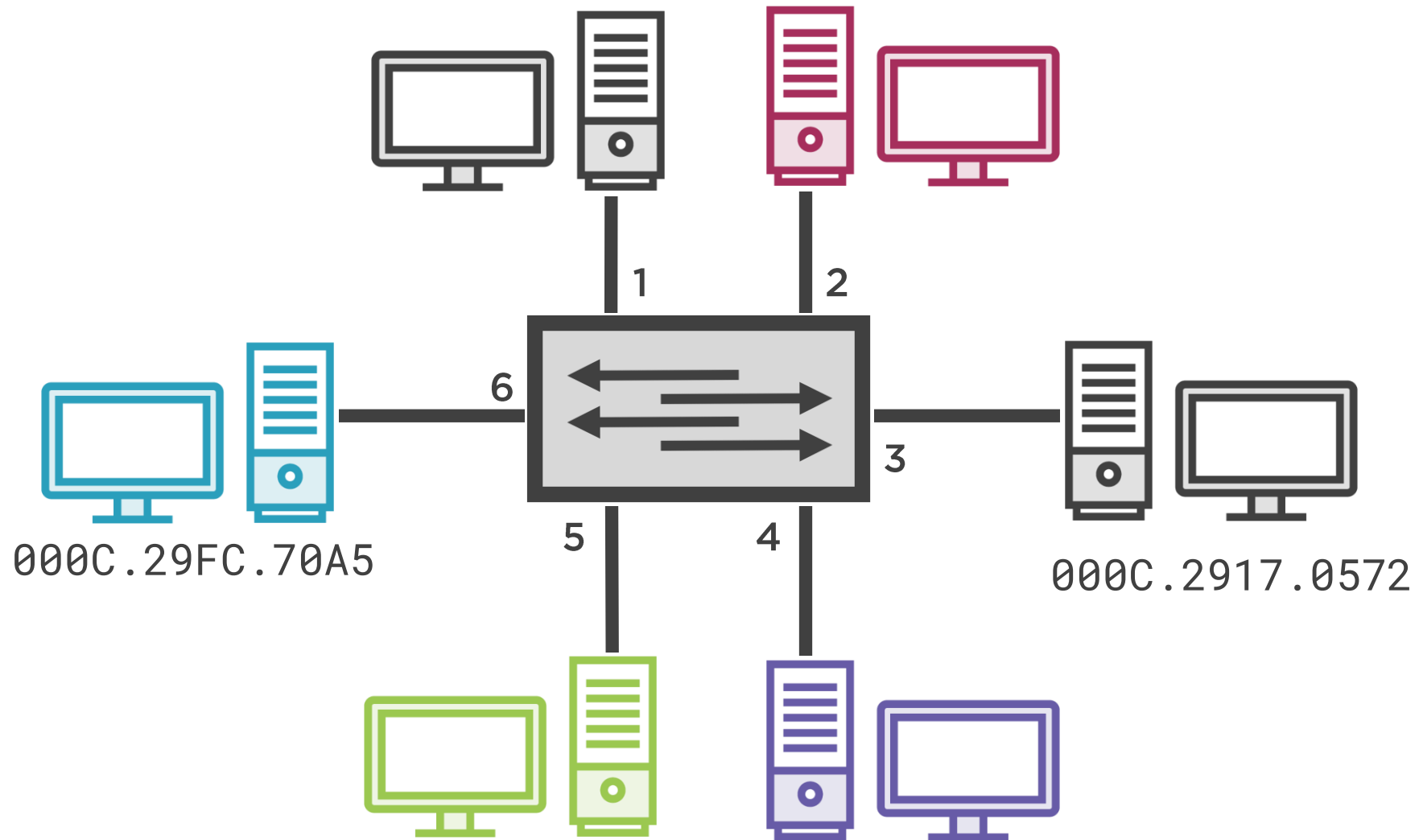
Demo



Examine the MAC Address Table

Ethernet Switch

1.3



Ethernet Switch

1.3

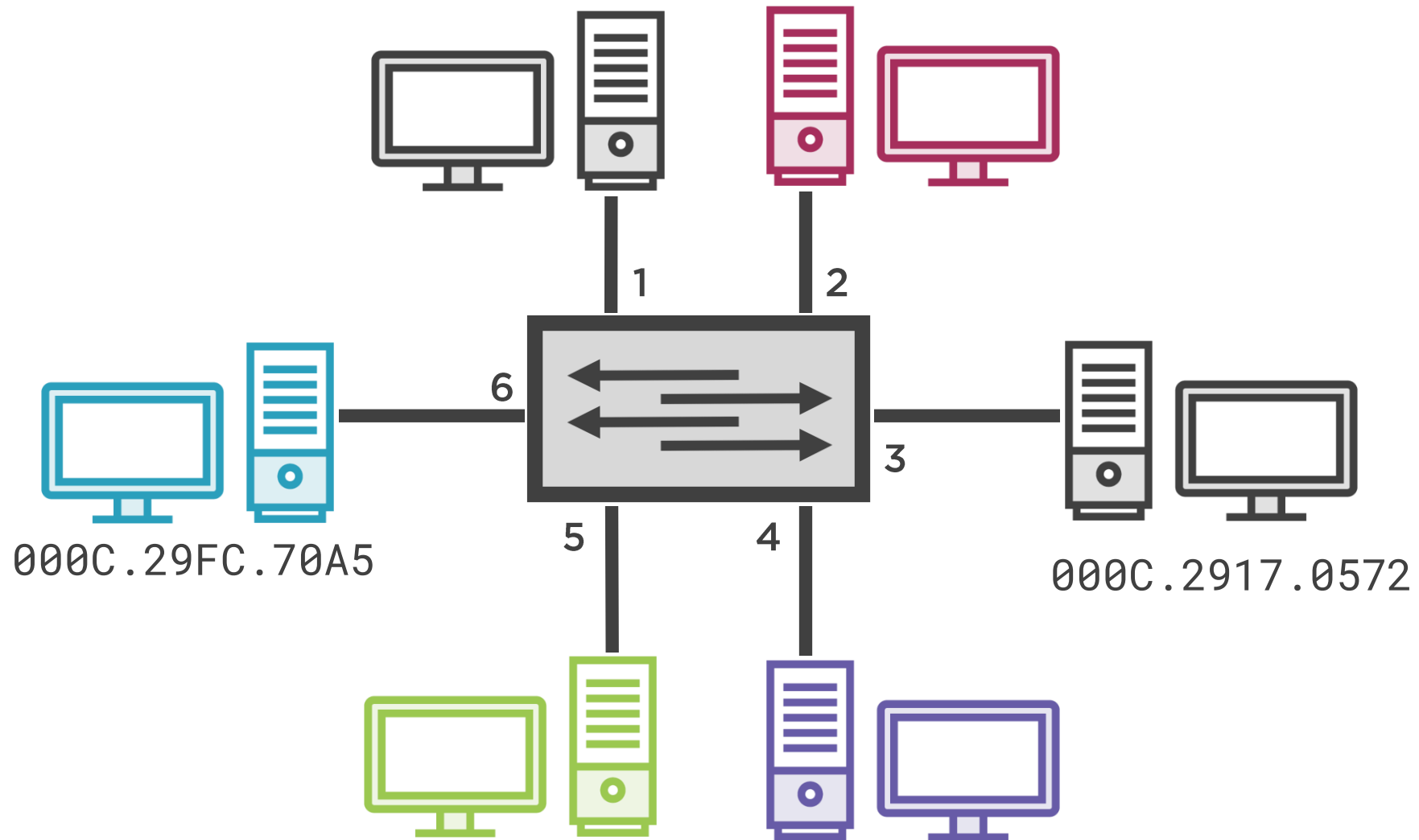


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Ethernet Switch

1.3



Summary



CSMA/CD

Collision Domains

Duplex and Speed

The Ethernet Frame

