Feature	Non-Automatic	Automatic
Variable lifetime	Static	Local to each call
Reentrancy	Not Safe	Safe
Recursive function Support	No	Yes
Memory allocation	Once	Per Function
Default Behavior Verilog	Yes	No

Example Code:

Non-Automatic	Automatic
function [7:0] increment;	function automatic [7:0] multiply_add;
input [7:0] a;	input [7:0] a, b;
reg [7:0] temp;	reg [7:0] temp;
begin	begin
temp = a + 1;	temp = a * b;
increment = temp;	multiply_add = temp + 1;
end	end
endfunction	endfunction