Linux I2C

Kaiden Yu

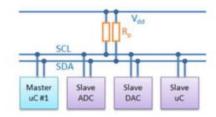
Overview

- Introduction to I2C
 - Basics
 - I2C/SMBus Protocol
- Linux I2C
 - Linux I2C Subsystem
 - I2C Bus Driver
 - I2C Adapter Driver
 - I2C-Dev Driver
 - I2C-Tools
 - I2C-Stub Driver
 - I2C Client Driver

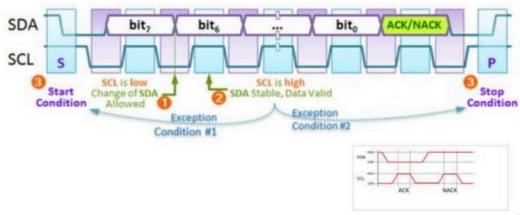
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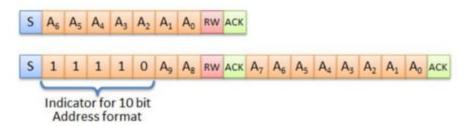
- I2C(Inter-Intergradted Circuit Bus):published by Philip, with licensed trademark and patent (not any more)
- · inter-chip communication
- wires
 - SCL(Serial Clock Line)
 - SDA(Serial Data Line)
- · with pull-up resistors
- · master/slave hierarchy
- bus speed
 - 100KHz/ 400KHz/ 1MHz/ 3.4MHz/ 5MHz



Signal



- Address
 - 7 bits
 - 10 bits



- TWI(Two-Wire Interface): introduced by Atmel to avoid conflicts with trademark issues related to I²C
- SMBus(System Management Bus) : defined by Intel
 - · with tighter constraints (100KHz, timeout, format)
 - I2C adapters can support most SMBus protocol, but not the other way around
- SCCB(Serial Camera Control Bus) : developed by OmniVision
 - three kind of transaction
 - · 3 Phase write
 - · 2 Phase read
 - · 2 Phase write
 - · Don't care bit

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I2C protocol

- · S (1 bit) : Start bit
 - . Sr (1 bit): Repeated start bit
- · Addr (7 bits): I2C 7 bit address
- · Rd/Wr (1 bit) : Read/Write bit
- . A, NA (1 bit): ACK/NACK bit
- Data (8 bits)
- P (1 bit): Stop bit

- Simple send transaction
 - S Addr Wr [A] Data [A] Data [A] ... [A] Data [A] P
- Simple receive transaction
 - · S Addr Rd [A] [Data] A [Data] A ... A [Data] NA P
- Combined transactions
 - S Addr Wr [A] Data [A] Sr Addr Rd [A] [Data] NA P
 - ...

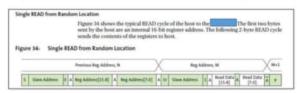
SMBus protocol

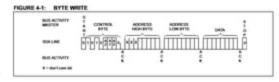
- · S (1 bit) : Start bit
 - · Sr (1 bit): Repeated start bit
- Addr (7 bits): I2C 7 bit address
- Rd/Wr (1 bit): Read/Write bit.
- · A, NA (1 bit): ACK/NACK bit
- Comm (8 bits): Command byte, a data byte which often selects a register on the device.
- Data (8 bits): A plain data byte
 - Count (8 bits): A data byte containing the length of a block operation.
- P (1 bit) : Stop bit

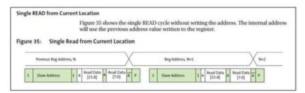
- SMBus Quick Command
 - S Addr Rd/Wr [A] P
- SMBus Receive Byte
 - 5 Addr Rd [A] [Data] NA P
- SMBus Send Byte
 - . S Addr Wr [A] Data [A] P.
- SMBus Read Byte
 - 5 Addr Wr [A] Comm [A] Sr Addr Rd [A] [Data] NA P
- SMBus Read Word
 - S Addr Wr [A] Comm [A] Sr Addr Rd [A] [DataLow] A [DataHigh] NA P
- SMBus Write Byte
 - S Addr Wr (A) Comm (A) Data (A) P
- SMBus Write Word
 - S Addr Wr [A] Comm [A] DataLow [A] DataHigh [A] P

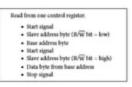
- SMBus Process Call
 - S Addr Wr [A] Comm [A] DataLow [A] DataHigh [A] Sr Addr Rd [A] [DataLow] A [DataHigh] NA P
- SMBus Block Read
 - S Addr Wr [A] Comm [A] Sr Addr Rd [A] [Count] A [Data] A [Data]
 A ... A [Data] NA P
 - · reads a block of up to 32 bytes from a device
- SMBus Block Write
 - S Addr Wr [A] Comm [A] Count [A] Data [A] Data [A] ... [A] Data [A] P
- SMBus Block Write Block Read Process Call
 - S Addr Wr [A] Comm [A] Count [A] Data [A] ... Sr Addr Rd [A] [Count] A [Data] ... A P
 - · sned then read 31 bytes
- 12C Block Read
 - S Addr Wr [A] Comm [A] Sr Addr Rd [A] [Data] A [Data] A ... A [Data] NA P
- I2C Block Write
 - S Addr Wr [A] Comm [A] Data [A] Data [A] ... [A] Data [A] P

Check the datasheet of the slave device for its protocol









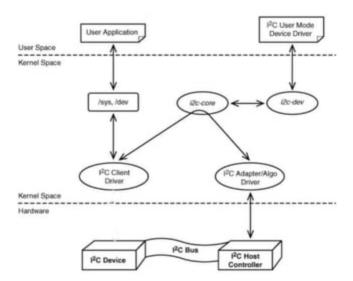
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Linux I2C – Linux I2C Subsystem



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Linux I2C

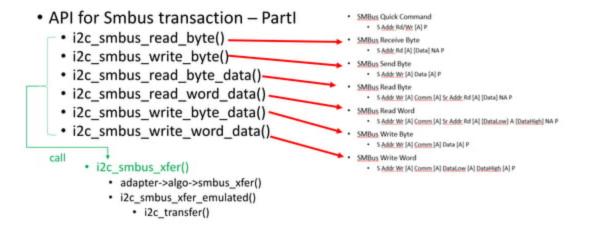
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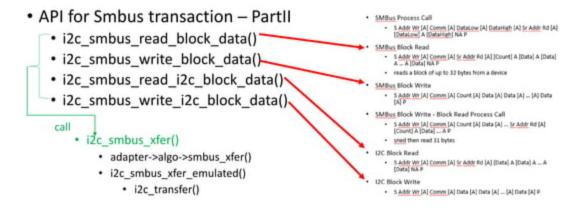
- The bus driver: i2c-core (i2c-core-*.c) · i2c.h
 - creates and registers the bus_type structure -> i2c_bus_type
 - provides API to
 - register and implement I2C adapter drivers
 - register and implement I2C device drivers
 - · matches the device drivers against the devieces detected by the adapter driver
 - define driver and device specific structures
 - struct i2c_adapter
 - · struct i2c algorithm
 - struct i2c_client
 - struct i2c_driver

٠...

- API for I2C transaction
 - i2c_master_send()
 - i2c_transfer_buffer_flags()
 - i2c_transfer()
 - i2c_master_recv()
 - i2c_transfer_buffer_flags()
 - · i2c_transfer()
 - i2c_transfer()
 - __i2c_transfer()
 - adap->algo->master_xfer()

- Simple send transaction
 - S Addr Wr [A] Data [A] Data [A] ... [A] Data [A] P
- Simple receive transaction
 - S Addr Rd [A] [Data] A [Data] A ... A [Data] NA P
- · Combined transactions
 - S Addr Wr [A] Data [A] Sr Addr Rd [A] [Data] NA P
 - ...





Structures for I2C and SMBus transaction messages

```
extern int _ilc_transfer(struct ilc_adapter 'adap, struct ilc_nsg 'nsgs,
                              (nt num)
struct $26,869
        w16 addr:
                       J+ slave oddresi
        wife flags
sorfine 12C M. NO
                                       /* read date, from slove to master */
                                       V* IZC # 80 (a querenteed to be duddes) AT
Edefine IZC STEE
                               8x0010 ** this is a ten bit chip address */
Softing INC H BAA SAFE
                                      /* the buffer of this message is DNR safe */
                                       V* notes unly sense to terrelance */
                                       V* operspace buffers are capied among */
Edefine ISC M RECV LEN
                                      J* length will be first received bute */
Edefine IZC H NO ND ACK
                                      "Y+ If T2C FUNC PROTOCOL NAMEL INC +/
                               Supplie
Riefine IIC M TORORE NAK
                                      ** If Z2C FUNC PROTOCOL MANCE INC */
Edefine IZC H NEV DIR ADDR
                                      ## 15 IZC FUNC PROTOCOL MANGLING */
Indefine IZC M NOSTANT
                                      " IF ISC FUNC ADSTART "/
ender the ISC H STOP
                               BASSON Nº 17 IZC FUNC PROTOCOL MANGZING */
        _wis len:
                               /* mus length
                               /* pointer to may dote
        _w& 'buf;
               data buffer
```

```
#define I2C_SMBUS_BLOCK_MAX 32 /* A
union i2c_smbus_data {
    __u8 byte;
    __u16 word;
    __u8 block[I2C_SMBUS_BLOCK_MAX + 2];

/* and one ma.
```

Functionalities

```
/* To determine what functionality is present */
Sdefine IIC FUNC IIC
DON'T NO THE FUNC SORIT ADDR
Bósfine IZC FUNC PRETOCEL MANGLING
                                        Bussesses /+ INC # TOMORE NAM etc. 4/
Diefine IIC FUNC SHOUS PEC
                                        0x00000010 /* IJC M NOSTART */
Edefine TIC FUNC NOSTART
minfine TIC PUNC SLAVE
                                        8x80000000 /* 5/8/45 2.8 */
BOOFLING TIC FUNC SHOUS BLOCK PROC CALL
BOUFLING THE FUNC SHOUS OUTCOM
DON'T THE TITLE FUNC SMEUS READ BYTE.
Biofine IIC FUNC SMUS WRITE BYTE
                                        8+94040004
poofine IN FUNC SMOUS READ BYTE DATA
SOUTH AND THE PURC SHEETS WELTE BYTE BATA BUSGISSOON
staffine TIC FUNC SMBUS MEAD WIND DATA
DON'T NO TIC FUNC SMEUS WRITE MORD BATA
                                        2v90400001
Rdefine IIC FUNC SHOUS PROC CALL
DON'T THE FUNC SMUUS READ BLOCK BATA
EdeFine IZC FUNC SHOUS WRITE BLOCK DATA 8v8200
SOUTH THE FUNC SAMUS READ THE BLOCK
                                        Bandenness /* IIC-IIAe black afer */
BOOFLING IZC FUNC SMBUS WRITE IZC BLOCK
                                        8x55000008 /* w/ 1-bute reg. oddr. */
Bdofine IIC FUNC SMBUS HOST NOTIFY
                                        8x10000000
```

combinations

```
Edefine IZC FUNC SMEUS BYTE
                                        (IZC FUNC SHBUS READ BYTE ) \
                                         IZC FUNC SHOUS WRITE BYTE)
Edefine IIC FUNC SMBUS BYTE DATA
                                        (T2C FUNC SHBUS READ BYTE DATA | \
                                         IZC FUNC SHOUS WRITE BYTE DATA)
Rdefine IZC FUNC SMBUS WORD DATA
                                        CT2C FUNC SHOUS READ WORD DATA | \
                                         IZC_FUNC_SMOUS_WRITE_WORD_DATA)
sdefine IZC_FUNC_SMBUS_BLOCK_DATA
                                        (T2C FUNC SHBUS READ BLOCK DATA | \
                                         T2C FUNC SHBUS WRITE BLOCK DATA
Rdefine T2C FUNC SMBUS T2C BLOCK
                                        (TZC FUNC SHBUS READ TZC BLOCK | \
                                         T2C FUNC SMBUS WRITE T2C BLOCK)
Edefine IIC FUNC SHOUS ENUL
                                        (IZC FUNC SMBUS QUICK | \
                                         IZC FUNC SHOUS BYTE | \
                                         THE FUNC SHIBLIS BYTE DATA
                                         T2C FUNC SHBUS WORD DATA
                                         THE FUNC SHRIPS PROC CALL |
                                        IZC FUNC SHBUS WRITE BLOCK DATA
                                         T2C FUNC SMBUS T2C BLOCK | \
                                         T2C_FUNC_SHBUS_PEC]
```

What about IZC FUNC SMBUS READ BLOCK DATA?

- Device Model data structures
 - Adapter
 - Bus : struct bus_type platform_bus_type
 - Driver: struct platform_driver
 - Device : struct i2c_adapter
 - Struct i2c_algorithm

- Client
 - Bus: struct bus_type i2c_bus_type
 - Driver : Struct i2c_driver
 - Device : Struct i2c_client

X Adapter could belong to other device driver

- module_usb_driver(/drivers/i2c/busses/i2c-robotfuzzosif.c)
- module_acpi_driver(/drivers/i2c/busses/i2c-scmi.c)
- module_serio_driver(/i2c/busses/i2c-taos-evm.c)
- isa_driver(/drivers/i2c/busses/i2c-elektor.c)

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- Adapter driver(/drivers/i2c/busses/i2c-cadence.c)
 - ■struct cdns_i2c
 - ■struct i2c_algorithm cdns_i2c_algo
 - =cdns_i2c_probe()
 - =cdns_i2c_remove()
 - ■struct platform_driver cdns_i2c_drv
 - module_platform_driver(cdns_i2c_drv)

· i2c_adapter structure

```
struct Us elepter
                                                                                    struct module 'owner
struct cdns 12c
                                                                                    unstoned tot class
                                                                                                                    /* classes to allow probing for */
        struct device
                                    "dev:
                                                                                    const struct (ic algorithm value /* the algorithm to access the for V
                                                                                    votd "algo data:
        vold __tonen 'menbase;
        struct 12c adapter adap:
                                                                                    29 data fields that are valid for all devices. 9/
        struct t2c_mss "p_msq:
                                                                                    const struct (Ir lock sperations 'lock spe-
        int err status:
                                                                                     struct rt_rutes bus_lock;
                                                                                     struct rt_mates mos_lock:
        struct completion afer done:
        unstoned char 'p send buf;
                                                                                     Int Hissout
                                                                                                                 J+ to JUffer 4/
        unsigned char "p recy buf;
                                                                                     int retries
        unsigned int send count:
                                                                                    struct device dev:
                                                                                                                 J* the adapter device */
        unsigned int recv_count;
                                                                                    int ar:
        unsigned int curr recy count:
                                                                                     char name(49):
        int tro:
                                                                                     struct completion dev_released:
        unsigned long input_clk;
                                                                                     struct ewter userspace clients lock
        unsigned int (2c_clk:
                                                                                     struct list_head uperspace_clients;
        unsigned int bus_hold_flag:
        struct elk relk:
                                                                                     struct the bus receivery tafe thus receivery tafa:
        struct motifier_block clk rate change nb;
                                                                                    count abruct the adapter gairle "quirks.
        w32 guirks:
                                                                                    struct ing densin "host notify donein:
                                                                             adefine to the adaptared) container ofed, struct the adaptar, deck
```

i2c_algorithm structure

```
struct 12c algorithm
       /* If an adapter algorithm can't do IZC-level access, set master xfer
           to MULL. If an adapter algorithm can do SMBus access, set
           smbus yfer. If set to MULL, the SMBus protocol is simulated
           using common I2C messages */
       /* master xfer should return the number of messages successfully
           processed, or a negative value on error */
       int ('master_xfer) struct 12c_adapter 'adap, struct 12c_msg 'msgs,
                           int num):
       int (*smbus xfer) struct 12c_adapter *adap, w16 addr.
                           unsigned short flags, char read write.
                           ull command, int size, union 12c smbus data "data);
        /* To determine what the adapter supports */
       u32 ("functionality) (struct 12c adapter "):
#1f IS_ENABLED(CONFIG I2C SLAVE)
       int ("reg_slave)(struct 12c_client "client);
       int ("unreg slave)(struct 12c_client "client);
#endtf
```

- master_xfer
 - i2c access
 - can emulate Smbus protocol
- smbus_xfer
 - smbus access

Algorithm

```
static int cdns_i2c_master_xfer(struct i2c_adapter *adap, struct i2c_msg *msgs,
                                              int num)
static const struct 12c_algorithm cdns_12c_algo = {
        master xfer
                        cdns_t2c_master_xfer,
        .functionality = cdns_12c_func,
              static u32 cdns 12t func(struct 12c adapter *adap)
                      return I2C_FUNC_I2C | I2C_FUNC_10BIT_ADDR
                              (I2C_FUNC_SMBUS_EMUL & -I2C_FUNC_SMBUS_QUICK)
                              I2C_FUNC_SMBUS_BLOCK_DATA:
```

```
12c8: 12c8ff828888
                                                                                       compatible = "cdns,12c-r1p14", "cdns,12c-r1p18"

    Transaction

                                                                                       status = "disabled":
                                                                                       interrupt-parent = <&gic>:

    static int cdns_i2c_master_xfer()

                                                                                       interrupts = of 17 4x
                                                                                       reg = <0x0 0xff020000 0x0 0x1000>;

    cdns i2c process msg()

                                                                                       Haddress-cells # <1a:
                                                                                       #stre-cells = effe

    cdns i2c mrecv()

                      · cdns i2c readreg()
                        cdns i2c writereg()
                   cdns_i2c_msend()
                                                                                    struct cdns_tzc (
                                                                                          struct device
                        cdns i2c readreg()
                                                                                          void __ionen 'nenbase
                                                                                           struct t2c_adapter adap
                     · cdns i2c writereg()
      #define cdns_12c_readreg(offset)
                                                 readl_relaxed id -> membase + offsct)
      #define cdns_12c_writereg(val, offset) writel_relaxed(val, id->membase + offset)
      #define readl relaxed(c)
                                         ({ u32 _r = le32_to_cpu((__force __le32)__raw_readl(c)); _r; })
      #define writel relaxed(v,c)
                                          ((void)__raw_writel((__force u32)cpu_to_le32(v),(c)))
```

Memory-mapped I/O access

```
#define __raw_writel __raw_writel
static inline void __raw_writel(u32 val, volatile void __tomem *addr)
{
        asm volatile("str %w8, [%1]" : "rZ" (val), "r" (addr));
}
```

register offsets and bit mask definitions

```
static void cdns_12c_mrecv(struct cdns_12c *id)
                                                                                             /* Register offsets for the TZC device. */
                                                                                             #define COMS_I2C_CR_OFFSET
                                                                                                                                 E+10
                                                                                                 the CONS 12C SR OFFSET
                                                                                                                                 0x04
                                                                                             moeftine CDMS IZC ADDR OFFSET
                                                                                                                                 0v08
                                                                                             #define CDMS IZC DATA OFFSET
                                                                                                                                 0x00
                                                                                             #define CONS_I2C_ISR_OFFSET
                                                                                                                                 0×10
         /* Put the controller in master receive mode and clear
                                                                                             Bdefine CDMS T2C XFER SIZE OFFSET
                                                                                                                                 0×14
         ctrl_reg = cdns_12c_readreg CDNS_I2C_CR_OFFSET):
                                                                                             #define CDMS_I2C_TIME_OUT_OFFSET
                                                                                                                                 0×10
                                                                                             #define COMS IZC IER OFFSET
                                                                                                                                 0x24
         ctrl reg | CDNS I2C CR RW | CDNS I2C CR CLR FIFO
                                                                                             #define CBMS_I2C-IBR_OFFSET
                                                                                                                                 0×28
         cdns_i2c_writereg(ctrl_reg, CDNS_I2C_CR_OFFSIJ);
```

```
/* Control Register Bit mask definitions */
Bdefine CDBS_IZC_CB_MOLD BIT(4) /* Hold Bus bit */
Bdefine CDBS_IZC_CB_MOLD BIT(3)
Bdefine CDBS_IZC_CB_MEA BIT(3)
Bdefine CDBS_IZC_CB_MEA BIT(3)
BIT(1)
```

Probe / remove

```
static int cdes 12c probe(struct platform device 'pdev)
        struct resource or non:
        struct cdns_12c *1d;
        int ret:
        const struct of device id 'match:
        td - devm kralloc/fodev -- dev. streef("td), GFP KERNEL);
        tf (!td)
                return - ENOMEN:
        td.-dev - Indev.-dev:
       platform_set_drvdata(pdev. id);
        td -adam numer - THIS MODULE:
        1d -adap dev. of node - pdev -dev. of node:
        td-adap algo - icdns t2c algo:
        id -adap timeout - CDMS_IZC_TIMEOUT
        id -adap retries - 3:
                                       /* Default retry value. */
        td -adap.algo data - td:
        id -adap.dev.parent - lpdev -dev:
        ret - down_request_irg(ipdev->dev, id->irg, cdns_12c_isr, 0,
                                 DRIVER NAME, 1d)
       ret - 12c_edd_edapter(lid -adap)
        LF (F#E - B)
               goto err clk dis:
```

```
static int cdns_i2c_renewe(struct platforn_device *pdev) {
    struct cdns_i2c *id = platforn_get_drvdata(pdev);
    i2c_del_adapter(&id -adapt);
    clk_nottruer_unregister(id -clk, &id -clk_rate_change_nb);
    clk_dsable_unregister(id -clk);
    pn_runtine_disable(&pdev -dev);
    return 0;
}
```

Get/set driver data

```
static inline void platform_set_drvdata(struct platform_device *pdev,
       dev_set_drvdata(&pdcv->dcv, data);
                                         static inline void dev_set_drvdata(struct device "dev, void "data)
                                                 dcv->driver_data = data;
static inline void "platform_get_drvdata(const struct platform_device "pdev)
        return dev_get_drvdata(&pdev->dev);
                                        static inline void *dev get drvdata(const struct device *dev)
                                                return dev->driver_data;
```

i2c_add_adapter() / i2c_del_adapter()

```
    i2c_add_adapter()
    i2c_register_adapter()
    device_register(&adap->dev)
    device_add()
    of_i2c_register_devices()
    of_i2c_register_device()
    i2c_new_device()
    device_register(&client->dev)
    device_add()
```

```
    i2c_del_adapter()
    i2c_unregister_device(client)
    device_unregister(&client->dev)
    device_del()
    device_unregister(&adap->dev)
    device_del()
```

Matching

/arch/arm64/boot/dts/xilinx/zynqmp.dtsi

```
12c0: 12c8ff020000 {
       compatible = "cdns_12c-r1p14", "cdns_12c-r1p10"
        status = "disabled":
        interrupt-parent = <figic>;
        interrupts = <0 17 4>:
        reg = <0x0 0xff020000 0x0 0x1000>:
        #address-cells = <1>:
        #size-cells = <0>;
}:
12c1: 12c8ff030000
       compatible = "cdns.12c-rip14", "cdns.12c-rip10"
        status = "disabled":
        interrupt-parent = «Agic»:
        interrupts = <0 18 4>;
        reg = <0x0 0xff030000 0x0 0x1000>;
        #address-cells = <1>:
        #size-cells = <0>:
}:
```

Device driver structure

· Driver registration/unregistration

```
#define module platform driver( platform driver)
       module driver( platform driver, platform driver register,
                       platform driver unregister)
#define module_driver( driver, register, unregister, ...)
static int init driver## init(void)
                                                                       tatic int __init cdns_i2c_drv_init(void)
       return register(&( driver) , ## VA ARGS );
                                                                              return platform_driver_register(&(cdns_i2c_drv));
                                                                       odule_init(cdns_i2c_drv_init);
module_init( driver##_init); \
                                                                       tatic void __exit cdns_i2c_drv_exit(void)
static void exit driver## exit(void) \
{ \
                                                                              platform_driver_unregister(&(cdns_i2c_drv));
       _unregister(%( driver) , ## VA ARGS ); \
                                                                       odule_exit(cdns_i2c_drv_exit)::
module exit( driver## exit):
```

platform_driver_register() / platrom_driver_unregister()

```
#define platform_driver_register(drv) \
__platform_driver_register(drv, THIS_MODULE)
```

```
    driver_register()

    bus add driver()

    driver attach()

    driver attach()

                         · driver_probe_device()

    really probe()

static int really probe(struct device "dev. struct device driver "drv)
     if (dev.)bus->probe)
            ret = dev->bus->probe(dev);
            if (ret)
                   goto probe failed:
            ret = drv->probe(dev);
            if (ret)
                   goto probe_failed;
```

probe not assigned in Platform_bus_type so drv->probe will be called

```
    driver_unregister()

    Bus remove driver()

    Driver detach()

    Device release driver internal()

    device release driver()

static vold device release driver(struct device "dev. struct device "parent)
       struct device_driver *drv;
       tf (dev->bus && dev->bus->renove)
              dev->bus->renove(dev):
       else if (dry->remove)
              dry->renove(dev):
```

remove not assigned in platform_bus_type so drv->remove will be called

platform_drv_probe() / platform_drv_remove()

```
static lift statform dry probetatruct device " devi
       struct platform driver "dry - to platform driver; dry -driver;
       struct platform device "dow - to platform device: dow):
        tat.ret:
        ret - of clk_set_defaults | der -of_node_false |:
        of (ret = 8)
         ceture ret.
        ret - dev am donain attach dev. true
        of (ret)
                goto out:
        (f (dry-)proba) (
                ret - dry--probe:dev):
                if (ret)
                        day on donain detach: day, true
out:
        if (dry-prevent deferred probe 65 ret -- EPROBE DEFER)
                dev_warm( dev, "probe deferral net supportedio");
                ret - - ENXIO
        return ret:
```

```
static int platform_drv_renove(struct device "_dev)
{
    struct platform driver "drv = to platform driver( dev-=driver):
    struct platform_device "dev = to_platform_device(_dev);
    int ret = 0;

    tf (drv-=renove)
        ret = drv-=renove(dev);
    dev_pm_domain_detsch(_dev, true);
}
```

Introduction to I2C

- Basics
- I2C/SMBus Protocol

Linux I2C

- Linux I2C Subsystem
- I2C Bus Driver
- I2C Adapter Driver
- I2C-Dev Driver
- 12C-Tools
- I2C-Stub Driver
- 12C Client Driver

- I2c-dev.c
 - · i2cdev_read()
 - i2cdev_write()
 - I2cdev_ioctl()
 - I2cdev_open()
 - 12cdev_release()
 - Struct file_operations i2cdev_fops
 - i2c_dev_init()
 - · i2c_dev_exit()

i2cdev_open()

```
statte int (Befre speniatrunt leads "Leads, atruct file "file)
       unchannel the winer - inteer (ineds):
       struct the client "client
       struct Ux eductor 'educ-
       adap - 12s_pet_adapter(winer);
       tf (Leiten)
               return ENGGER
       /* Ibis creates an ammymus Uc cilent, which may later be
        * pointed to some others using INC SCARE or INC SLARE FORCE.
        * Fits alliest to ** MEVER RECEIVED ** with the driver model
        * or IN core code? It hist helds private cooles of addressing
        * Information and Acube a PEC flow.
       client - kralles: sizesf("client), GFP sreets)
       of ( |client)
               (Dr. pot_adapter(adap):
               return - ENGAGE
       searlest cliest -name. Ill mass till '(): der th', adap -ar);
       eliget -adapter - adap-
       file -private data - client
       return 8.
```

- i2c_get_adapter()
 - · idr find()
 - try_module_get()
 - · get_device()
- creates an anonymous i2c_client
- can be pointed to address using I2C SLAVE or I2C SLAVE FORCE
- assign file->private_data to client

· i2cdev_release()

```
static int i2cdev_release(struct inode *inode, struct file *file)
{
    struct i2c_client *client = file->private_data;
    i2c_put_adapter(client->adapter);
    kfree(client);
    file->private_data = NULL;
    return 0;
}
```

- i2c_put_adapter()
 - put_device()
 - · module put()
- · kfree client
- assign file->private_data to NULL

```
i2cdev_write()
medup_user()
copy_from_user()
i2c_master_send()
i2c_transfer_buffer_flasgs(..., 0)
i2c_transfer()
__i2c_transfer()
__adap->algo->master_xfer()
```

```
    i2cdev_read()
    i2c_master_recv()
    i2c_transfer_buffer_flags(..., I2C_M_RD)
    i2c_transfer()
    i2c_transfer()
    adap->algo->master_xfer()
    copy_to_user()
```

eventually calls algo->master_xfer() which means SMBus adapters won't support these functions

i2cdev_ioctl() – set slave address

```
static long i2cdev_ioctl(struct file *file, unsigned int cmd, unsigned long arg)
       struct 12c client *client = file->private data:
       unstaned long funcs:
       dev_dbg(&client->adapter->dev, "toctl, cmd=8x802x, arg=8x802lx\n",
                cmd, arg);
        switch (cmd) (
       case IZC SLAVE:
        case IZC SLAVE FORCE
                if ((arg > 0x3ff) ||
                    (((client -> flags & I2C M TEN) == 0) & arg > 0x7f))
                        return -EINVAL-
                if (cmd == I2C_SLAVE && i2cdev_check_addr(client->adapter, arg))
                        return - FBUSY-
                /* REVISIT: address could become busy later */
                client->addr = arg:
                return 8:
```

- if cmd==I2C_SLAVE_FORCE , i2cdev_check_addr won't be called
- i2cdev_check_addr()
 - i2cdev_check()
- busy if driver bound to it

```
static tet Meder_absoluterant device "der, end "adep)
struct Oc_ettent "client - thc_entfr_ctlien(dev);
if (iclient |) client -ader to "(entired but "jadep)
return der -driver 7 - 1966 | 8;
```

i2cdev_ioctl() – ten bit address

```
static long i2cdev_loctl(struct file *file, unsigned int cmd, unsigned long arg)
{
    ...
    case I2C_TEMBIT:
        if (arg)
            client->flags |= I2C_M_TEM;
        else
            client->flags &= -I2C_M_TEM;
        return 0;
```

i2cdev_ioctl() – get functionality

```
static long i2cdev_ioctl(struct file *file, unsigned int cmd, unsigned long arg)
{
...

case I2C_FUNCS:
    funcs = i2c_get_functionality(client->adapter);
    return put_user(fulcs, (unsigned long __user *)arg);

static inline u32 i2c_get_functionality(struct i2c_adapter *adap)
{
    return adap->algo->functionality(adap);
}
```

i2cdev_ioctl() – I2C transaction

```
static long 12cdev_loctl(struct file "file, unsigned int cmd, unsigned long arg)
  case I2C RDWR:
         struct 12c_rdwr_loctl_data rdwr_arg;
         struct 12c_msg *rdwr_pa:
             copy_from_user &rdwr_arg.
                             (struct 12c_rdwr_loctl_data __user *)arg.
                             stzeof(rdwr arg)))
                  return -EFAULT-
         If (rdwr arg nmsgs > I2C RDWR IOCTL MAX MSGS)
                  return -EINVAL:
          rdwr pa | mendup user rdwr arg.msgs.
                                rdwr_arg.nmsgs * sizeof(struct 12c_msg));
          if (IS_ERR(rdwr_pa))
                  return PTR_ERR(rdwr_pa);
         return (2cdev loctl rdwr/client, rdwr arg.nmsgs, rdwr pa);
```

- copy i2c rdwr loctl data
- · copy i2c msg
- · i2cdev ioctl rdwr()

i2cdev_ioctl() – I2C transaction

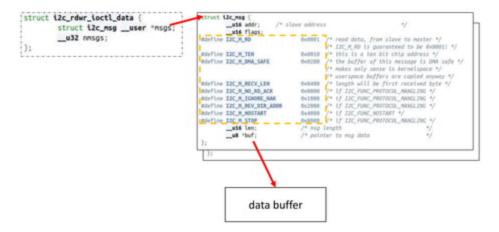
```
static noinline int i2cdev_loctl_rdwr(struct i2c_client "client,
                unsigned nesgs, struct 12c_msg 'msgs)
        uB user ""data ptrs:
        int i. res:
        data ptrs = kmalloc_array(nmsgs, stzeof(uB __user *), GFP_KERNEL);
        for (1 = 0: 1 < nmsqs: 1++) (
                /* Limit the size of the message to a some amount */
                data_ptrs[t] = (u8 __user *)msgs[t].buf;
                msgs[1].buf = mendup_user(data_ptrs[1], msgs[1].len);

    12c_transfer(client->adapter, msgs, nmsgs);

        while (1-- > 0) (
                tf (res >= 0 && (msgs[t].flags & I2C_M_RD)) {
                            copy to user data ptrs[1], msgs[1] buf.
                                          msqs[t].len))
                                 res - - EFAULT:
```

- copy i2c msg buf
- i2c_transfer()
- · copy to user if I2C_M_RD is set

Structures for I2C transaction



SMBus protocol transaction

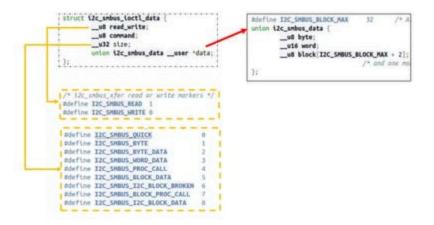
- copy i2c smbus loctl data
- · i2cdev ioctl smbus()

SMBus protocol transaction

```
static noinline int i2cdev_loctl_smbus(struct i2c_client *client.
                u8 read_write, u8 command, u32 size,
                union i2c_smbus_data __user *data)
        union 12c snbus data temp = {}:
        int datasize, res;
        if ((size = I2C SMBUS BYTE DATA)
            (stze = I2C_SMBUS_BYTE))
                datasize = sizeof(data->byte);
                   (copy from user(&temp.
                                          data.
                                                datastze)
                        return -EFAULT:
             12c_smbus_xfer client->adapter, client->addr, client->flags,
              read_write, command, size, Atemp);
                   copy_to_user data, &temp, datasize))
                        return - EFAULT:
```

- · copy i2c smbus data data
- i2c_smbus_xfer()
 - copy to user if I2C_SMBUS_READ or PROC_CALL is set

· Structures for SMBus transaction



· struct file_operations i2cdev_fops

Userspace

- read()
- · write()
- loctl()
- open()
- close()

i2c_dev_init()

```
static int __init i2c_dev_init(void)
{
...
   res = register_chrdev_region(MKDEV(I2C_MAJOR, 0), I2C_MINORS, "i2c");
...
   i2c_dev_class = class_create(THIS_MODULE, "i2c-dev");
...
   i2c_for_each_dev(NULL, i2cdev_attach_adapter);
```

- register major number(89) and minor numbers
- · create class structure
- call i2c_attach_adapter for every adapter

- create cdev structure
- add character device to system
- create device (/dev/i2c-%d)

· i2c_dev_exit()

```
static void __exit i2c_dev_exit(void) {

bus_unregister_notifier(5i2c_bus_type, 5i2cdev_notifier);
    i2c_for_each_dev(NULL, i2cdev_detach_adapter);
    class_destroy(i2c_dev_class);
    unregister_chrdev_region(MKDEV(I2C_MAJOR, 0), I2C_MINORS);
}
```

```
static int i2cdev_detach_adapter(struct device *dev, void *dummy) {
    ...
    cdev_del(&i2c_dev->cdev);
    put_i2c_dev(i2c_dev);
    device_destroy(i2c_dev_class, MKDEV(I2C_MAJOR, adap->nr));
```

- call i2c_detach_adapter() for every adapter
- · destroy class structure
- unregister major number(89) and minor numbers

- create cdev structure
- · destroy device

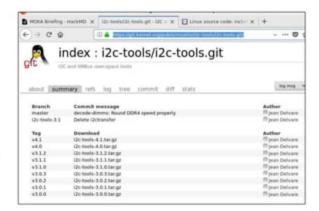
```
Terminal - kaiden@debian: /
kaiden@debian:/$ sudo modprobe
kaiden@debian:/S ls -al /dev
                              grep i2c
                                               10:08 i2c-0
                root i2c
                root i2c
                                              10:08 i2c-8
             1 root i2c
                                           30 10:08 i2c-9
 aiden@debian:/S
```

- Introduction to I2C
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Linux I2C

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- I2C-Stub Driver
- 12C Client Driver

- I2C-tools
 - ■i2cdetect
 - ■i2cdump
 - ■i2cset
 - ■i2cget
 - ■i2ctransfer
 - By "apt-get install i2c-tools" this executable won't be included



i2cdetect: show all the adapter(bus) info

```
Terminal - kaiden@debian: -/Kaidenspace/i2c-tools-4.1/tools
             Terminal Tabs Help
kaiden@debian:~/Kaidenspace/i2c-tools-4.1/tools$ sudo ./i2cdetect -l
                          1915 ambus dpd
                                                                    I2C adapter
12c-1
                          1915 gmbus dpc
                                                                    I2C adapter
        12c
                         nvkm-8008:81:80.8-bus-8085
                                                                    I2C adapter
12c-6
        12c
                         nvkm-8000:01:00.0-bus-0001
                                                                    I2C adapter
        12c
                         DPDDC-A
                                                                    I2C adapter
                         1915 ambus dob
                                                                    I2C adapter
        smbus
                          SMBus I801 adapter at f040
                                                                    SMBus adapter
                         Synopsys DesignWare 12C adapter
                                                                    IZC adapter
12c-18
        12c
                         Synopsys DesignWare I2C adapter
                                                                    I2C adapter
        12c
                         nvkm-0000:01:00.0-bus-0002
                                                                    I2C adapter
                         DPDDC-B
                                                                    I2C adapter
kaiden@debian:-/Kaidenspace/i2c-tools-4.1/tools5
```



i2cdetect : check funtionality

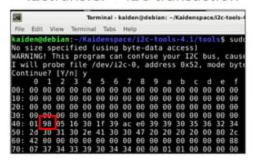
```
Terminal - kaiden@debian: -/Kaidenspace/2c-tools-4.1/tools
        View Terminal Tabs Help
Functionalities implemented by /dev/i2c-0:
SMBus Ouick Command
                                   yes
SMBus Send Byte
                                   ves
SMBus Receive Byte
                                   yes
SMBus Write Byte
                                   yes
SMBus Read Byte
                                   yes
                                   ves
SMBus Read Word
                                   yes
SMBus Process Call
SMBus Block Write
SMBus Block Read
                                   yes
SMBus Block Process Call
                                   yes
I2C Block Write
I2C Block Read
                                   yes
aiden@debian:-/Kaidenspace/i2c-tools-4.1/tools5
```

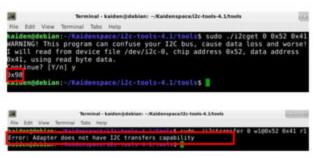
i2cdetect: check slave devices attach to a certain adapter

i2cdump: show the values of all the registers within a slave device

```
Terminal - kaiden@debian: -/Kaidenspace/(2c-tools-4, 1/tools
File Edit View Terminal Tabs Help
No size specified (using byte-data access)
MARNING! This program can confuse your I2C bus, cause data loss and worse!
 will probe file /dev/12c-0, address 0x52, mode byte
```

- i2cget/i2cset : SMBus transaction
- i2ctransfer : I2C transaction





I2C transaction not supported by a SMBus adapter

- i2cget/i2cset : SMBus transaction
- i2ctransfer : I2C transaction

```
The field view Terminal this meight and the control of the control
```

both kinds of transaction work well on a I2C adatper,

I2C-tools source code : tools/i2cbusses.c

```
at open 12; dev(int 12;bus, chur *fllename, size t size, int quiet)
  int file;
  saprintf(filename, size, "/dev/12c/%", 12cbus);
  filename(size - 11 - '10':
  file - mem(filename, O ACMR);
   if (file c 0 AA (errso -- ENCENT || errso -- ENCECTR)) (
      speletf(filename, "/dev/12c-%0", 12chus);
      file - comm(filename, O RDAR);
      erum adt 12c get funcs((nt 12cbus)
   unsigned long funcs;
   int file:
   char filename[20];
   enue adt ret:
   file - open ilc dev(12cbus, filename, s
                                               (filename), 1);
              adt unknown;
    (loctl(file, I2C funcs, &funcs) ( 0)
       ret = ast unwnown;
           (funcs & 12C FUNC 12C)
       ret - adt 12c:
```

I2C-tools source code : lib/smbus.c

```
__s32 12c_smbus_read_byte(int file) {
    union 12c_smbus_data data;
    int err;
    err = [2c_smbus_access(file, I2C_SMBUS_READ, 0, I2C_SMBUS_BYTE, &data);
    11 (err < 0)
        return err;
    ruturn BROFF & data.byte;
}
```

i2c-tools source code : tools/i2ctransfer.c

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- 12C Client Driver

Linux I2C – I2C-Stub Driver

• I2C-stub driver: a fake I2C/SMBus driver

```
Terminal - kalden@deblan: /yyu/bus/I2c/devices
                sudo modorobe 12c-stub chip addr=0x50
                        12c-FTE1001:00
                                           Terminal - kaiden@debian: -/Kaidenspace/Uc-tools-4.14mm
                                                   nvkm-0860:01:00.0-bus-6001
                                                   Synopsys DesignWare I2C adapter
                                                   Synopsys DesignWare I2C adapter
                                                   nykm-0880:01:00.0-bus-8002
                                                   DPDDC-B
                                                                                               I2C adapter
                                debian: -/Kaidenspace/i2c-tools-4.1/tools5
```

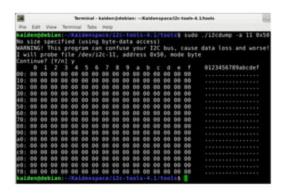
I2C-stub adapter funtionality

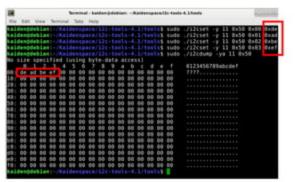
```
Terminal - kaiden@debian: -/Kaidenspace/i2c-tools-4.1/tools
    Edit View Terminal Tabs Help
kaiden@debian:~/Kaidenspace/i2c-tools-4.1/tools$ sudo ./i2cdetect -F 11
Functionalities implemented by /dev/i2c-11:
                                   по
SMBus Quick Command
                                   ves
SMBus Send Byte
                                   yes
SMBus Receive Byte
                                   yes
MBus Write Byte
                                   yes
                                   yes
                                   yes
                                   yes
 MRus Process Call
                                   no
                                   no
MBus Block Read
                                   no
MBus Block Process Call
                                   no
MBus PEC
                                   no
2C Block Write
                                   yes
I2C Block Read
                                   yes
kaiden@debian:-/Kaidenspace/i2c-tools-4.1/tools5
```

chip_addr : slave address of virtual client

```
Terminal - kalden@deblan: /sys/bus/Gc/devices
                  Terminal - kaiden@debian: -/Kaidenspace/I2c-tools-4.1/tools
        ebian; -/Kaidenspace/i2c-tools-4.1/tools5_sudo ./i2cdetect
WARNING! This program can confuse your I2C bus, cause data loss and worse!
      probe file /dev/12c-11.
      probe address range 0x00-0x7f
aiden@debiam: - /Kaidenspace/i2c-tools-4.1/tools$
```

• I2C-stub adapter works as a real adapter (can do SMBus transfer)





- I2C driver testing with i2c-stub adapter
 - addresses of eeprom device are between 0x50~ 0x57
 - with chip_addr=0x50, device will be bound after modprobe eeprom driver

```
Terminal - kaiden@debian: /sys/bus/i2c/devices/11-0050

File Edit View Terminal Tabs Help

kaiden@debian:/sys/bus/i2c/devices$ ls

i2c-0 i2c-10 i2c-2 i2c-4 i2c-6 i2c-8 i2c-FTE1001:00

i2c-1 i2c-11 i2c-3 i2c-5 i2c-7 i2c-9

kaiden@debian:/sys/bus/i2c/devices$ sudo modprobe eeprom

kaiden@debian:/sys/bus/i2c/devices$ ls

0-0052 4-0050 i2c-1 i2c-11 i2c-3 i2c-5 i2c-7 i2c-9

11-0050 i2c-0 i2c-10 i2c-2 i2c-4 i2c-6 i2c-8 i2c-FTE1001:00

kaiden@debian:/sys/bus/i2c/drivers/eeprom$ ls

0-0052 11-0070 4-0050 bind module uevent unbind

kaiden@debian:/sys/bus/i2c/drivers/eeprom$
```

- I2C driver testing with i2c-stub adapter
 - with chip_addr=0x70, device won't be detected after modprobe eeprom driver -> file "new_device" comes to the rescue

```
Terminal - kaldem@debian: //ys/bus/12c/drtvers/wegron
drivers autoprobe drivers probe uevent
```

- I2C driver testing with i2c-stub adapter
 - devices : new_device/delete_device

```
Terminal - kalden@deblan:/kys/bus/2c/devices/2c-11

File Edit View Terminal Tabs Help

kaiden@deblan:/sys/bus/i2c/devices/i2c-11s ls

i1-0070 delete_device i2c-dev name new device power subsystem uevent

kaiden@deblan:/sys/bus/i2c/devices/i2c-11s echo 0x70 > delete_device

kaiden@deblan:/sys/bus/i2c/devices/i2c-11s echo eeprom 0x70 > new_device

kaiden@deblan:/sys/bus/i2c/devices/i2c-11s echo eeprom 0x70 > new_device

kaiden@deblan:/sys/bus/i2c/devices/i2c-11s ls

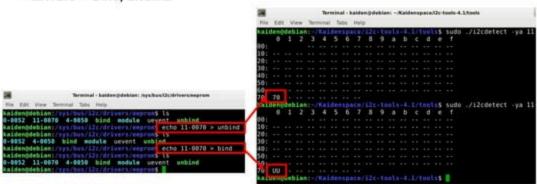
i1-0070 delete_device i2c-dev name new_device power subsystem uevent

kaiden@deblan:/sys/bus/i2c/devices/i2c-11s ls

i1-0070 delete_device i2c-dev name new_device power subsystem uevent

kaiden@deblan:/sys/bus/i2c/devices/i2c-11s
```

- I2C driver testing with i2c-stub adapter
 - · drivers : bind/unbind



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- Client driver (/drivers/mfd/tps65086.c)
 - ■tps65086_probe()
 - ■tps65086_remove()
 - ■struct i2c_driver tps65086_driver
 - module_i2c_driver(tps65086_driver)

Probe / remove

```
static int tpo55006 probe(struct 12c client "client
                         const struct 12c_device_1d *ids)
       struct tps65006 "tps;
       unaigned int version:
       int ret:
       tps - devm_ksalloc(sclient >dev, streef(*tps), GFP_KERNEL);
       of (thes)
                return - ENGMEN
       12c set clientdata(client, tps);
        tps->dev - Sclient->dev
        tps-stru = client-stru
        tps-regnap = devm_regnap_init_l2c(client, itps65006_regnap_conflg);
             regnap read(tps--regnap, TPS65086 DEVICEID, Eversion):
       ret = regnap_add_irq_chip(tps->regnap, tps->trg, IRGF_ONESHOT, 8,
                                 Stps65086_irq_chip, Stps--irq_data);
             mfd add devices tos -- dry. PLATFORM DEVID AUTO. tps65006 cells.
                              ARRAY SIZE (tpo65006 cells), NULL. 0.
                              regnap tru get donain(tps->irg data))
```

```
static int tps65086_remove(struct i2c_client *client)
{
    struct tps65086 *tps = i2c_get_clientdata(client);
    regnap_del_irq_chip(tps->irq, tps->irq_data);
    return 0;
}
```

i2c_get_clientdata() / i2c_set_clientdata()

```
static inline void *i2c_get_clientdata(const struct i2c_client *dev)
{
    return dev_get_drvdata(&dev->dev);
}
```

```
static inline void "dev_get_drvdata(const struct device "dev)
{
    return dev->driver_data;
}
```

```
static inline void i2c_set_clientdata(struct i2c_client *dev, void *data)
{
          dev_set_drvdata(&dev->dev, data);
}
```

```
static inline void dav_set_drvdata(struct device "dev, void "data)
{
    dev->drlver_data = data;
}
```

- devmem_regmap_init_i2c()
 - __regmap_lockdep_wrapper()
 - __regmap_init_i2c()
 - regmap_get_i2c_bus()
 - i2c_check_functionality()
 - return
 - __regm_p_init()

®map_i2c ®map_i2c_smbus_i2c_block ®map_smbus_word_swapped ®map_smbus_word ®map_smbus_byte

```
static const struct regrap hus 'regrap get the bus(struct the client 'the.
                                       const struct regree config 'config'
       if (12c_check_functionality(12c->adapter, I2C_FUNC_I2C))
                return fragmap 12c:
       else of (coefig -wal bits - # AA coefig -reg bits -- # AA
                12c_check_functionality(12c >adapter.
                                         I2C FUNC SMBUS I2C BLOCK))
                return Aregnap t2c snbus t2c block
       else if (config-)val bits - 16 65 config->reg bits - 8 65
                12c check functionality 12c -adapter.
                                         TIC FUNC SHBUS WORD DATA ) )
                switch (regnap get val andian(#12c->dev, MILL, config)) (
               CALE REGNAP ENDIAM LITTLE
                       return fregnap sobus word:
                CALL RECORD ENDIAN BIG
                       return fregnap_smbus_word_swapped;
                default:
                                       /* everything else is not supported */
                       break:
       else if (config. >val.bits - # AA config. >reg bits - # AA
                12c_check_functionality(12c -adapter.
                                         I2C_FUNC_SHBUS_BYTE_DATA())
                return freenes sobus byte
       return ERR_PTR(-ENOTSUPP);
```

regmap_i2c

```
static int regrap the read(void *context.
static struct regnap_bus regnap_12c = {
                                                                                                                     const word 'req. size t req size.
          write = regmap 12c write.
                                                                                                                      world "wal, stre, t vol. stre)
           gather_write = regmap_12c_gather_write.
                                                                                                     struct device they a contact
          read = regmap_t2c_read.
                                                                                                     struct tlc_client "tlc - te_tlc_client(dev);
          .reg_format_endian_default = REGMAP_ENDIAN_BIG,
                                                                                                     struct $2c_mag afer[2]:
                                                                                                     int cet:
          val format endian default - REGNAP ENDIAN BIG.
                                                                                                     sfer(*) addr - $2e-addr:
                                                                                                     xfer[0] Flags - 0;
                                                                                                     xfer[8].len = reg stre:
                                                                                                     xfer(*).buf - (vold *)reg;
                                                                                                     sfer[1] addr = tlc -addr:
                                                                                                     xfer[1] flags - IIC # 80
                                                                                                     sfer[1] len - val stre:
                                                                                                     sfer[1] buf - val:
                                                                                                     ret - 12c_transfer(12c -adapter, afer, 2);
                                                                                                     if (ret - 2)
                                                                                                            return 8
                                                                                                     else of (ret = 0)
                                                                                                            return ret;
                                                                                                     -the
                                                                                                            return - 810:
```

- mfd_add_devices()
 - mfd_add_device()
 - platform_device_add()
 - device_add()

Matching

/arch/arm64/boot/dts/xilinx/zynqmp-zcu100-recvC.dts

Device driver structure

Driver registration/unregistration

```
#define module_12c_driver( 12c_driver) \
       module driver( 12c driver, 12c add driver,
                       (2c del driver)
#define module_driver( driver, register, _unregister, ...)
static int __init __driver##_init(void)
                                                                           int __init tps65086_driver_init(void)
       return register(&( driver) , ## VA ARGS ); \
                                                                            return i2c add driver(&(tps65086 driver));
                                                                     module_init(tps65086_driver_init):
module_init( driver##_init); \
                                                                      tatic void __exit tps65086_driver_exit(void)
static void exit driver## exit(void) \
                                                                            i2c_del_driver(&(tps65086_driver));
       _unregister(%( driver) , ## VA ARGS ); \
                                                                      nodule_exit(tps65086_driver_exit);;
module exit( driver## exit):
```

i2c_add_driver() / i2c_del_driver()

```
#define 12c add driver(driver) \
           12c register driver(THIS MODULE, driver)
tot the register driver(struct redule namer, struct the driver ndriver)
       net rest
       J'4 Can'd register soft! offer driver model tott 4/.
       of chief on the registered;
              return EAGAIN.
       /* mid the driver to the list of the drivers to the driver cars A
       driver odriver maker - muner
       driver odriver bus - Hill bus type.
       DEPT_LIST_MEADCHDriver -- chicags)
       J't Men registration returns, the driver care
        * will have called probe() for all metching-but unbound devices.
       res - driver_register(idriver -driver
               defines dex.
       ar debug driver (to) repolated in driver odriver name);
       Us for each decid/terr . process new driver
       ceture 4.
EXPORT SYMBOL (2c register driver)
```

```
    driver_register()

    bus add driver()

    driver attach()

    driver attach()

    driver probe device()

    really probe()

static int really probe(struct device 'dev, struct device driver 'drv)
     if (dev->bus->probe)
            ret = dev->bus->probe(dev);
            if (ret)
                    goto probe failed:
     } else if (drv->probe)
            ret = drv-:probe(dev);
            if (ret)
                    goto probe_fatled;
```

probe is assigned in i2_bus_type so bus->probe will be called

```
    driver_unregister()

    Bus remove driver()

    Driver detach()

    Device_release_driver_internal()

    device release driver()

static void device release driver(struct device "dev. struct device "parent)
      struct device_driver "drv;
       if (dev->bus 65 dev->bus->renove)
              dev->bus->renove(dev):
              dry->renove(dev):
```

i2c_device_probe() / i2c_device_remove()

```
static int ile device probe(struct device "dev)
        struct the client
                                "client = ile_verify_client(dev);
        struct the driver
                                *driver:
        int status:
       if (|driver->id_table ss
           (ile acpi match device(dev->driver->acpi match table, client) AA
           (ile_of_match_device(dev->driver->of_match_table, client))
               return - ENCOEV:
        if (driver->probe_new)
                status = driver->probe new(client):
        else if (driver-probe)
                status = driver->grabe/client
                                       (Se match id(driver-)id table, client))
        else
                status - - EINVAL
        if (status)
                poto err detach pa domain:
```

```
static int i2c_device_remove(struct device "dev)
        struct i2c_client
                                "client = i2c_verify_client(dev);
        struct ite driver
                                *drlver:
        int status = 0;
        if (|client || |dev->driver)
               return 0:
        driver = to_i2c_driver(dev->driver);
        if (driver->remove)
               dev_dbg(dev, "renove\n");
               status = driver->renove(client);
       dev pm domain detach(%client->dev. true):
       dev on clear wake ire(sclient >dev):
       device init wakeup(telient->dev. false):
       return status:
```

- process new driver() i2c do add adapter() i2c detect() i2c detect address() i2c_default_probe() i2c check functionality() i2c smbus xfer() driver->detect() i2c new device() device_register(&client->dev) device add() driver->attach_adapter()
- process removed driver() i2c do del adapter() i2c unregister device() device_unregister(&client->dev) device del()

Thank you for your patience