Education of Embedded Systems Programming in C and Assembly Based on ARM's Cortex-M Microprocessors

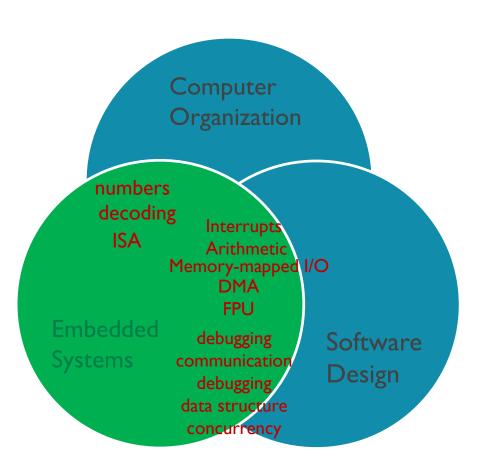




Yifeng Zhu, Libby Professor University of Maine

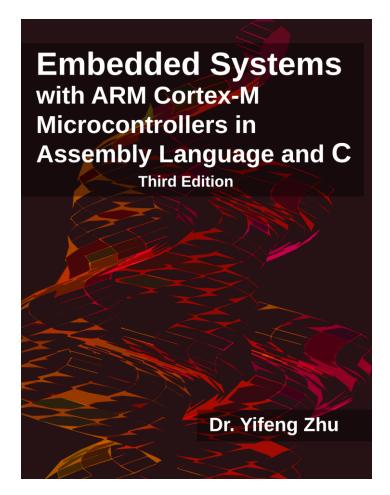
Webinar Series
October 2018

Role of Embedded Systems: Lays foundation



- Laying foundation in curriculum:
 - Computer organization & architecture
 - Operating systems
 - Software design & algorithms
 - Senior project design
- Body of Knowledge (IEEE/ACM Computer Engineering Curricula 2016)
 - Number systems and data encoding
 - Instruction set architecture
 - Relevant tools, standards and/or engineering constraints
 - Input/output interfacing and communication
 - Interrupts, timers, waveform generation
 - Implementation strategies for complex embedded systems
 - Computing platforms for embedded systems

Textbook



- 1. See a program running
- 2. Data representation
- 3. ARM instruction set architecture
- 4. Arithmetic and logic
- 5. Load and store
- 6. Branch and conditional execution
- 7. Structured programming
- 8. Subroutines
- 9. 64-bit data processing
- 10. Mixing C and assembly
- 11. Interrupt
- 12. Fixed-point & floating-point arithmetic
- 13. Instruction encoding and decoding
- 14. General-purpose I/O
- 15. General-purpose timers
- 16. Stepper motor control
- 17. Liquid-crystal display (LCD)
- 18. Real-time clock (RTC)
- 19. Direct memory access (DMA)
- 20. Analog-to-digital converter (ADC)
- 21. Digital-to-analog converter (DAC)
- 22. Serial communication protocols
- 23. Multitasking
- 24. Digital signal processing

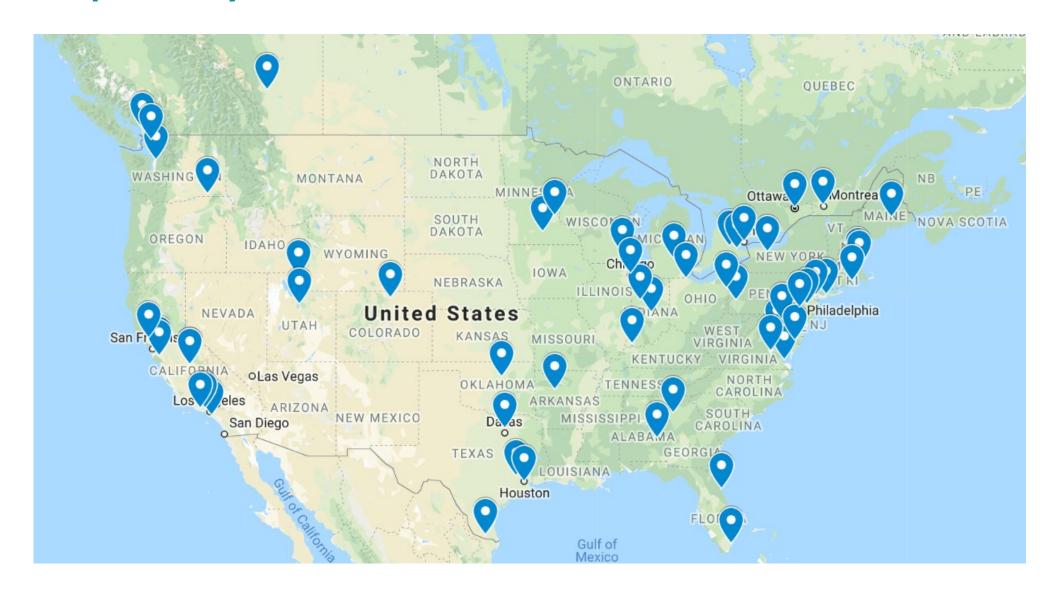
Complete instructor's resource:

- Lecture slides, quizzes and exams, tutorials, lab handouts and solutions (pre-lab, in-lab, and post-lab), solutions to end-of-chapter exercises
- Bare-metal programming at the register level without using any API libraries
- Line-by-line translation from C to ARM assembly
- Strike the balance between theoretical foundations and technical practices
- Using flowcharts as a reading guide for processor datasheets
- Online YouTube tutorials (received over 866,000 minutes of watch time)
- Adopted by over 80 universities

738 pages, \$69.50



Adopted by universities in US & Canada



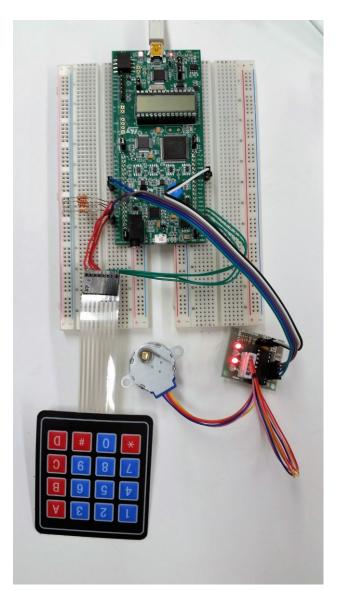
My approach of teaching

- 1. Using modern platforms and tools
- 2. Bare-metal programming
- 3. Structured programming in Assembly
- 4. Lab-centered learning
- 5. Online tutorials

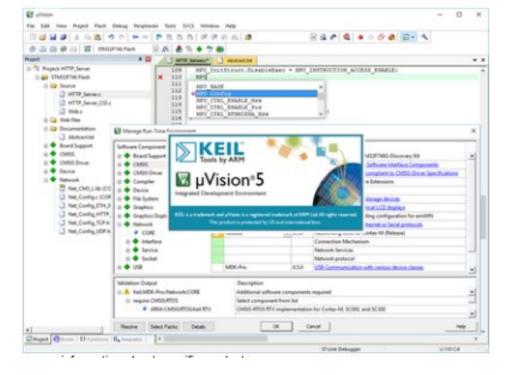
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Cheap and engaging platform and tools







Reference manual & datasheet



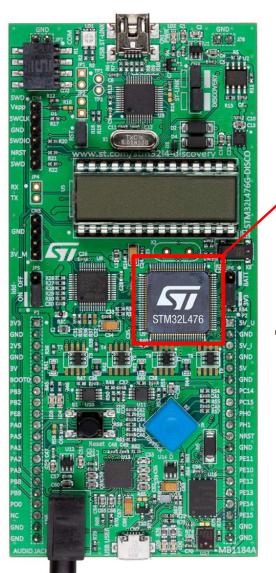
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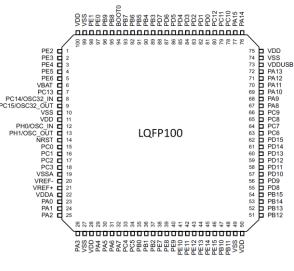
Lab in a box, \$25

Selecting a Platform: Hardware Component

- Low cost
 - ~\$25 each
- Hands-on experiences
 - develop and test real systems
- Rewarding and engaging
 - immediately enjoy the fruit of labor
- Convenient
 - mobile lab without time and location constrains
- Versatile
 - pins are extended for easy access



STM32L476G ARM Cortex-M4F



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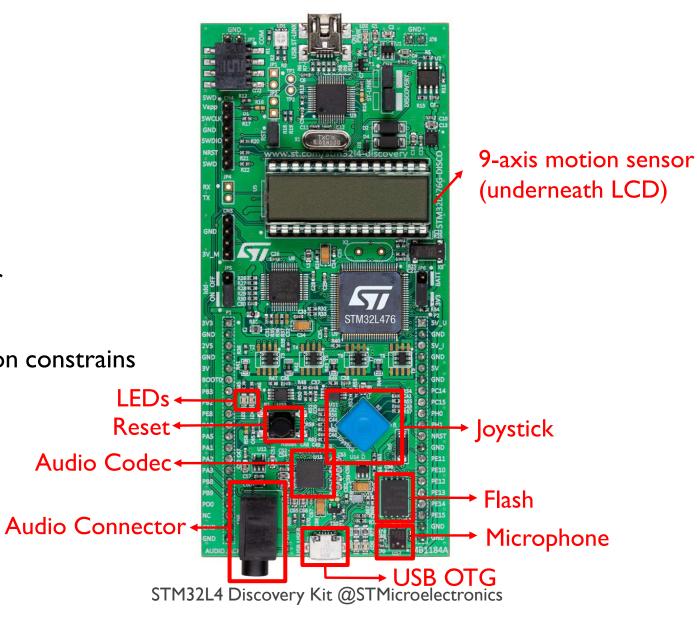


Integrated ST-Link/V2 programming and debugging tool

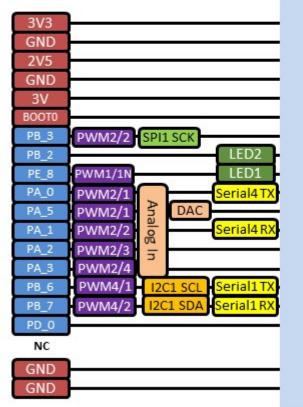


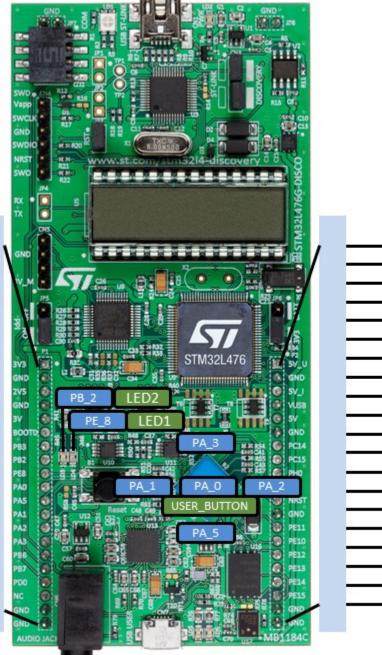
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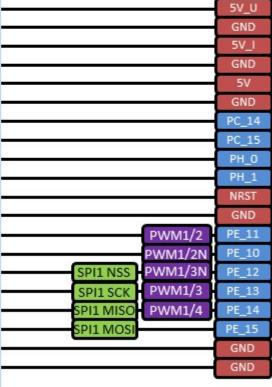
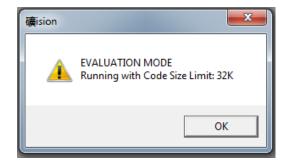


Image from mbed.com

Selecting a Platform: Software Component

Debug Keil uVision Development Tools - 0 X 🔣 C:\Users\zhu\Google Maine EDU\Dropbox\ECE271\Kits\STM32L4_Documents\Labs\Lab_01_LED\STM32L476G_LED_Assembly\project.uvprojx - 😹 sion File Edit View Project Flash Debug Peripherals Tools SVCS Window Help LOAD Target 1 startup_stm32I476xx.s Project: project 77 INCLUDE core cm4 constants.s : Load Constant Definitions □ 3 Target 1 78 INCLUDE stm321476xx constants.s Build 79 AREA main, CODE, READONLY main.s EXPORT ; make main visible to linker startup_stm32l476xx.s ENTRY stm32I476xx_constants.s main PROC CMSIS 85 : Enable the clock to GPIO Port B LDR r0, =RCC BASE LDR r1, [r0, #RCC AHB2ENR] ORR r1, r1, #RCC AHB2ENR GPIOBEN **Breakpoints** STR r1, [r0, #RCC AHB2ENR] ; MODE: 00: Input mode, 01: General purpose output mode 93 10: Alternate function mode, 11: Analog mode (reset state) LDR r0, =GPIOB BASE 95 LDR r1, [r0, #GPIO MODER] EOR r1, r1, #(0x03<<(2*2)) 97 ORR r1, r1, # (1<<4) STR r1, [r0, #GPIO_MODER] 99 100 LDR r1, [r0, #GPIO ODR] Build Output **д**

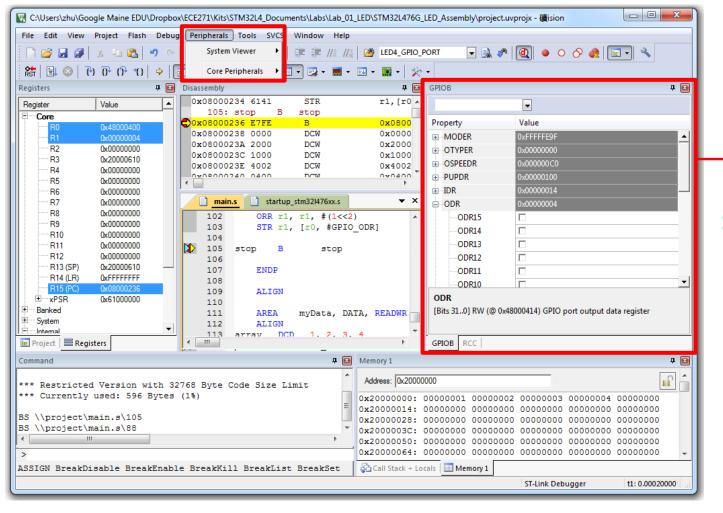
ST-Link Debugger



But this has not been a problem.

Selecting a Platform: Software Component

Keil uVision Development Tools



Monitor or modify peripheral registers

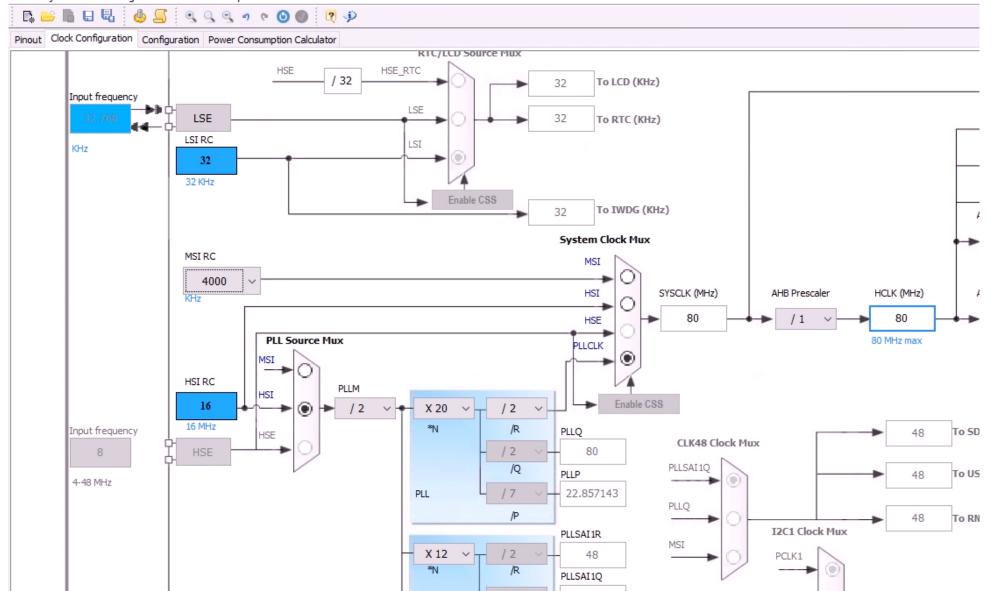
Students found this very helpful!

Free version limited the code size to 32 KB. But this has not been a problem.

STM32Cube

STM32CubeMX Untitled*: STM32L476VGTx

File Project Clock Configuration Window Help



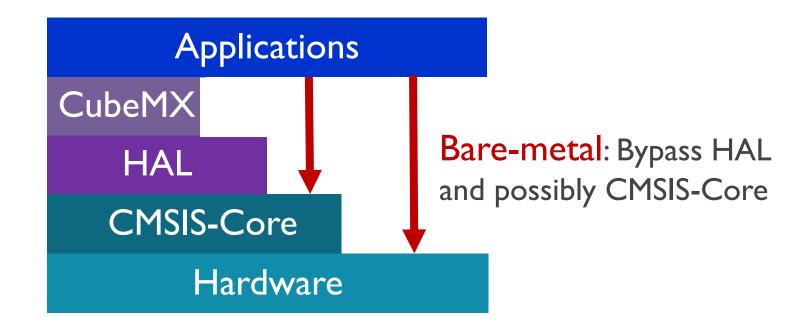
Nice clock tree visualization

My approach of teaching

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Teach at which level?

- Visual wizard tools (such as STMCubeMX)
- HAL (Hardware Abstraction Layer) libraries
- Bare-metal



HAL Level

```
; Initialize the Red LED pin (PB.2)
static GPIO_InitTypeDef GPIO_InitStruct;
GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;
GPIO_InitStruct.Pull = GPIO_PULLUP;
GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_VERY_HIGH;
GPIO_InitStruct.Pin = GPIO_PIN_2;

HAL_GPIO_Init(GPIOB, &GPIO_InitStruct);
HAL_GPIO_TogglePin(LED4_GPIO_PORT, LED4_PIN);
```

Pros

- Simplify implementation
- Better portability
- Many examples

Cons

- Very complex to understand
- Cannot meet students' curiosity

```
void HAL_GPIO_Init(GPIO_TypeDef *GPIOx, GPIO_InitTypeDef *GPIO_Init) {
   uint32_t position = 0x00;
   uint32_t iocurrent = 0x00;
   uint32_t temp = 0x00;
   ...
}
```

Bare-Metal Level in C

```
#define LED PIN 2
// GPIO Mode: Input(00), Output(01), AlterFunc(10), Analog(11, reset)
GPIOB->MODER &= ~(3<<(2*LED PIN)); // Clear by using mask
GPIOB->MODER |= 1<<(2*LED PIN); // Set as Output
// GPIO Speed: Low speed (00), Medium speed (01), Fast speed (10), High speed (11)
GPIOB->OSPEEDR &= ~(3<<(2*LED PIN)); // Clear by using mask
GPIOB->OSPEEDR |= 2<<(2*LED PIN); // Fast speed
// GPIO Output Type: Output push-pull (0, reset), Output open drain (1)
GPIOB->OTYPER &= ~(1<<LED PIN); // Push-pull
// GPIO Push-Pull: No pull-up pull-down (00), Pull-up (01), Pull-down (10),
Reserved (11)
GPIOB->PUPDR &= ~(3<<(2*LED_PIN)); // No pull-up, no pull-down

    Only 6 lines of code

// Toggle up the LED
GPIOB->ODR ^= 1 << LED PIN;

    Focus on directly interfacing with hardware.

                                     Do not use any libraries!
```

Bare-Metal Level in Assembly

Bare-metal level programming helps learning assembly programming

Set Pin B.2 as GPIO output

```
#define LED_PIN 2

// GPIO Mode: Input(00), Output(01), AlterFunc(10), Analog(11, reset)

GPIOB->MODER &= ~(3<<(2*LED_PIN));

GPIOB->MODER |= 1<<(2*LED_PIN); // Output(01)</pre>
```

C implementation

Translate naturally

```
LED_PIN EQU 2

LDR r0, =GPIOB_BASE

LDR r1, [r0, #GPIO_MODER]

EOR r1, r1, #(0x03<<(2*LED_PIN))

ORR r1, r1, #(1<<LED_PIN)

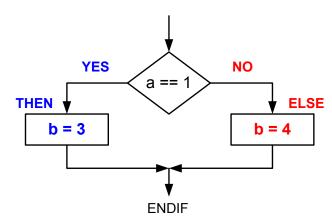
STR r1, [r0, #GPIO_MODER]
```

My approach of teaching

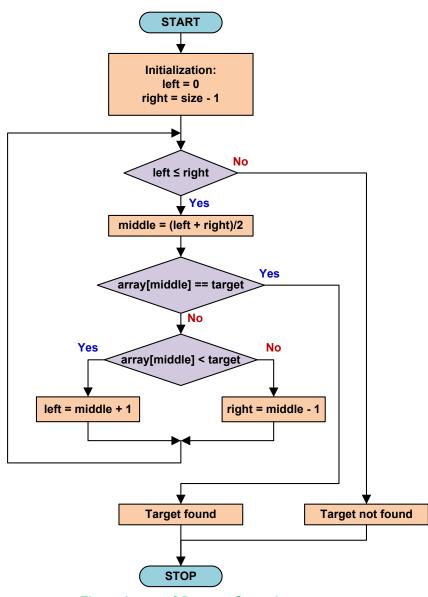
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- Assembly is not a structured programming language
 - No high-level control constructs to avoid GOTOs (unconditional branches)
 - Difficulty to learn and program
 - Prone to create spaghetti codes
- My approaches
 - Using flowcharts
 - Leveraging C programs

- Using flowcharts
 - Separate program structuring from code writing

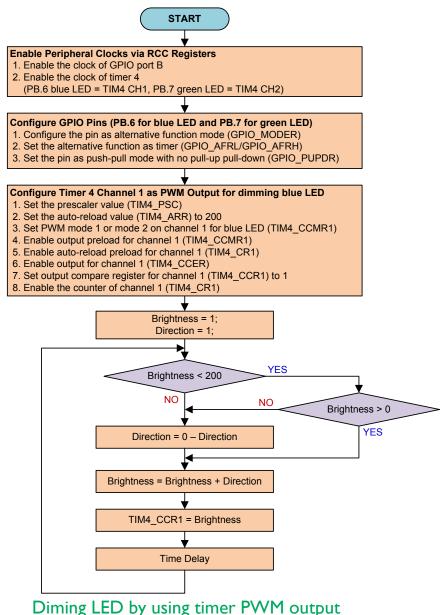


- Using flowcharts
 - Separate program structuring from code writing



Flowchart of Binary Search

- Using flowcharts
 - Separate program structuring from code writing



Using flowcharts in all labs

Write down your last name, and complete the following table.

1	2	3	4	5	6		
						F H K B	BAR3
						G M COLON	BAR2
					17/15	E Q N C	BAR1
(INDAIL)		(LELLE)				D DP	BARO

21 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0

Your Last Name: ______ (First Six Characters)

	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	- /	6	5	4	3	2	1	U
LCD_RAM[0]	4E	4G	3M	3B		6 G	5M	5 B	1M	1B					6 E		3 E	3 G	2M	2B			6 B	6M		2E	2 G	1E	1G			
									\sim									~			L											
LCD_RAM[1]	\otimes	\otimes			\bowtie	\otimes			\otimes	\bigotimes	XX	\boxtimes		\otimes	\bigotimes	X	\otimes	\otimes	XX		1								5 E	5 G	4M	4B
	88		XX	XX		883	***	XX	88	XX	XX	XX	X	88	**	XX	88	88	**	XX	_											
LCD_RAM[2]	4D	4F	3C	3A		6F	5C	5A	1C	1A			L		6D		3D	3F	2C	2A			6A	6C		2D	2F	1D	1F			
LCD_RAM[3]		\otimes			X		X		\otimes		\otimes	X		\bigotimes		\bowtie		\otimes	XX			_							5D	5 F	4C	4A
	88	888	Χ×	XX	X X	883	××		×	883	XX	XX	X	88		XX	88	88	XX	XX		_										_
LCD_RAM[4]	4P	4Q	3 Col	3K	_	6Q	3 Bar	5K	Col	1K			L		6 P		3P	3Q	2 Col	2K			6K	1 Bar		2P	2Q	1P	1Q			
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LCD_RAM[5]						\otimes			\otimes	\bigotimes	\gg	\boxtimes		\otimes				\bigotimes	XX			_							5 P	5 Q	4 Col	4K
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LCD_RAM[6]	4N	4H	DP	3J		6H	2 Bar	5 J	DP	1J			L		6N		3N	3H	2 DP	2J			6J	0 Bar		2N	2H	1N	1H			
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LCD_RAM[7]			\bowtie		\bowtie	\bigotimes	₩	X	\otimes	\otimes	\bowtie	\bowtie	X	\bigotimes		\bowtie		\otimes	₩	X									5N	5H	4 DP	4J
	88	888	\mathbb{X}	X	188	883	888	XX	\otimes	88	XX	怒	\mathbb{X}	88	888	X	\otimes	88	$8\!\!\times\!\!$	X												

LCD Clock Initialization

 Disable RTC clock protection (RTC and LCD share the same clock). Write 0xCA and 0x53 to RTC WRP register to unlock the write protection

START

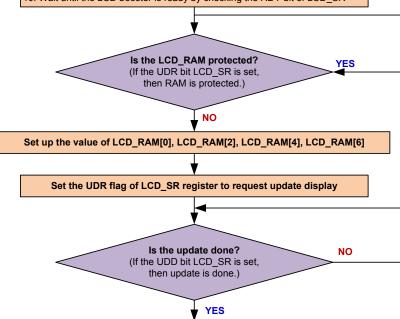
- 2. Enable LSI clock (RCC_CSR)
- 3. Select LSI as LCD clock source (RCC CSR RTCSEL field)
- 4. Enable LCD/RTC clock (RCC_CSR RTCEN field)

Configure LCD GPIO Pin as Alternative Functions

- 1. Enable the clock of GPIO port A, B, and C
- 2. Configure Port A Pin 1, 2, 3, 8, 9, 10, and 15 as AF 11 (0x0B)
- 3. Configure Port B Pin 3, 4, 5, 8, 9, 10, 11, 12, 13, 14, and 15 as AF 11 (0x0B)
- 4. Configure Port C Pin 0, 1, 2, 3, 6, 7, 8, 9, 10, and 11 as AF 11 (0x0B)

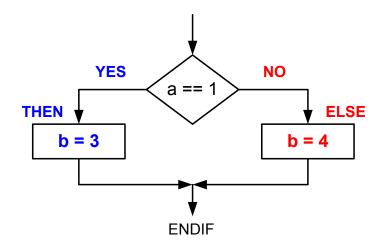
LCD Configuration

- 1. Configure BIAS[1:0] bits of LCD CR and set the bias to 1/3
- 2. Configure DUTY[2:0] bits of LCD CR and set the duty to 1/4
- 3. Configure CC[2:0] bits of LCD_FCR and set the contrast to max value 111
- Configure PON[2:0] bits of LCD_FCR and set the pulse on period to 111, i.e., 7/ck_ps. A short pulse consumes less power but might not provide satisfactory contrast.
- 5. Enable the mux segment of the LCD_CR
- 6. Select internal voltage as LCD voltage source
- 7. Wait until FCRSF flag of LCD SR is set
- 8. Enable the LCD by setting LCDEN bit of LCD CR
- 9. Wait until the LCD is enabled by checking the ENS bit of LCD SR
- 10. Wait until the LCD booster is ready by checking the RDY bit of LCD SR



STOP

- Using flowcharts
 - Separate program structuring from code writing
- Leveraging C programs
 - Relate an unstructured to a structured
 - C vs. Assembly line-by-line comparison



C Program	Assembly Program
	; r1 = a, r2 = b
if (a == 1)	CMP r1, #1
b = 3	BNE else
else	then MOV r2, #3
b = 4;	B endif
	else MOV r2, #4
	endif

Methods of teaching structured programming in assembly

- Using flowcharts
 - Separate program structuring from code writing
- Leveraging C programs
 - Relate an unstructured to a structured
 - C vs. Assembly line-by-line comparison
 - Mixing C and assembly

C calling assembly functions

```
int main(void) {
    ...
    s = sum(1,2,3,4);
    ...
}
sum PROC
ADDS r0,r0,r1
...
BX LR
ENDP
```

Assembly calling C functions

```
main PROC
...
BL sum
return a+b+c+d;
}
ENDP
```

Inline assembly

```
int sum(...) {
   __asm {
     ADD t, a, b;
     ...
   }
   ...
}
```

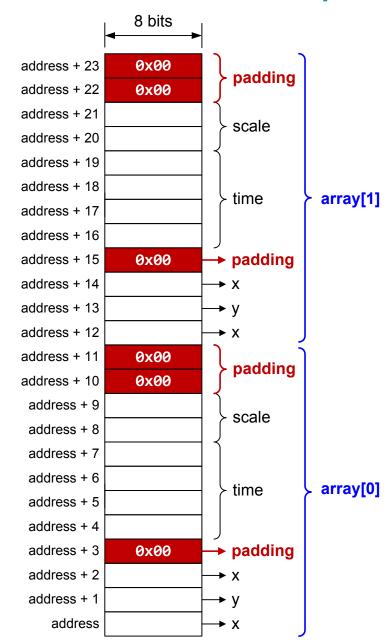
Extra benefits: Assembly helps to some difficult C concepts

Structure padding

```
struct Position {
  char x;
  char y;
  char x;
  int time;
  short scale;
} array[10];
```

```
address of array[0].time = array + offset
```

When assembly access a variable in a C structure, the address offset has to take padding into consideration



Extra benefits: Assembly helps to some difficult C concepts

• *static* variables

C Program	Assembl	y Program
<pre>int foo();</pre>	×	AREA myData, DATA ALIGN // Reserve space for x DCD 5 AREA static_demo, CODE EXPORTmain ALIGN ENTRY
<pre>int main(void) { int y; y = foo(); // y = 6 y = foo(); // y = 7 y = foo(); // y = 8 while(1); }</pre>	main	BL foo ; r0 = 6 BL foo ; r0 = 7 BL foo ; r0 = 8
<pre>int foo() { // local static variable // x is initialized only once static int x = 5; x = x + 1; return(x) }</pre>	foo	PROC ; load address of x LDR r1, =x ; load value of x LDR r0, [r1] ADD r0, r0, #1 ; save value of x STR r0, [r1] BX lr ENDP END

Extra benefits: Assembly helps to some difficult C concepts

volatile variables

```
Main Program (main.c)
                                      Interrupt Service Routine (isr.s)
volatile unsigned int counter;
                                           AREA ISR, CODE, READONLY
extern void task();
                                           IMPORT counter
extern void SysTick_Init();
                                           ENTRY
int main(void) {
                                      SysTick_Handler PROC
   counter = 10;
                                           EXPORT SysTick Handler
   SysTick Init();
                                           LDR r1, =counter
   while(counter != 0); // Delay
                                           LDR r0, [r1]; load counter
                                           SUB r0, r0, #1 ; counter--
   // continue the task
                                           STR r0, [r1]; save counter
   . . .
   while(1);
                                           BX LR ; exit
                                           ENDP
                                           END
```

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Lab modules

Covering both fundamental and advanced topics

- Lower level courses
 - Push button and light up LEDs
 - LCD display driver
 - Interfacing with keypad
 - Stepper motor control
 - SysTick
 - RTC
 - PWM (diming LED, servo motors)
 - Timer input capture (Ultra sonic distance sensor)
 - ADC (potentiometer, infrared distance sensing)
 - DAC (music synthesizing)



- **External Interrupts**
- UART (Bluetooth hc-05, **ESP8266**)
- 12C (temperature sensor, OLED display)
- SPI (gyro, accelerometer, nRF24L01)
- RGB LED strip (WS2812)
- ADC

Polling,

DMA

Interrupt, •

- **CODEC** and Mic
- **CRC**









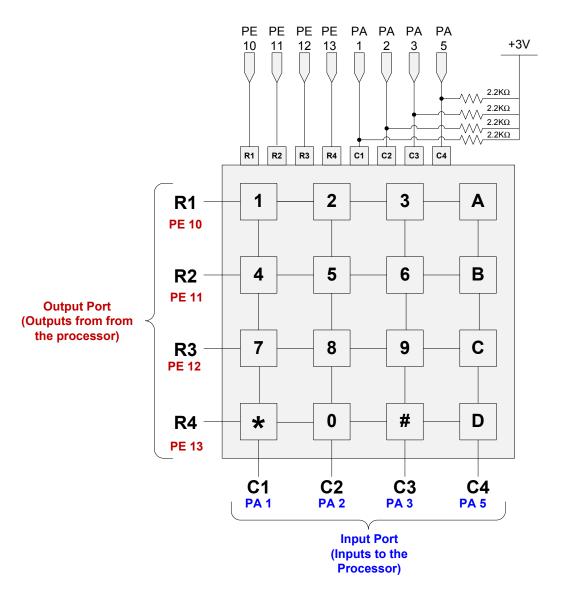






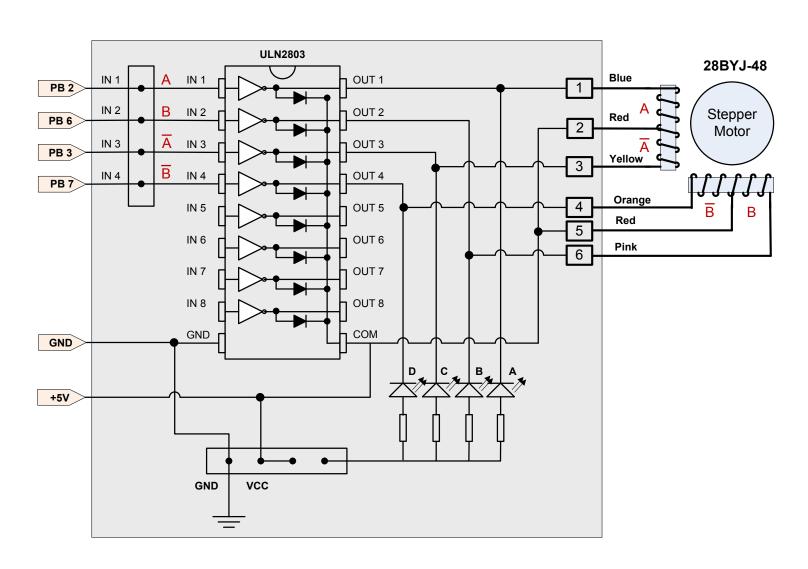
Example Lab: Digital Inputs





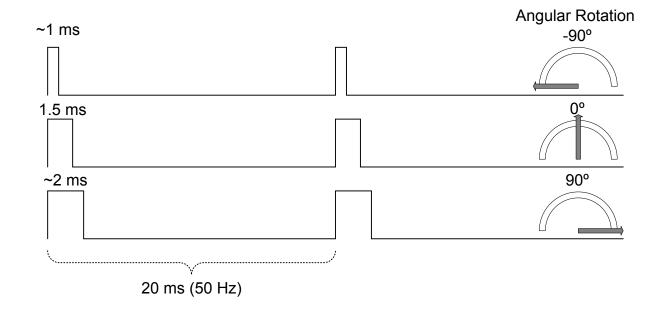
Example Lab: Digital Outputs





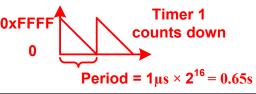
Example Lab: Timer PWM output

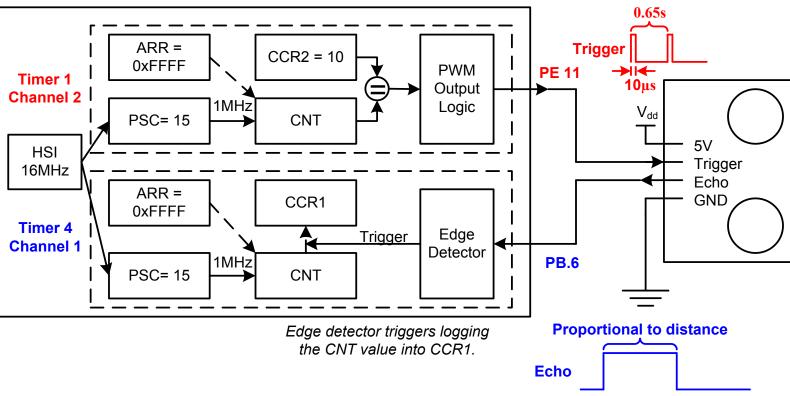




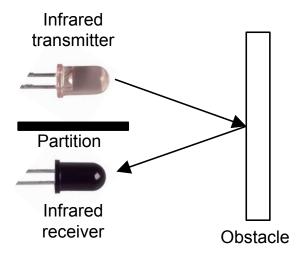
Example Lab: Ultrasonic Distance Measurement

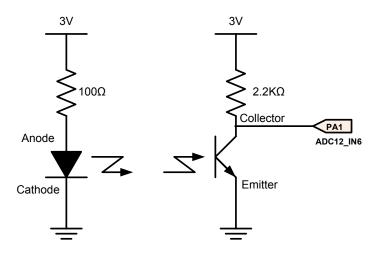


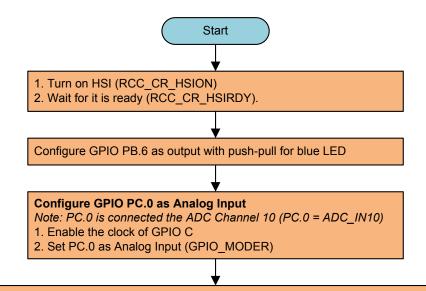




Example Lab: ADC







Analog to Digital Converter 1 (ADC1) Setup

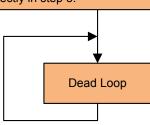
Note: HSI (16MHz) is always used for ADC on STM32L.

- 1. Turn on the ADC clock (RCC APB2ENR ADC1EN)
- 2. Turn off the ADC conversion (ADC1->CR2)
- Set the length of the regular channel sequence to 1 since we only perform ADC in Channel 10. (L[4:0] bits of register ADC1->SQR1)
- 4. Set Channel 10 as the 1st conversion in regular sequence (SQ1[4:0] bits of register ADC1->SQR5)
- 5. Configure the sample time register for channel 10 (SMP10[2:0] bits of register ADC1->SMPR2)
- 6. Enable End-Of-Conversion interrupt (EOCIE bit of register ADC1->CR1)
- 7. Enable continuous conversion mode (CONT bit of register ADC1->CR2)
- 8. Configure delay selection as delayed until the converted data have been read (DELS[2:0] bits in register ADC1->CR2)
- 9. Enable the interrupt of ADC1_IRQn in NVIC
- 10. Configure the interrupt priority of ADC1_IRQn
- 11. Turn on the ADC conversion (ADON bit of register ADC1->CR2)

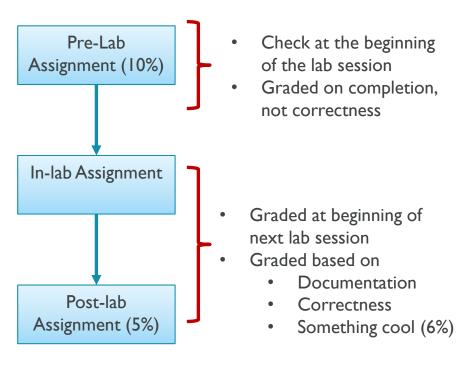
Note: Make sure that we should write to CR2 register before the next step since SWSTART cannot be updated if ADC is off.

12. Start the conversion of the regular channel (ADC_CR2_SWSTART)

Note: If SWSTART only performs one conversion, then it is very likely that your code did not set up the delay correctly in step 8.



Lab Components



Points	Requirements	Poor	Fair	Good	İ
2	Completion of Pre-lab Assignments	0	1	2	
	Poor: absent from lab or does not complete pre-lab assignment				
	Fair: complete pre-lab assignment but lacks some details				
	Good: complete pre-lab assignment with details and minimal 90% correct answers				
·					
5	Documentation & Maintainability	Poor	Fair	Good	
	Proper indentations, whitespaces, and blank lines, ample and non-redundant comments	0	0.5	1	
	Completion of readme.txt write-up (status, description of something cool, feedbacks)	0	0.5	1	
	Header description (author, program objectives, pin usage, clock frequency)	0	0.5	1	
	Frequent and correct commits with comments in Gitlab	0	0.5	1	
	Program uses constant symbols defined whenever possible	0	0.5	1	
				Total	
	Functionality & Correctness	Poor	Fair	Good	
	No compilation errors or warnings (except warning L6314W)	0	0.5	1	
	Exhibits all required functionality	0	1	2	
	Concise code (Codes that are unnecessary should be deleted)	0	0.5	1	
	Efficient and robust code	0	0.5	1	

5	Lab Time and Demonstration	Poor	Fair	Good	
	Make good use of lab time (Poor: leave before lab is done; Fair: accomplish a few objectives; Good: completes all objectives)	0	0.5	1	
	Demo as specified by the lab assignment	0	1	2	
	Answer TA's questions clearly and demonstrate thorough understanding	0	0.5	1	
	Complete post-lab assignments	0	0.5	1	
		Total			

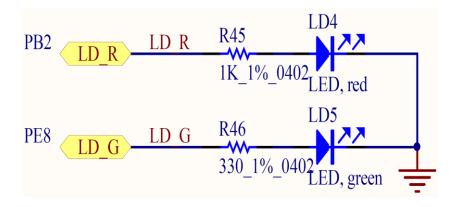
Total

Number of late days

3	Something Cool	Poor	Fair	Good	
	Note: Flashing LED is NOT considered as something cool except Lab 1.	0	1.5	3	

Hands-on Lab #1

Light up an LED in 100% assembly



Pre-Lab Assignment

1. Enable the clock of GPIO Port A (for joy stick), Port B (for Red LED) and Port E (for Green LED)

Register	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	6	8	2	9	2	4	3	2	_	0
AHB2ENR														RNGEN		AESEN			ADCEN	OTGFSEN					GPIOPHEN	GPIOPGEN	GPIOPFEN	GPIOPEEN	GPIOPDEN	GPIOPCEN	GPIOPBEN	GPIOPAEN
Mask																																
Value																																

a. Configure PB 2 as Output

GPIO Mode: Input (00), Output (01), Alternative Function (10), Analog (11, default)

Register	31	30	29	28	22	26	25	24	23	22	21	20	19	18	17	16			13	12	11	10	6	8	7	9	9	4	8	2	1	0
MODER	04614.	MODER [3[1.0]	. 177	۷	7.0	MODER13[1:0]	MODER12[1:0]	17111	7	MODER11[1:0]	MODER 10[1:0]	-170171-	_	MODERS[1.0]	MODER8[1:0]		0754.	MODER/[1:0]	MODERE[1.0]	-		[0.1]CN=CO[N]	MODER4[1:0]			MODERS[1:0]	MODEB2[4:0]	טבתצן	7	MODERI[1:0]		MODERU[1:0]
Mask																																
Value																																

b. Configure PB 2 Output Type as Push-Pull

Push-Pull (0, reset), Open-Drain (1)

Register	31 30 29 28 27 27 26 25 25 23 23 21 20 21 20 19 19	,	14	13	12	11	10	6	8	7	9	2	4	8	2	1	0
OTYPER		OT15	OT14	OT13	OT12	OT11	OT10	ОТ9	ОТ8	OT7		OT5	OT4	OT3	ОТ2	OT1	ОТО
Mask	Reserved																
Value																	

My approach of teaching

- I. Using modern platforms and tools
- 2. Bare-metal programming
- 3. Structured programming in Assembly
- 4. Lab-centered learning
- 5. Online tutorials

YouTube Lectures & Tutorials

Tutorials

- I. Create a project in Keil v5
- Debugging in Keil v5
- 3. Clock configuration of STM32L4 processors
- 4. Printing messages via UART through ST-Link V2.1
- 5. How to fix common errors?

Short Lectures

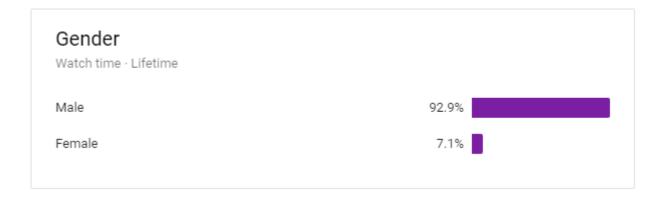
- I. Why do we use Two's Complement?
- Carry and Borrow Flag
- 3. Overflow Flag
- 4. Pointer
- 5. Memory Mapped I/O
- 6. GPIO Output: Lighting up a LED
- 7. GPIO Input: Interfacing a joystick

- 8. Timer: PWM output
- 9. Interrupt Enable and Interrupt Priority
- 10. Interrupts
- II. External Interrupts (EXTI)
- 12. System Timer (SysTick)
- 3. Booting process
- 14. LCD
- 15. Race Conditions



One open challenge: How to get more female students?

Out of 60K subscribers



Summary

- I. Using modern platforms and tools
- 2. Bare-metal programming
- 3. Structured programming in Assembly
- 4. Lab-centered learning
- 5. Online tutorials

For more information

- Send email to <u>Yifeng.Zhu@maine.edu</u> for
 - An exam copy of my book
 - Complete instructor resources: slides, exams, quizzes, solutions, lab handouts & solutions
- My book website: http://web.eece.maine.edu/~zhu/book/
 - Sample labs, lab kit, FAQ
- My YouTube Channel: <u>https://www.youtube.com/channel/UCY0sQ9hpSR6yZobt1qOv6DA</u>

Thank STMicroelectronics for organizing this workshop!