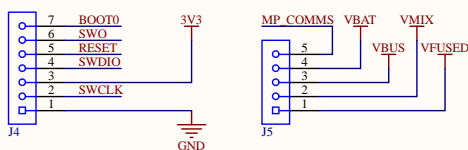
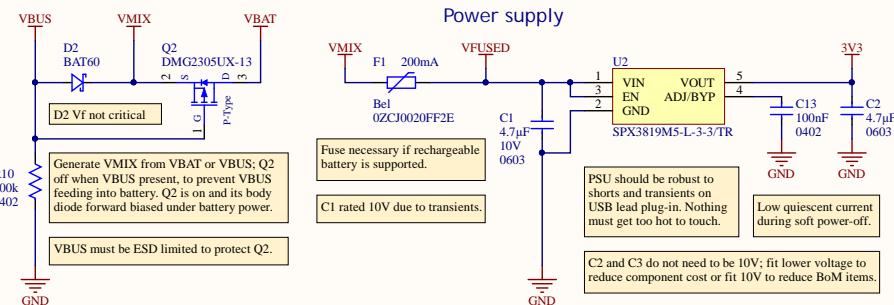
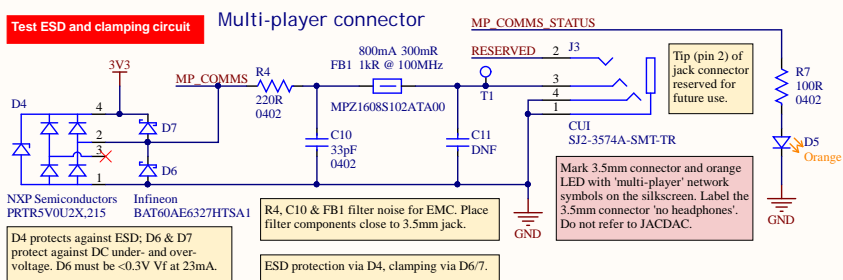


USB shield and GND connected together following ST dev board designs.



Mark the allowable voltage as 3.6-5V.

Built-in a battery socket may be used (e.g. 3xAAA) instead of J6. Rechargeable battery with charging circuit may be used instead.

J6

1

2

M1

M2

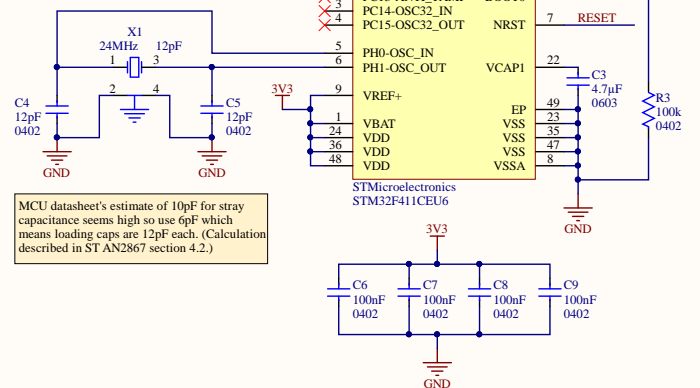
M3

M4

IST

VBAT

When connected to USB the device is always bus-powered and PA9 Vbus sensing is optional, hence DNF. However, if PA9 is to be used it cannot be connected directly to Vbus even though it is 5V tolerant; it must be 4V at most (see AN4879). ST recommend a potential divider which generates between 0.7Vdd and 4V. 200uA is required. ST Forum suggests 4k7/10k.



MCU datasheet's estimate of 10pF for stray capacitance seems high so use 6pF which means loading caps are 12pF each. (Calculation described in ST AN2867 section 4.2.)

SPKR_EN
AUDIO_OUT

Fit an audio filter, amplifier and speaker which can be driven from a 300kHz digital PWM output from the MCU.

Audio circuit not yet implemented

LED backlight current should be 30mA when LCD_BL is high. Power dissipation through 10R will be 9mW.

Manufacturing flashing and testing requires connection to test points/hack-connect, USB and multi-player connectors.

Design
notes

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