

BANK 0 OPERATING VOLTAGE = 3.3V



Place Close to U2



```

2 4 GND
2
1 AVCC_BAR
SPLIT_INST
A1 DXPINCP_VN_0 VCGNDOPREF_V0 BATT_0

```

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C232

CDS_LMAN_SPLIT_NUMBER

R237

R225

R224

C230

C145

PS_SPLIT_NUMBER

PS_SPLIT_NUMBER

PS_SPLIT_NUMBER



R20
X

R44 R42 R18
X X X X

C134
+
C133
+
C132
+
C131
+
C130
+
C129
+
C128
+
C127
+
C126
+

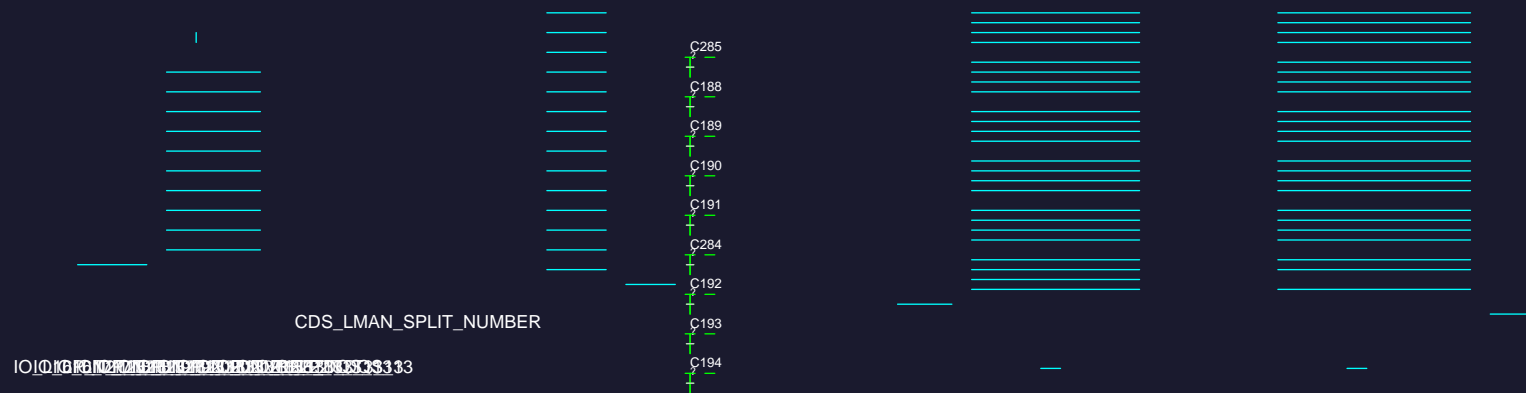
CDS_LMAN_SPLIT_NUMBER

R88 R94
X X

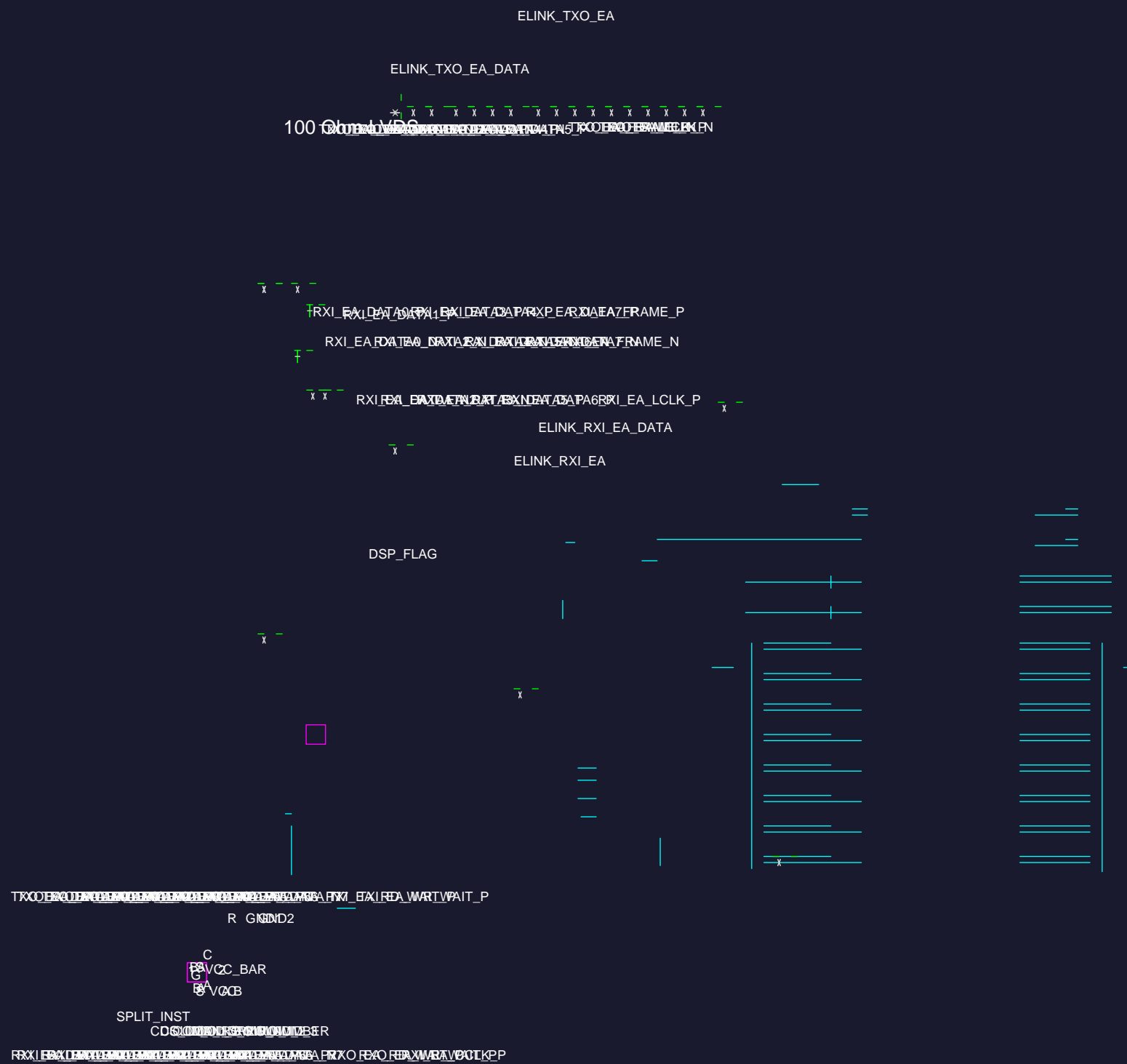
HDMI_DE

R223
X

IO_25[0:10] IO_1[0:10] IO_0[0:10] IO_24[0:10] IO_23[0:10] IO_22[0:10] IO_21[0:10] IO_20[0:10] IO_19[0:10] IO_18[0:10] IO_17[0:10] IO_16[0:10] IO_15[0:10] IO_14[0:10] IO_13[0:10] IO_12[0:10] IO_11[0:10] IO_10[0:10] IO_9[0:10] IO_8[0:10] IO_7[0:10] IO_6[0:10] IO_5[0:10] IO_4[0:10] IO_3[0:10] IO_2[0:10] IO_1[0:10] IO_0[0:10]

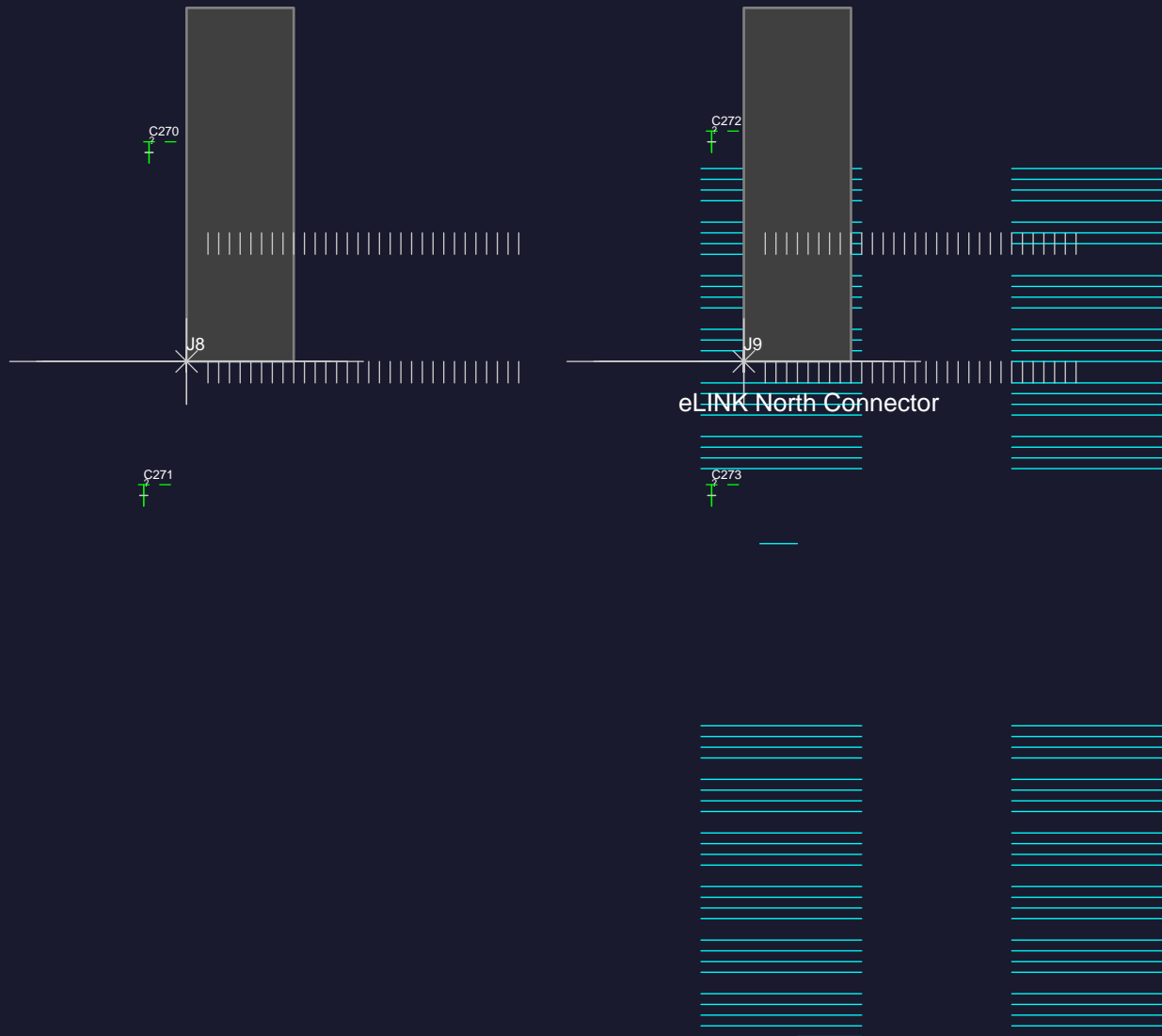


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```
RXKIN_KB = 0;
SPLIT_INST
```



PART NAME
1 G VC2_BAR CONNECTOR_SIGNAL_MODEL

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