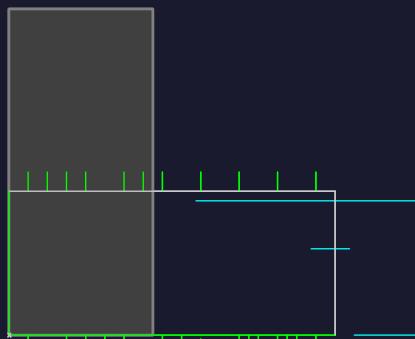




Y - Y -

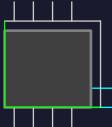
PS POR\_L  
100-ohm Differential



CONNECTOR\_SIGNAL\_MODEL

SPIR\_L

VOLTAGE\_SOURCE\_PIN



C  
GND/CC\_BAR  
AA  
END\_N\_P\_CLKOUT  
P\_BOARD\_RESET\_L  
VDD88 GND  
P1\_USB\_CLK P1\_USB\_NXT

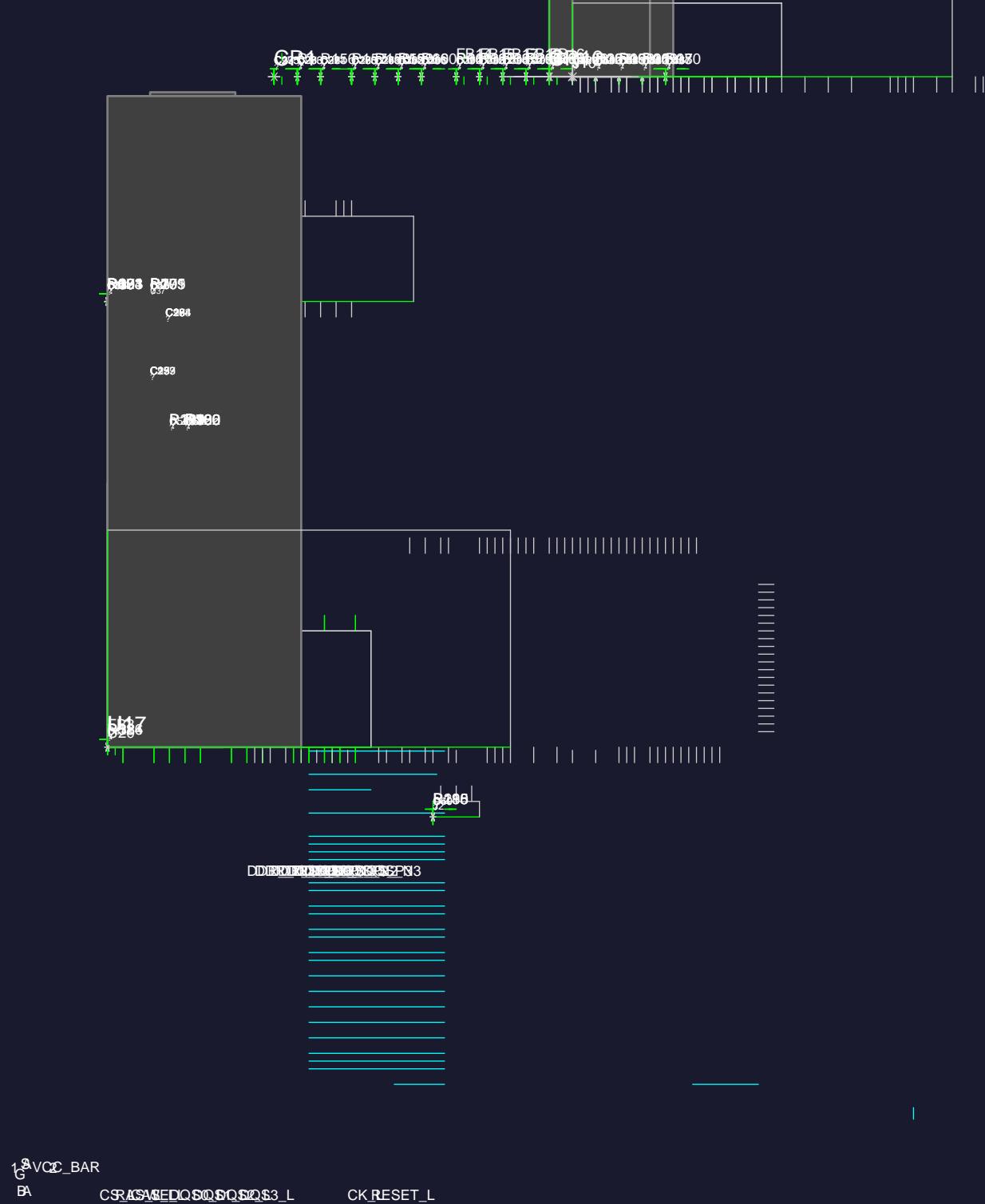


K  
BAvCC\_BAR  
G  
AA  
CECSNDABBD\_BGDND23  
CECSNDABD\_DVDSOHPD\_5V



RST\_L  
Y  
B  
C GND  
BAVCC\_BAR  
G CONNECTOR\_SIGNAL\_MODEL  
BAVOC  
BAVOC









C232

### CDS\_LMAN\_SPLIT\_NUMBER

R237

R225

X  
P224

C230  
C145

PERSONAL INFORMATION FORM NO. 961

BAVCC BAR  
SDP IT INST

SPLIT\_INST

VOCOMM10PS1MO\_VREF\_501



-R20-

R4 R44 R42 R18

### CDS\_LMAN\_SPLIT\_NUMBER

R8B94

HDMI\_DE

C134  
C133  
C132  
C131  
C130  
C129  
C128  
C127  
C126

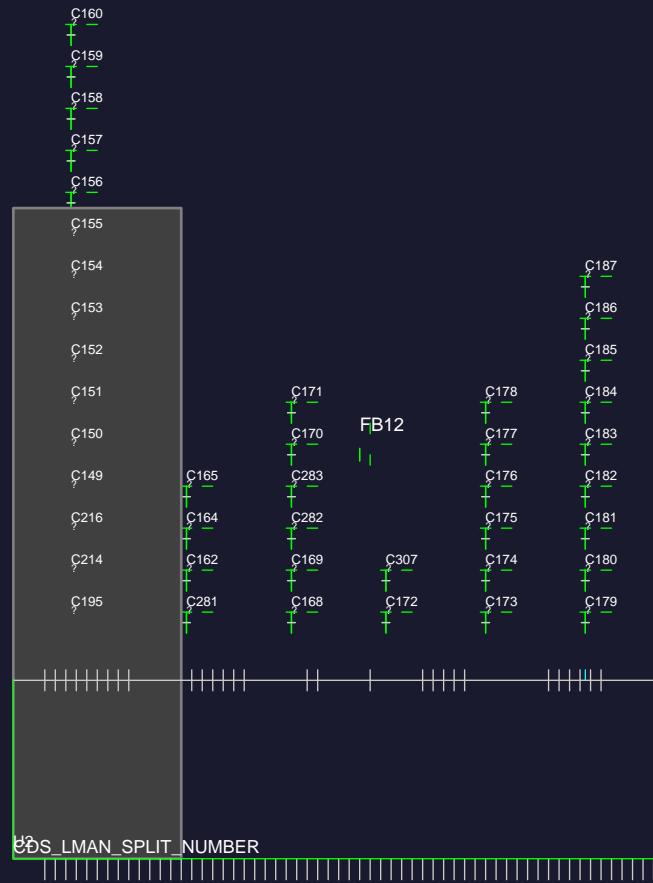
-R223-

BS VCE BAR  
SPLIT\_INST

IO\_0 [04] IO[2] IO[1] IO[0] C[4] C[3] C[2] C[1] C[0] B[4] B[3] B[2] B[1] B[0] A[4] A[3] A[2] A[1] A[0]







2  
1. VCO\_BAR  
SPLIT\_INST  
1 GND

ELINK\_TXO\_EA

ELINK\_TXO\_EA\_DATA

100 Channel ID[6:0] DATA[5:0] RxDONE[1] EPN

RXI\_EA\_DATA[DATA11:DATA0] RXP\_EA[DATA7:FRAME\_P]

RXI\_EA[DATA10:DATA0] RXI\_EA[DATA9:DATA0] RXI\_EA[DATA8:DATA0] RXI\_EA[DATA7:DATA0]

RXI\_EA[DATA6:DATA0] RXI\_EA[DATA5:DATA0] RXI\_EA[DATA4:DATA0] RXI\_EA[DATA3:DATA0] RXI\_EA[DATA2:DATA0] RXI\_EA[DATA1:DATA0] RXI\_EA[DATA0:DATA0] RXI\_EA\_LCLK\_P

ELINK\_RXI\_EA\_DATA

ELINK\_RXI\_EA

DSP\_FLAG

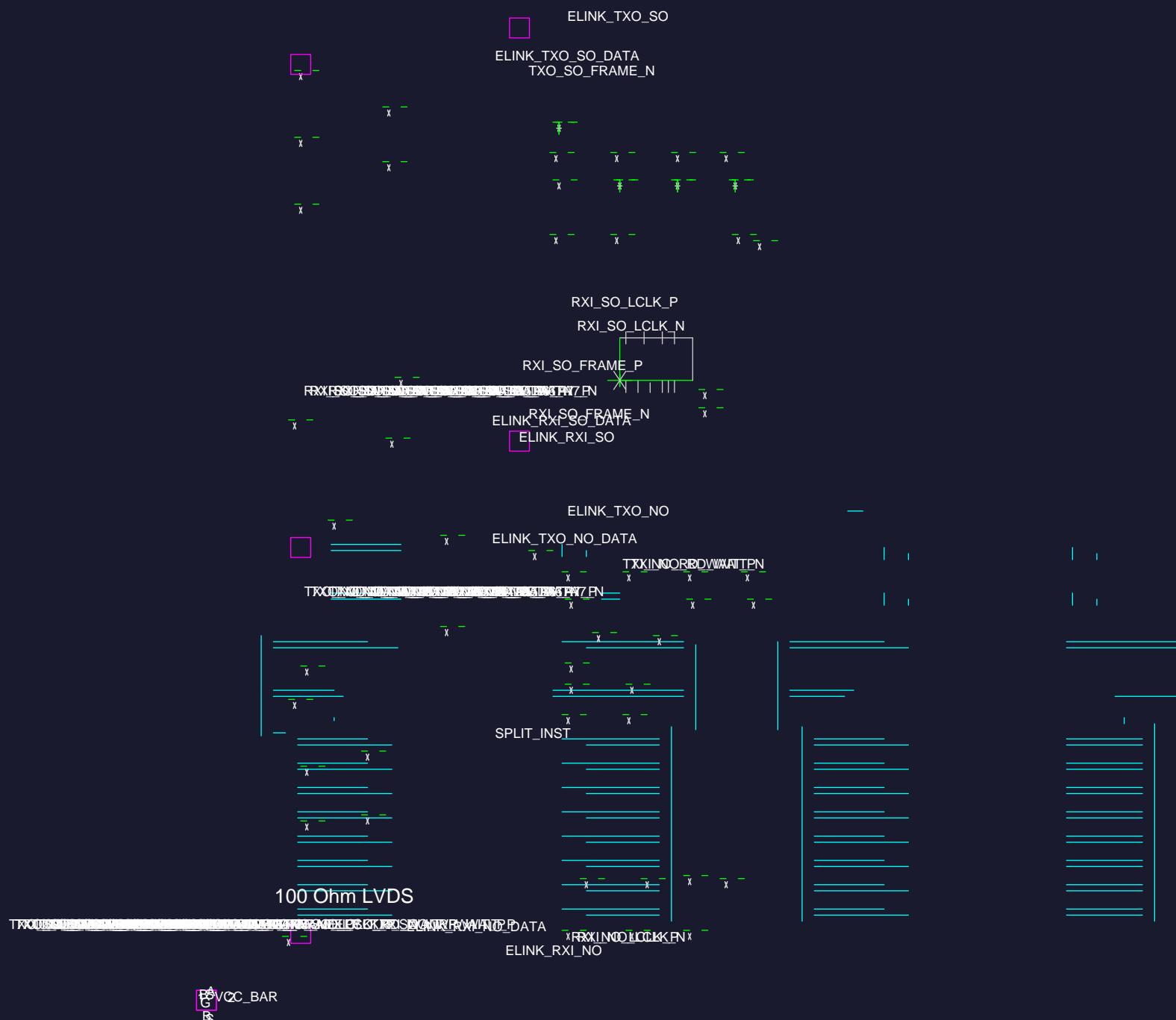
TXO\_EA[DATA11:DATA0] TXO\_EA[DATA10:DATA0] TXO\_EA[DATA9:DATA0] TXO\_EA[DATA8:DATA0] TXO\_EA[DATA7:DATA0] TXO\_EA\_WRT\_WAIT\_P

R\_GND2

C  
EVCC\_BAR  
GA  
BVCCB

SPLIT\_INST  
CDG\_COUNTER\_ID[NUMBER]

RXI\_EA[DATA11:DATA0] RXI\_EA[DATA10:DATA0] RXI\_EA[DATA9:DATA0] RXI\_EA[DATA8:DATA0] RXI\_EA[DATA7:DATA0] RXI\_EA[DATA6:DATA0] RXI\_EA[DATA5:DATA0] RXI\_EA[DATA4:DATA0] RXI\_EA[DATA3:DATA0] RXI\_EA[DATA2:DATA0] RXI\_EA[DATA1:DATA0] RXI\_EA[DATA0:DATA0] RXI\_EA\_WRT\_WAIT\_P



## CDS\_LMAN\_SPLIT\_NUMBER

TXCOM REMOVE FROM POOL WAIT\_P

1<sup>A</sup>VCC\_BAR

## SPLIT\_INST



PART\_NAME  
1\_G\_VCD\_BAR  
CONNECTOR\_SIGNAL\_MODEL

For Lab Test Only  
VMD-CRITICAL-SETTING:88E1318  
PHAD[0]=0



## DISABLED INTERNAL REGULATORS

## CONFIGURATION MAPPING

PHY provides integrated Termination

## CONNECTOR\_SIGNAL\_MODEL

## 100 Ohm Differential

SIG\_NAME

E1E1TRXRXE1TM1DIO  
M0D3M0D2M0D1M0D0PNLBD0D\_1CLK1250DATA(P\_GNENRREG1013  
ARDAN3D0G0D1D1250A2

2 4  
2  
AVCC\_BAR  
BA1 3 VADJ013823  
XXXXXXXXXX XXXXXXXX XXXXXXXX XXXXXXXX XXXXXXXX  
XTAL1AN\_RESET\_N