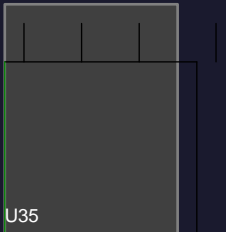
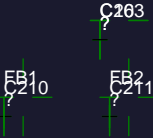


RST_L
Y
B
C
VCC2BAR

GND



CDS_LMAN_SPLIT_NUMBER



BANK 0 OPERATING VOLTAGE = 3.3V



Place Close to U2



DONE_TPROG_B_0 TMSCKT0D0L_0 CFGBVS_0 VCCO_0

2 4 GND

2

A VCC2BAR
LIT_INST



U7

U5

CDS_LMAN_SPLIT_NUMBER

C201

PS_1PS_2PS_3PS_4PS_5MIO9PS_1PS_2PS_3PS_4PS_5PS_6PS_7PS_8PS_9PS_10PS_11PS_12MIO0PS_1PS_2PS_3MIO9

GND

S_NDI_DQ1 C

GND

1 2 3

PIN_TEXT
CONNECTOR_SIGNAL_MODEL
VCC2BAR
SPLIT INST
A



C137

Ç136

Ç135

R178

[illegible]



IO_L0 IO_L1 IO_L2 IO_L3 IO_L4 IO_L5 IO_L6 IO_L7 IO_L8 IO_L9 IO_L10 IO_L11 IO_L12 IO_L13

CDS_LMAN_SPLIT_NUMBER

C285

C188

C189

C190

C191

C284

C192

C193

C194

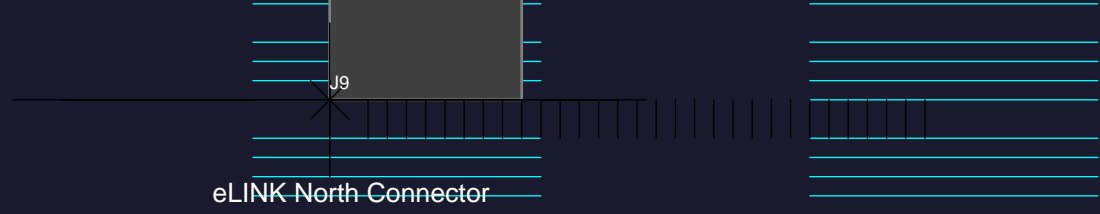
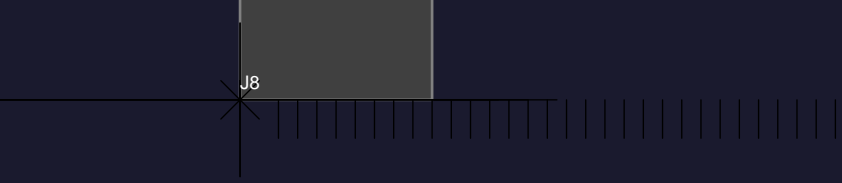






CDS_LMAN_SPLIT_NUMBER

DATA_RD_WE_FRAME_TRO_WE_LCLK_PTXI_WE_RD_WAITXP_WE_WR_WAIT_P



C271

C273

CONFIGURATION MAPPING



From preface integrated feminization

