

# SIRASANAGANDLA BHAVYESH

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## OBJECTIVE

Ambitious Electronics and Communication Engineering student with strong analytical and technical skills, seeking an entry-level role in software development. Eager to apply hands-on experience in full-stack web development, algorithms, and database management to contribute to innovative projects within a dynamic and collaborative environment.

## EDUCATION

**B. Tech in ECE:** Gokaraju Rangaraju Institute of Engineering and Technology 2021 – present  
**CGPA:** 9.4/10 (Up to 7<sup>th</sup> Semester)

**Intermediate- MPC:** Narayana Junior College, Hyderabad 2019 – 2021  
**Percentage:** 94.6%

**Matriculation (10<sup>th</sup>):** Newton High School, Hyderabad 2018 – 2019  
**CGPA:** 10/10

## SKILLS

- **Technical Skills:** Data Structures & Algorithms, Object-Oriented Programming, Database Management Systems
- **Frontend Development:** HTML5, CSS3, JavaScript (ES6), React.js, Next.js
- **Backend Development:** Node.js, Express.js, RESTful APIs
- **Databases:** PostgreSQL
- **Web3 Technologies:** Internet Computer (Basic)
- **Programming Languages:** C++, Python, MATLAB, Verilog (Basic)
- **Tools:** VS Code, Git/GitHub, Docker, Postman, WSL, Canva, Tableau (Basic)

## PROJECTS

### Custom Blog:

- Developed a dynamic blog platform with full CRUD functionality using Express.js, EJS, and PostgreSQL, delivering seamless user experience. Built a custom API with Express.js, achieving a 30% reduction in page load time and enhancing user retention. The front end was crafted with React.js, utilizing Axios for efficient data fetching and React Router for smooth, client-side navigation. [\[GitHub link\]](#)

### To-do-list:

- Designed and implemented a to-do list application with full CRUD functionality, focusing on optimizing database queries to improve performance and enhance the user experience. [\[GitHub link\]](#)

### Complete KeepApp Project:

- Developed an advanced decentralized application (DApp) with a React.js frontend and Motoko backend, enabling note storage and retrieval with 99.9% uptime. [\[GitHub link\]](#)

### Wireless Charging:

- Led the development of a wireless charging system that achieved a 75% charging efficiency at 5 cm by applying magnetism and power electronics principles as a Mini project in 3-2 semester.

### Spatial Image processing using Verilog:

- Developed an image processing system using Verilog as a major group project. Designed a custom

architecture on Zynq FPGA for grayscale image processing with 4 parallel pixel lines for high-speed convolution. Implemented an optimized state machine for colored image processing, enabling 2-pixel parallel operations for various transformations.

## CERTIFICATIONS

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- Completed 'The Complete 2024 Web Development Bootcamp' on Udemy, gaining practical experience in full-stack development with technologies such as HTML, CSS, JavaScript, Node.js, and React. [Click here](#)
- Completed 'Joy of Computing using Python' by NPTEL, learning core programming concepts and Python-based problem-solving techniques. [Click here](#)
- Certified in 'Hardware Modelling using Verilog' by NPTEL, with a focus on hardware description languages for digital design. [Click here](#)
- Completed 'MATLAB Onramp' by MathWorks, covering fundamental MATLAB functionalities for numerical computation and data analysis. [Click here](#)

## EXTRA CURRICULAR ACITIVIES & ACHIEVEMENTS

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### **Executive Board Member** - Street Cause GRIET (Student-run NGO)

- Managed and executed fundraising events, raising over ₹50,000 for local causes; coordinated alumni relations and led the graphics team in producing 30+ marketing materials.

### **Graphics Lead & Designer** - Street Cause Originate Hyderabad

- Designed 50+ posters and visuals for fundraising events, significantly enhancing event visibility and earning recognition as a "Dedicated Designer" for RFC-10.

## DECLARATION

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I hereby declare that the information provided is true and accurate to the best of my knowledge and belief.

Sirasanagandla Bhavyesh