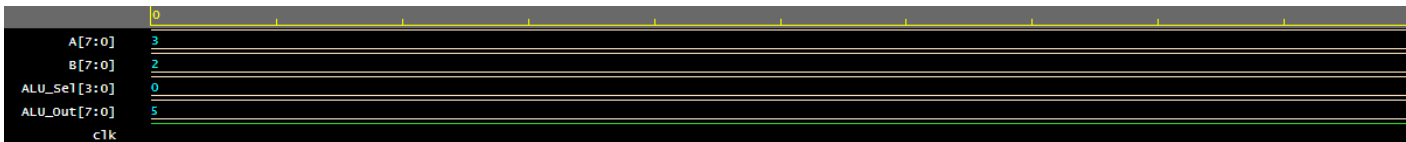


Work Package 5 - Practical
CLXBHE005, KMPKWE002
EEE3096S 2021

1. Screen captures of test bench output and waveform:

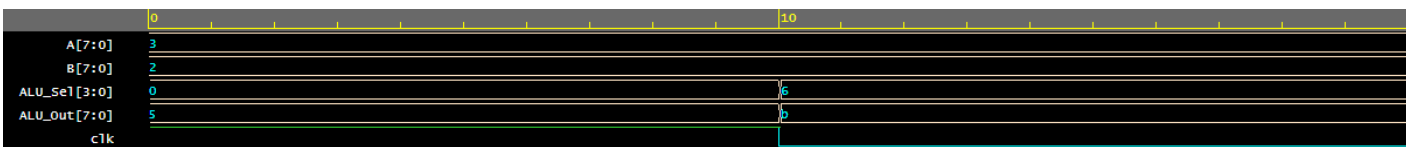
1.1 ADD:

A	B	ALU_Sel	ALU_Out
00000101	00000010	0000	00000111



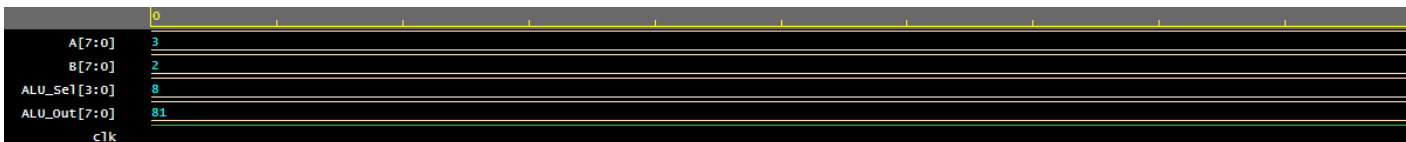
1.2 MAC, with an ACC initial value of 0b00000101:

A	B	ALU_Sel	ALU_Out
00000011	00000010	0000	00000101
00000011	00000010	0110	00001011



1.3 ROR:

A	B	ALU_Sel	ALU_Out
00000011	00000010	1000	10000001



1.4 LTH:

A	B	ALU_Sel	ALU_Out
00000011	00000010	1111	00000000

