```
 \verb|Printing C:\Users\bhitt\Desktop\Computer\_Architecture\Simple\_Computer\_Simulation\_3\\ | src\simple\_computer\_simulation\Memory\Control.jarchitecture\Simple\_computer\_simulation\Memory\Control.jarchitecture\Simple\_computer\_simulation\Memory\Control.jarchitecture\Simple\_computer\_simulation\Memory\Control.jarchitecture\Simple\_computer\_simulation\Memory\Control.jarchitecture\Simple\_computer\_simulation\Memory\Control.jarchitecture\Simple\Memory\Control.jarchitecture\Simple\Memory\Simple\Memory\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simple\Simp
    1 /*
    2
                              Memory Control: controls the memory
    3
    4 package simple computer simulation;
    6
    7 /**
    8
            * @author bhitt
10
            * /
11 public class MemoryControl {
12
                               //Properties
13
                              private Memory memory;
14
15
                               //Default constructor
16
                               MemoryControl(){
17
                                                  memory = new Memory();
18
                               }
19
20
                               //Mutators
2.1
                               void setMemory(Integer address, Integer value) {
22
                                                 memory.set(address, value);
2.3
                                }
24
25
                               //Accessors
26
                               Integer getMemory(Integer address){
27
                                                  return memory.get(address);
28
                                }
29
30
                               //Memory dump call
                              void memoryDump() {
31
32
                                                 memory.memoryDump();
33
                                }
34 }
```

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 $\label{thm:c:decomputer_architecture} \textbf{Printing C:} \\ \textbf{Users\_bhitt\_Desktop\_Computer\_Architecture\_Simple\_Computer\_Simulation\_3\$src\_simple\_computer\_simulation\_MemoryControl.jarchitecture\_Simple\_Computer\_Simulation\_3\$src\_simple\_computer\_simulation\_MemoryControl.jarchitecture\_Simple\_Computer\_Simulation\_3\$src\_simple\_computer\_simulation\_Simple\_Computer\_Simple\_Comp$ 

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