```
 \verb|Printing C:\Users\| bhitt\| Desktop\| Computer\_Architecture\| Simple\_Computer\_Simulation\_3 \\| src\| simple\_computer\_simulation\| MemoryDataRegistors \\| simple\_computer\_simulation \\|
      1 /*
      2
                                   Memory Data Register: register of the computer's
      3
                                   control unit that contains the data to be stored
      4
                                   in the computer storage, OR the data after a
 fetch
                                from computer storage.
      5
     7 package simple computer simulation;
      8
      9
 10 /**
 11
 12
              * @author bhitt
 13
               * /
14 public class MemoryDataRegister {
 15
                                 //Properties
                                  private Integer val;
 16
 17
 18
                                  //Default Constructor
 19
                                   MemoryDataRegister() {
 2.0
                                                       val=0;
 21
 2.2
 23
                                   //Mutator
 24
                                   void set(Integer value) {
 25
                                                      val = value;
 26
                                    }
 27
 28
                                   //Accessor
 29
                                    Integer get() {
 30
                                                       return val;
 31
                                    }
 32
 33
```

34 }

35