

Bhoumik	Shah (3)	5	eximen +	-3	MIT-WPU ॥ विक्यान्तिपूर्व पुरा ॥	UNIVERSITY PUNE TECHNOLOGY, RESEARCH, SOCIAL, MICHAELON & PASTNERSHIPS
					u half subt	ractor
	Logic diagram & truth table of halfadler schalf subtrador					
	Half add cr					
	8			T	85 51	60 1611
same o	at this	toal has	VI 316 N	May have	>-) g Com
76	-	And this	11812		6 5 50	019 1
	8			-11		
					robott	SLIZ HOH
ovat	to the	tist trus	is sitte	office dans	100	Carry
Buglus	100 31	לסדר מנו מס	\$ 53.850	o diffe	1000	Lindary
	A		В	Sum		Carr y
10	0	to se half	0	1 100	AUA DI S	
	1		0	1	10 61	0
	0		1	19	or gale z	O
	1		1	0	sof stop	MA
	Half	subtractor			difractor	2 9161
	4		2	1	() top 18	Lx3 (3) 11 11
				1 30	sol for	- Difference
	3			1/200	1000	18M (1)
						Borrow
	A	B	Di	ference	Borr	τοω
	0	0	-			ho Hotel
	0	1		1	1	
	,	0		1	-	
	,	1		0	^	
	1			0	0	

Asswer the following practical related questions a) Define half adder & half subtractor Half adder It's a combinational arithmetic circuit that add two numbers & produce a sum bital scarry bits both as output Half subfractor Its a combinational arithmetic circuit that subtracts two numbers & produce a difference & borrow both as output b) List the IC number used in half adder & halt subtractor -> Half adder i) ex-or gate \$186 ii) AND gate, 7408 Walf subfrador i) ex-6r gate, 7486 ii) AND gate, 7408 iii) Not gote, 7404 Conclusion: From this expt we were able to verify truth table of halfadder & half subtract or